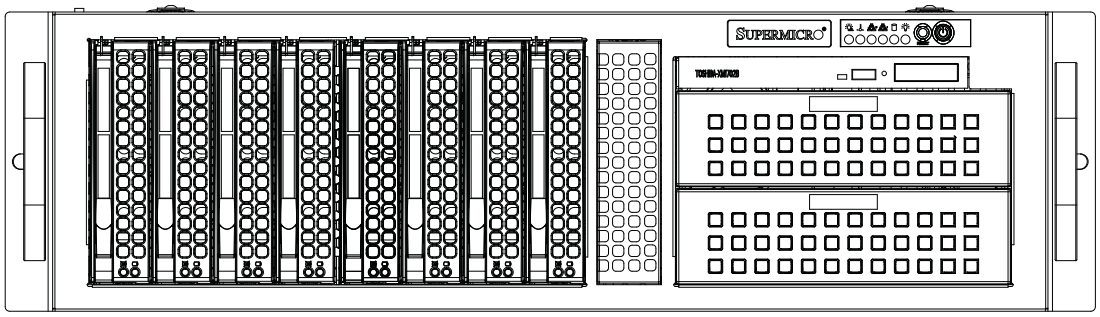
SUPERMICRO®

SUPERSERVER

6037R-72RFT



USER'S MANUAL

1.0

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Preface

About This Manual

This manual is written for professional system integrators and PC technicians. It provides information for the installation and use of the SuperServer 6037R-72RFT. Installation and maintainance should be performed by experienced technicians only.

The SuperServer 6037R-72RFT is a high-end server based on the SC835TQ-R920B 3U rackmount chassis and the X9DRH-7TF dual processor serverboard.

Manual Organization

Chapter 1: Introduction

The first chapter provides a checklist of the main components included with the server system and describes the main features of the X9DRH-7TF serverboard and the SC835TQ-R920B chassis.

Chapter 2: Server Installation

This chapter describes the steps necessary to install the SuperServer 6037R-72RFT into a rack and check out the server configuration prior to powering up the system. If your server was ordered without processor and memory components, this chapter will refer you to the appropriate sections of the manual for their installation.

Chapter 3: System Interface

Refer here for details on the system interface, which includes the functions and information provided by the control panel on the chassis as well as other LEDs located throughout the system.

Chapter 4: System Safety

You should thoroughly familiarize yourself with this chapter for a general overview of safety precautions that should be followed when installing and servicing the SuperServer 6037R-72RFT.

Chapter 5: Advanced Serverboard Setup

Chapter 5 provides detailed information on the X9DRH-7TF serverboard, including the locations and functions of connections, headers and jumpers. Refer to this chapter when adding or removing processors or main memory and when reconfiguring the serverboard.

Chapter 6: Advanced Chassis Setup

Refer to Chapter 6 for detailed information on the SC835TQ-R920B server chassis. You should follow the procedures given in this chapter when installing, removing or reconfiguring SAS/SATA or peripheral drives and when replacing system power supply units and cooling fans.

Chapter 7: BIOS

The BIOS chapter includes an introduction to BIOS and provides detailed information on running the CMOS Setup Utility.

Appendix A: BIOS Error Beep Codes

Appendix B: System Specifications

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Chapter 1

Introduction

1-1 Overview

The SuperServer 6037R-72RFT is a high-end server comprised of two main subsystems: the SC835TQ-R920B 3U server chassis and the X9DRH-7TF dual processor serverboard. Please refer to our web site for information on operating systems that have been certified for use with the system (www.supermicro.com).

In addition to the serverboard and chassis, various hardware components have been included with the 6037R-72RFT, as listed below:

- Two passive CPU heatsinks (SNK-P0048PS)
- One slim DVD-ROM drive (DVM-PNSC-DVD-SBT1)
- Three 8-cm system fans (FAN-0126L4)
- Two 8-cm rear exhaust fans (FAN-0125L4)
- One air shroud (MCP-310-39001-0N)
- SAS/SATA Accessories
  One SAS/SATA backplane (BPN-SAS-833TQ)
  Two 27-cm. SATA cables (CBL-0118L-03)
  Eight drive carriers (MCP-220-00075-0B)
- One rackmount kit (MCP-290-00053-0N)
- One CD containing drivers and utilities
- SuperServer 6037R-72RFT User's Manual

1-2 Serverboard Features

The SuperServer 6037R-72RFT is built around the X9DRH-7TF, a dual processor serverboard based on the Intel C600 chipset and designed to provide maximum performance. Below are the main features of the X9DRH-7TF. (See Figure 1-1 for a block diagram of the chipset).

Processors

The X9DRH-7TF supports single or dual Intel® E5-2600 Series (Socket R) processors in LGA 2011 sockets. Please refer to our web site for a complete listing of supported processors (www.supermicro.com).

Memory

The X9DRH-7TF has sixteen DIMM slots that can support up to 512 GB of ECC registered/unbuffered DDR3-1600/1066/800 memory. Please refer to Chapter 5 for details on installing memory.

SAS

An LSI 2208 hardware RAID controller provides support for eight SAS 2.0 ports, which are RAID 0, 1, 5, 6, 10, 50 and 60 capable.

SATA

A SATA controller is integrated into the C600 chipset to provide a six-port SATA subsystem, which is RAID 0, 1, 10 and 5 capable. Two ports support SATA 3.0 (I-SATA0/1) and four support SATA 2.0 (I-SATA2-5).

PCI Expansion Slots

The X9DRH-7TF has six PCI-E 3.0 x8 (in x16 slots), two PCI-E 3.0 x8 and one PCI-E 3.0 x16 slots. Note that the PCI slots are controlled by the CPU so some slots may not be available when two CPUs are not installed on the board at the same time. See the serverboard layout in Chapter 5 for details.

I/O Ports

The color-coded I/O ports include one COM port, a VGA (monitor) port, four USB, two 10 Gb Ethernet LAN ports and a dedicated IPMI LAN port.

Graphics Controller

The X9DRH-7TF features an integrated Matrox G200eW video controller. The G200eW is a 2D/3D/video accelerator chip with a 128-bit core.

1-3 Server Chassis Features

The following is a general outline of the main features of the SC835TQ-R920B server chassis.

System Power

The SC835TQ-R920B features a redundant 920W power supply composed of two separate power modules. This power redundancy feature allows you to replace a failed power supply without shutting down the system.

SAS/SATA Subsystem

The SC835TQ-R920B supports up to eight SAS or SATA drives. These drives are hot-swappable units and are connected to a backplane that provides power and control.

Front Control Panel

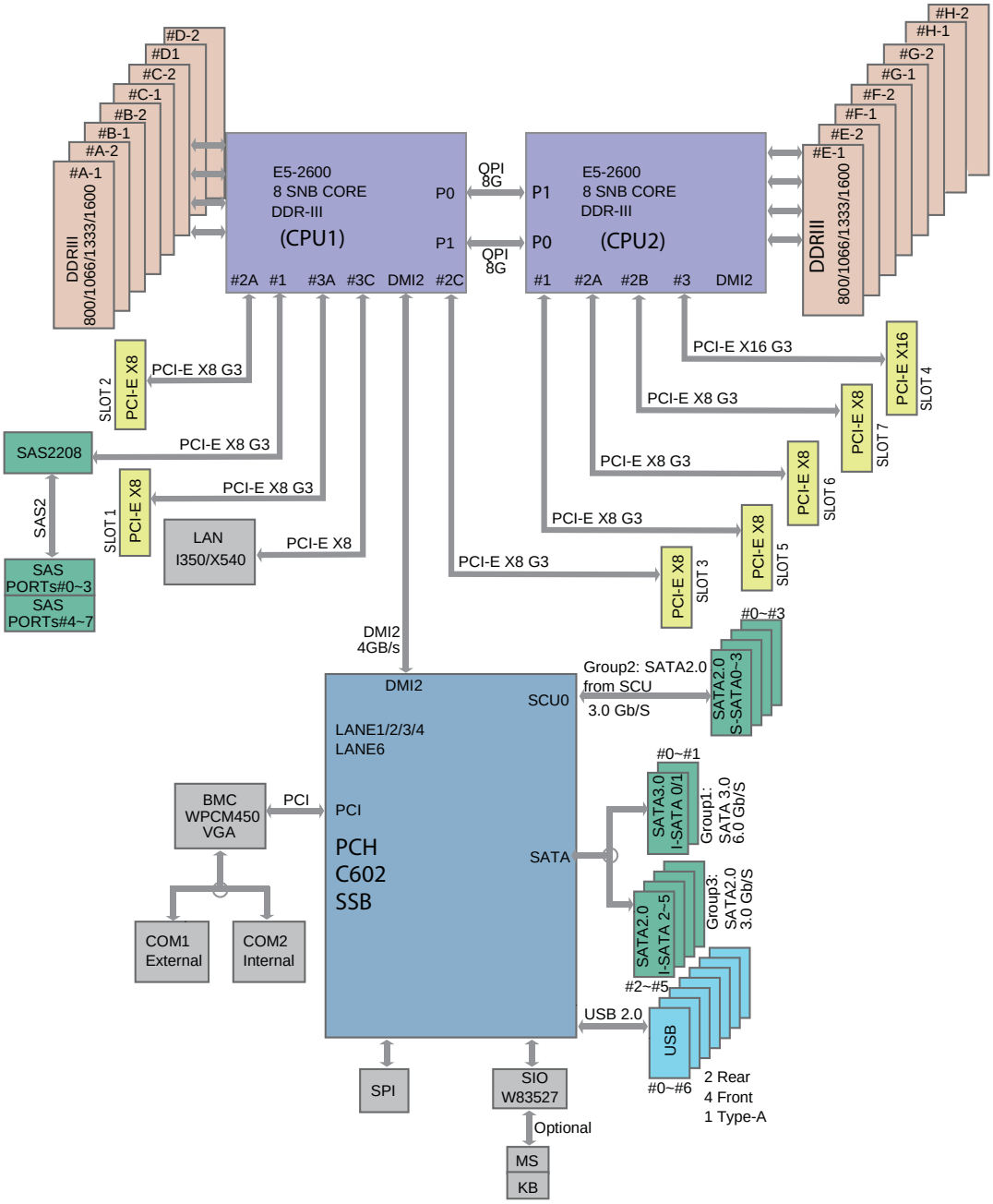
The control panel on the SuperServer 6037R-72RFT provides you with system monitoring and control. LEDs indicate system power, HDD activity, network activity, system overheat and power supply failure. The main power button and a system reset button are also located here.

Cooling System

The SC835TQ-R920B chassis has an innovative cooling design that includes three 8-cm hot-plug system cooling fans located in the middle section of the chassis and two 8-cm exhaust fans. An air shroud channels the airflow from the system fans to efficiently cool the processor area of the system. The power supply module also includes a cooling fan.

Figure 1-1. Chipset Block Diagram

Note: This is a general block diagram. Please see Chapter 5 for details.



1-4 Contacting Supermicro

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Notes

Chapter 2

Server Installation

2-1 Overview

This chapter provides a quick setup checklist to get your SuperServer 6037R-72RFT up and running. Following these steps in the order given should enable you to have the system operational within a minimum amount of time. This quick setup assumes that your system has come to you with the processors and memory preinstalled. If your system is not already fully integrated with a serverboard, processors, system memory etc., please turn to the relevant section in Chapter 5 for details on installing specific components.

2-2 Unpacking the System

You should inspect the box the SuperServer 6037R-72RFT was shipped in and note if it was damaged in any way. If the server itself shows damage you should file a damage claim with the carrier who delivered it.

Decide on a suitable location for the rack unit that will hold the SuperServer 6037R-72RFT. It should be situated in a clean, dust-free area that is well ventilated. Avoid areas where heat, electrical noise and electromagnetic fields are generated. You will also need it placed near a grounded power outlet. Read the Rack and Server Precautions in the next section.

2-3 Preparing for Setup

The box the SuperServer 6037R-72RFT was shipped in should include two sets of rail assemblies, two rail mounting brackets and the mounting screws you will need to install the system into the rack. Follow the steps in the order given to complete the installation process in a minimum amount of time. Please read this section in its entirety before you begin the installation procedure outlined in the sections that follow.

Choosing a Setup Location

- Leave enough clearance in front of the rack to enable you to open the front door completely (~25 inches) and approximately 30 inches of clearance in the back of the rack to allow for sufficient airflow and ease in servicing.

- This product is for installation only in a Restricted Access Location (dedicated equipment rooms, service closets and the like).
- This product is not suitable for use with visual display work place devices acccording to §2 of the the German Ordinance for Work with Visual Display Units.

Warnings and Precautions!

Rack Precautions

- Ensure that the leveling jacks on the bottom of the rack are fully extended to the floor with the full weight of the rack resting on them.
- In single rack installation, stabilizers should be attached to the rack. In multiple rack installations, the racks should be coupled together.
- Always make sure the rack is stable before extending a component from the rack.
- You should extend only one component at a time - extending two or more simultaneously may cause the rack to become unstable.

Server Precautions

- Review the electrical and general safety precautions in Chapter 4.
- Determine the placement of each component in the rack *before* you install the rails.
- Install the heaviest server components on the bottom of the rack first, and then work up.
- Use a regulating uninterruptible power supply (UPS) to protect the server from power surges, voltage spikes and to keep your system operating in case of a power failure.
- Allow any hot plug drives and power supply modules to cool before touching them.
- Always keep the rack's front door and all panels and components on the servers closed when not servicing to maintain proper cooling.

Rack Mounting Considerations

Ambient Operating Temperature

If installed in a closed or multi-unit rack assembly, the ambient operating temperature of the rack environment may be greater than the ambient temperature of the room. Therefore, consideration should be given to installing the equipment in an environment compatible with the manufacturer's maximum rated ambient temperature (Tmra).

Reduced Airflow

Equipment should be mounted into a rack so that the amount of airflow required for safe operation is not compromised.

Mechanical Loading

Equipment should be mounted into a rack so that a hazardous condition does not arise due to uneven mechanical loading.

Circuit Overloading

Consideration should be given to the connection of the equipment to the power supply circuitry and the effect that any possible overloading of circuits might have on overcurrent protection and power supply wiring. Appropriate consideration of equipment nameplate ratings should be used when addressing this concern.

Reliable Ground

A reliable ground must be maintained at all times. To ensure this, the rack itself should be grounded. Particular attention should be given to power supply connections other than the direct connections to the branch circuit (i.e. the use of power strips, etc.).

2-4 Installing the System into a Rack

This section provides information on installing the SC835 chassis into a rack unit with the quick-release rails provided. There are a variety of rack units on the market, which may mean the assembly procedure will differ slightly. You should also refer to the installation instructions that came with the rack unit you are using.

Installing the Inner Rack Rails

Installing the Inner Rails

1. Place the inner rack extensions on the side of the chassis aligning the hooks of the chassis with the rail extension holes.

2. Slide the extension toward the front of the chassis.

3. Secure the chassis with four screws as illustrated.

4. Repeat steps 1-3 for the other inner rail.

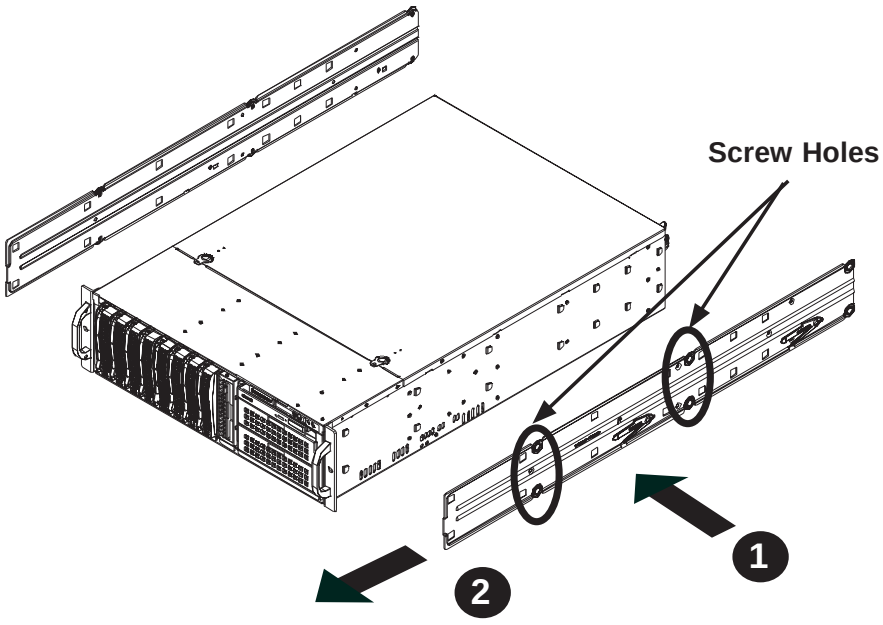


Figure 2-1. Installing the Inner Rack Rails

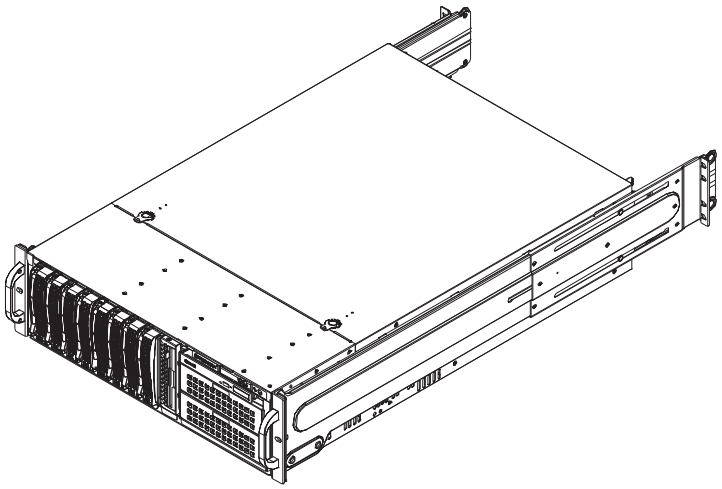


Figure 2-2. Inner Rack Rails Installed

Installing the Outer Rack Rails

Outer rails attach to the server rack and hold the server in place. The outer rails for the SC835 chassis extend between 30 inches and 33 inches.

Installing the Outer Rails

1. Begin by measuring the distance from the front rail to the rear rail of the rack
2. Attach a short bracket to the front side of the right outer rail and a long bracket to the rear side of the right outer rail.
3. Adjust both the short and long brackets to the proper distance so that the rail can fit snugly into the rack.
4. Secure the short bracket to the front side of the outer rail with two screws and the long bracket to the rear side of the outer rail with three screws.
5. Repeat these steps for the left outer rail.

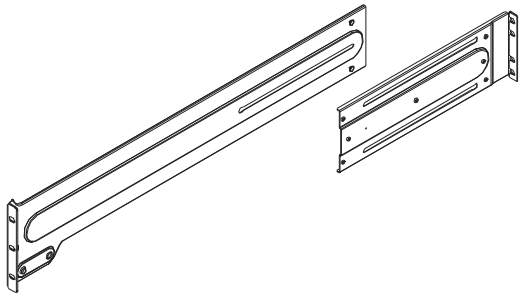


Figure 2-3. Outer Rack Rails

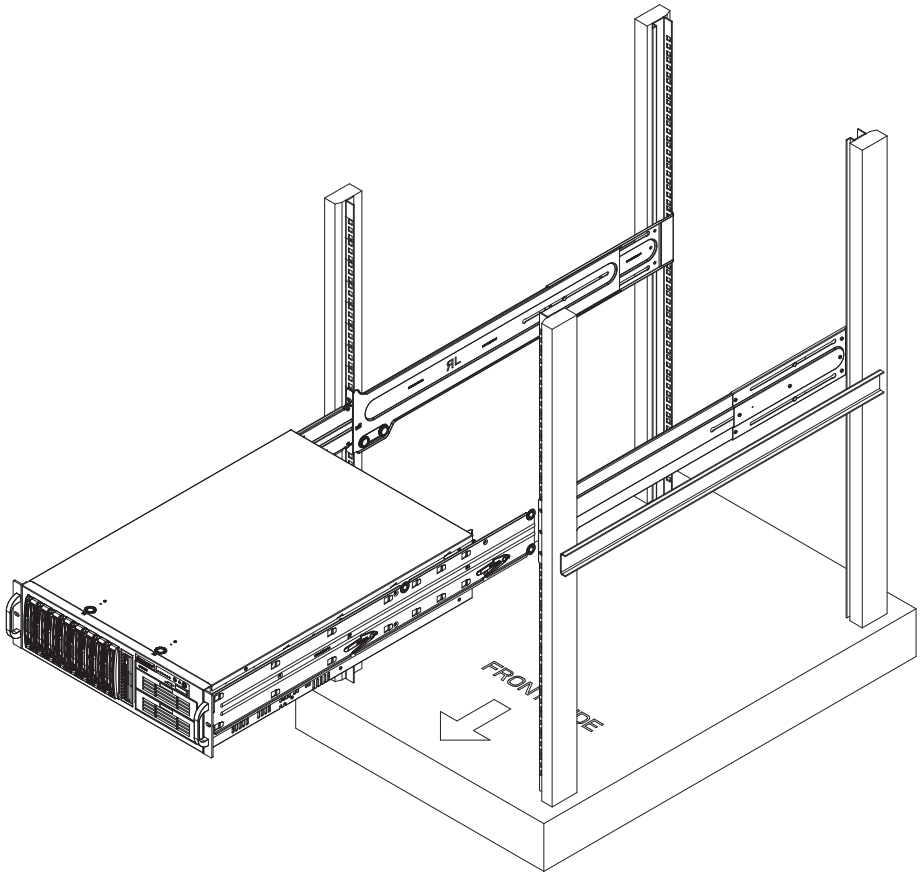


Figure 2-4. Installing the Chassis into the Rack

Installing the Chassis into a Rack

Installing into a Rack

1. Confirm that the inner and outer rails are installed on the rack.

2. Line chassis rails with the front of the rack rails.

3. Slide the chassis rails into the rack rails, keeping the pressure even on both sides (you may have to depress the locking tabs when inserting). When the server has been pushed completely into the rack, you should hear the locking tabs "click" into position.

4. (Optional) Insert and tighten the thumbscrews that hold the front of the server to the rack.

Chapter 3

System Interface

3-1 Overview

There are several LEDs on the control panel as well as others on the drive carriers to keep you constantly informed of the overall status of the system as well as the activity and health of specific components. There are also two buttons on the chassis control panel.

3-2 Control Panel Buttons

The two buttons located on the front of the chassis include a reset button and a power on/off button.

Reset

Use the reset button to reboot the system.

Power

This is the main power button, which is used to apply or turn off the main system power. Turning off system power with this button removes the main power but keeps standby power supplied to the system.

3-3 Control Panel LEDs

The control panel located on the front of the chassis has several LEDs. These LEDs provide you with critical information related to different parts of the system. This section explains what each LED indicates when illuminated and any corrective action you may need to take.

Power Fail

Indicates a power supply module has failed. The second power supply module will take the load and keep the system running but the failed module will need to be replaced. Refer to Chapter 6 for details on replacing the power supply. This LED should be off when the system is operating normally.

Overheat/Fan Fail:

When this LED flashes, it indicates a fan failure. When on continuously it indicates an overheat condition, which may be caused by cables obstructing the airflow in the system or the ambient room temperature being too warm. Check the routing of the cables and make sure all fans are present and operating normally. You should also check to make sure that the chassis covers are installed. Finally, verify that the heatsinks are installed properly (see Chapter 5). This LED will remain flashing or on as long as the indicated condition exists.

NIC1

Indicates network activity on the LAN1 port when flashing.

NIC2

Indicates network activity on the LAN2 port when flashing.

HDD

On the SuperServer 6037R-72RFT, this LED indicates SATA hard drive and/or DVD-ROM drive activity when flashing.

Power

Indicates power is being supplied to the system's power supply units. This LED should normally be illuminated when the system is operating.

3-4 Drive Carrier LEDs

Each drive carrier has two LEDs:

SAS Drives

- **Green:** When illuminated, the green LED on the drive carrier indicates the drive is powered on. If this LED is not lit, it means no power is being provided for the drive. Please refer to Chapter 6 for instructions on replacing failed drives.

- **Red:** A solid red LED indicates a drive failure. If one of the drives fails, you should be notified by your system management software. Please refer to Chapter 6 for instructions on replacing failed drives. If this LED flashes ~ once per second (1 Hz) it indicates RAID rebuilding activity.

SATA Drives

- **Green:** When illuminated, the green LED on the drive carrier indicates drive activity. A connection to the backplane enables this LED to blink on and off when that particular drive is being accessed.

- **Red:** The red LED to indicate a drive failure. If one of the drives fails, you should be notified by your system management software. Please refer to Chapter 6 for instructions on replacing failed drives.

Notes

Chapter 4

System Safety

4-1 Electrical Safety Precautions

Basic electrical safety precautions should be followed to protect yourself from harm and the SuperServer 6037R-72RFT from damage:

- Be aware of the locations of the power on/off switch on the chassis as well as the room's emergency power-off switch, disconnection switch or electrical outlet. If an electrical accident occurs, you can then quickly remove power from the system.

- Do not work alone when working with high voltage components.

- Power should always be disconnected from the system when removing or installing main system components, such as the serverboard and memory modules. When disconnecting power, you should first power down the system with the operating system. The unit has more than one power supply cord. Disconnect both power supply cords before servicing to avoid electrical shock.

- When working around exposed electrical circuits, another person who is familiar with the power-off controls should be nearby to switch off the power if necessary.

- Use only one hand when working with powered-on electrical equipment. This is to avoid making a complete circuit, which will cause electrical shock. Use extreme caution when using metal tools, which can easily damage any electrical components or circuit boards they come into contact with.

- Do not use mats designed to decrease static electrical discharge as protection from electrical shock. Instead, use rubber mats that have been specifically designed as electrical insulators.

- The power supply power cords must include a grounding plug and must be plugged into grounded electrical outlets.

- This product may be connected to an IT power system. In all cases, make sure that the unit is also reliably connected to Earth (ground).

- Serverboard Battery: **CAUTION** - There is a danger of explosion if the onboard battery is installed upside down, which will reverse its polarites (see Figure 4-1). This battery must be replaced only with the same or an equivalent type recommended by the manufacturer (CR2032). Dispose of used batteries according to the manufacturer's instructions.

- DVD-ROM Laser: **CAUTION** - this server may have come equipped with a DVD-ROM drive. To prevent direct exposure to the laser beam and hazardous radiation exposure, do not open the enclosure or use the unit in any unconventional way.

- Mainboard replaceable soldered-in fuses: Self-resetting PTC (Positive Temperature Coefficient) fuses on the mainboard must be replaced by trained service technicians only. The new fuse must be the same or equivalent as the one replaced. Contact technical support for details and support.

4-2 General Safety Precautions

Follow these rules to ensure general safety:

- Keep the area around the 6037R-72RFT clean and free of clutter.

- The 6037R-72RFT weighs approximately 75 lbs (34 kg.) when fully loaded. When lifting the system, two people at either end should lift slowly with their feet spread out to distribute the weight. Always keep your back straight and lift with your legs.

- Place the chassis top cover and any system components that have been removed away from the system or on a table so that they won't accidentally be stepped on.

- While working on the system, do not wear loose clothing such as neckties and unbuttoned shirt sleeves, which can come into contact with electrical circuits or be pulled into a cooling fan.

- Remove any jewelry or metal objects from your body, which are excellent metal conductors that can create short circuits and harm you if they come into contact with printed circuit boards or areas where power is present.

- After accessing the inside of the system, close the system back up and secure it to the rack unit with the retention screws after ensuring that all connections have been made.

4-3 ESD Precautions

Electrostatic Discharge (ESD) is generated by two objects with different electrical charges coming into contact with each other. An electrical discharge is created to neutralize this difference, which can damage electronic components and printed circuit boards. The following measures are generally sufficient to neutralize this difference before contact is made to protect your equipment from ESD:

- Use a grounded wrist strap designed to prevent static discharge.

- Keep all components and printed circuit boards (PCBs) in their antistatic bags until ready for use.

- Touch a grounded metal object before removing the board from the antistatic bag.

- Do not let components or PCBs come into contact with your clothing, which may retain a charge even if you are wearing a wrist strap.

- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or contacts.

- When handling chips or modules, avoid touching their pins.

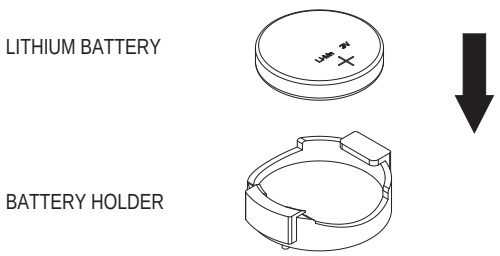
- Put the serverboard and peripherals back into their antistatic bags when not in use.

- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the serverboard.

4-4 Operating Precautions

Care must be taken to assure that the chassis cover is in place when the 6037R-72RFT is operating to assure proper cooling. Out of warranty damage to the system can occur if this practice is not strictly followed.

Figure 4-1. Installing the Onboard Battery



Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

Chapter 5

Advanced Serverboard Setup

This chapter covers the steps required to install the X9DRH-7TF serverboard into the chassis, connect the data and power cables and install add-on cards. All serverboard jumpers and connections are also described. A layout and quick reference chart are included in this chapter for your reference. Remember to completely close the chassis when you have finished working with the serverboard to better cool and protect the system.

5-1 Handling the Serverboard

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to any printed circuit boards (PCBs), it is important to handle them very carefully (see previous chapter). To prevent the serverboard from bending, keep one hand under the center of the board to support it when handling. The following measures are generally sufficient to protect your equipment from electric static discharge.

Precautions

- Use a grounded wrist strap designed to prevent Electrostatic Discharge (ESD).
- Touch a grounded metal object before removing any board from its antistatic bag.
- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the serverboard, add-on cards and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the serverboard.

Unpacking

The serverboard is shipped in antistatic packaging to avoid electrical static discharge. When unpacking the board, make sure the person handling it is static protected.

5-2 Connecting Cables

The cables listed below should already be connected to the serverboard. These include the data cables for the peripherals and control panel and the power cables.

Connecting Data Cables

The cables used to transfer data from the peripheral devices have been carefully routed to prevent them from blocking the flow of cooling air that moves through the system from front to back. If you need to disconnect any of these cables, you should take care to keep them routed as they were originally after reconnecting them (make sure the red wires connect to the pin 1 locations). The following data cables (with their locations noted) should be connected. (See the layout for connector locations.)

- SAS (SAS0 ~ SAS7) or SATA (I-SATA0 ~ 5) drive cables
- Control Panel cable (JF1)

Important! Make sure the the cables do not come into contact with the fans.

Connecting Power Cables

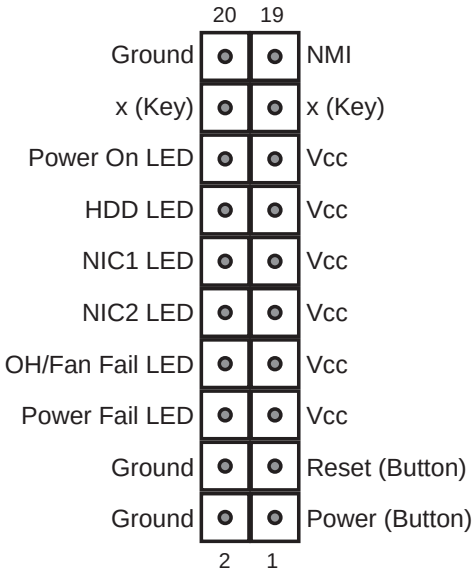
The X9DRH-7TF has a 24-pin primary power supply connector (J22) for connection to the ATX power supply. In addition, there are two 8-pin 12V processor power connectors (JPW1 and JPW2) that must be connected to your power supply. See Section 5-9 for power connector pin definitions.

Connecting the Control Panel

JF1 contains header pins for various front control panel connectors. See Figure 5-1 for the pin locations of the various front control panel buttons and LED indicators.

All JF1 wires have been bundled into a single cable to simplify this connection. Make sure the red wire plugs into pin 1 as marked on the board. The other end connects to the Control Panel PCB board, located just behind the system status LEDs on the chassis. See Chapter 5 for details and pin descriptions.

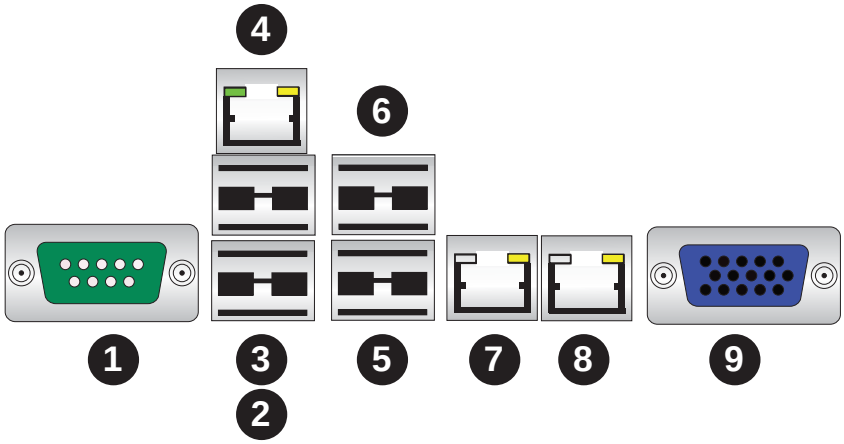
Figure 5-1. Control Panel Header Pins



5-3 I/O Ports

The I/O ports are color coded in conformance with the PC 99 specification. See Figure 5-2 below for the colors and locations of the various I/O ports.

Figure 5-2. I/O Ports



IO Ports			
1	COM1 Port	6	USB Port 3
2	USB Port 0	7	LAN Port 1
3	USB Port 1	8	LAN Port 2
4	IPMI LAN Port	9	VGA Port
5	USB Port 2		

5-4 Installing the Processor and Heatsink

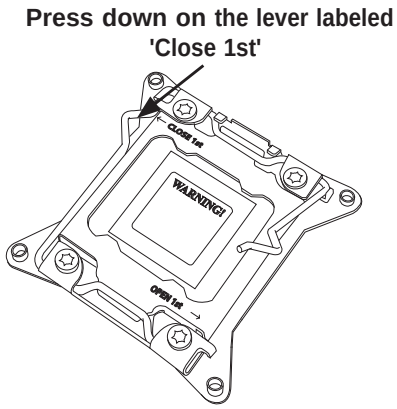
When handling the processor package, avoid placing direct pressure on the label area of the fan.

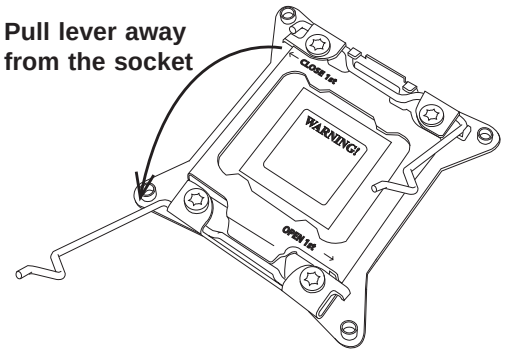
Notes:

- Always connect the power cord last and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU socket before you install the CPU heatsink.
- If you buy a CPU separately, make sure that you use an Intel-certified multi-directional heatsink only.
- Make sure to install the serverboard into the chassis before you install the CPU heatsinks.
- When receiving a serverboard without a processor pre-installed, make sure that the plastic CPU socket cap is in place and none of the socket pins are bent; otherwise, contact your retailer immediately.
- Refer to the Supermicro web site for updates on CPU support.

Installing an LGA2011 Processor

1. There are two levers on the LGA2011 socket. First press and release the load lever labeled 'Open 1st'.
2. Press the second load lever labeled 'Close 1st' to release the load plate from its locked position.



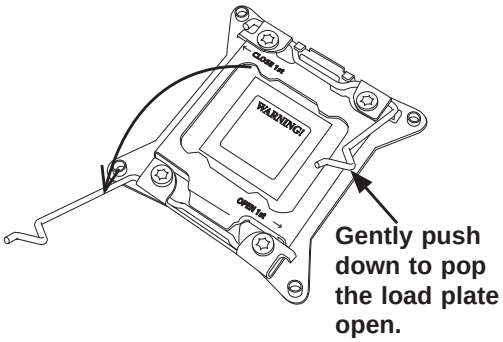


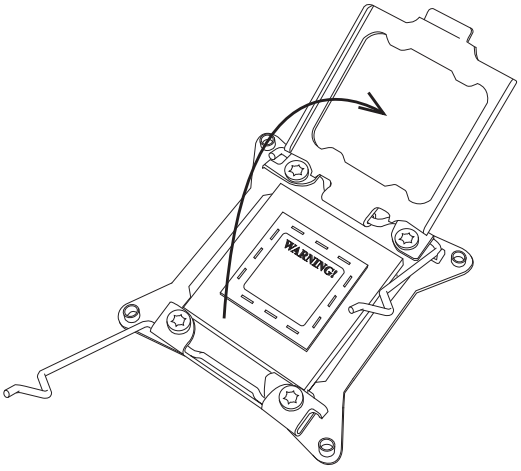
3. With the lever labeled 'Close 1st' fully retracted, gently push down on the 'Open 1st' lever to open the load plate. Lift the load plate to open it completely.

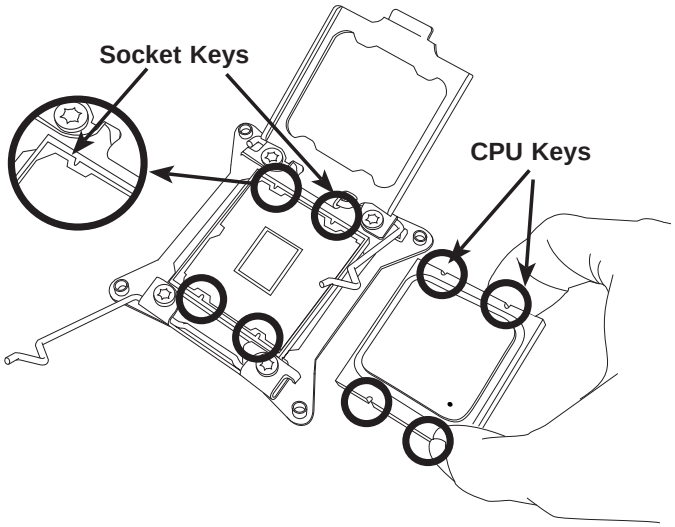
4. Using your thumb and the index finger, remove the 'WARNING' plastic cap from the socket.

5. Use your thumb and index finger to hold the CPU by its edges. Align the CPU keys, which are semi-circle cutouts, against the socket keys.

6. Once they are aligned, carefully lower the CPU straight down into the socket. (Do not drop the CPU on the socket. Do not move the CPU horizontally or vertically and do not rub the CPU against any pins of the socket, which may damage the CPU or the socket.)



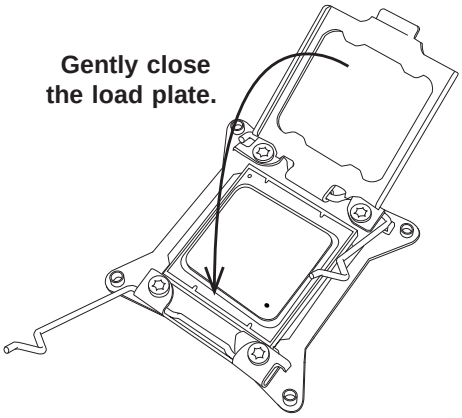


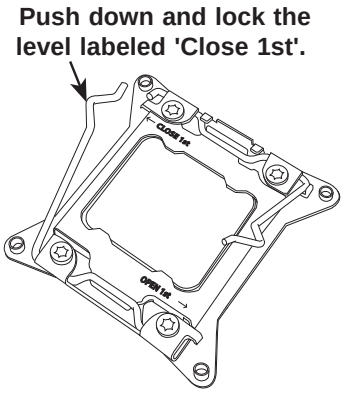


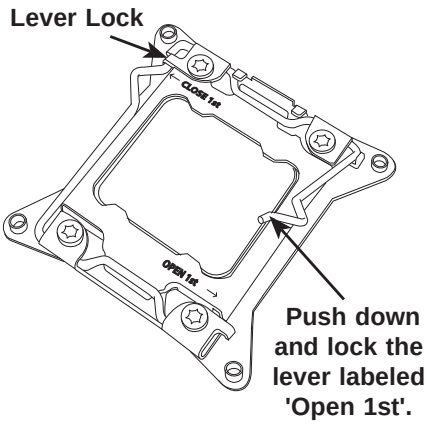
Warning: You can only install the CPU to the socket in one direction. Make sure that the CPU is properly inserted into the socket before closing the load plate. If it doesn't close properly, do not force it as it may damage your CPU. Instead, open the load plate again and double-check that the CPU is aligned properly.

7. With the CPU in the socket, inspect the four corners of the CPU to make sure that they are flush with the socket.

8. Close the load plate. Lock the lever labeled 'Close 1st', then lock the lever labeled 'Open 1st'. Use your thumb to gently push the load levers down until the lever locks.





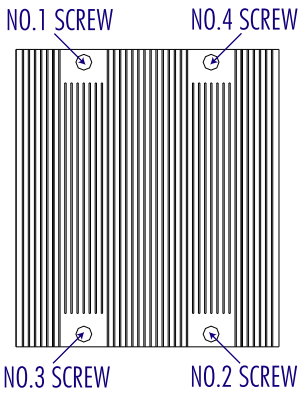


Installing a CPU Heatsink

1. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the retention mechanism.

2. Screw in two diagonal screws (i.e. the #1 and the #2 screws) until just snug (do not over-tighten the screws, which may damage the CPU.)

3. Finish the installation by fully tightening all four screws.



Removing the Heatsink

Warning: We do not recommend removing the CPU or the heatsink. If you do need to remove the heatsink, please follow the instructions below to prevent damage to the CPU or other components.

1. Unplug the power cord from the power supply.

1. Unscrew and remove the heatsink screws in the sequence shown in the picture below.

2. Hold the heatsink and gently wiggle it to loosen it from the CPU. (Do not use excessive force when doing this!)

3. Once the heatsink is loosened, remove it from the CPU.

4. Clean the surface of the CPU and the heatsink to get rid of the old thermal grease. Reapply the proper amount of thermal grease before you re-install the heatsink.

5-5 Installing Memory

CAUTION! Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

Memory Support

The X9DRH-7TF supports up to 512 GB of DDR3-1600/1333/1066/800 RDIMM, LRDIMM ECC or UDIMM ECC/non-ECC memory. Use memory modules of the same type and speed. See the following tables for memory installation. Please refer to the Supermicro web site for possible updates to supported memory.

DIMM Installation

Installing Memory Modules

1. Insert the desired number of DIMMs into the memory slots starting with DIMM #P1-DIMMA1. When populating two DIMM modules within a channel, always start with Bank1 first. For optimal memory performance, please install a pair (or pairs) of memory modules of the same type and speed with a maximum of 12 modules (see the Memory Installation Table below).

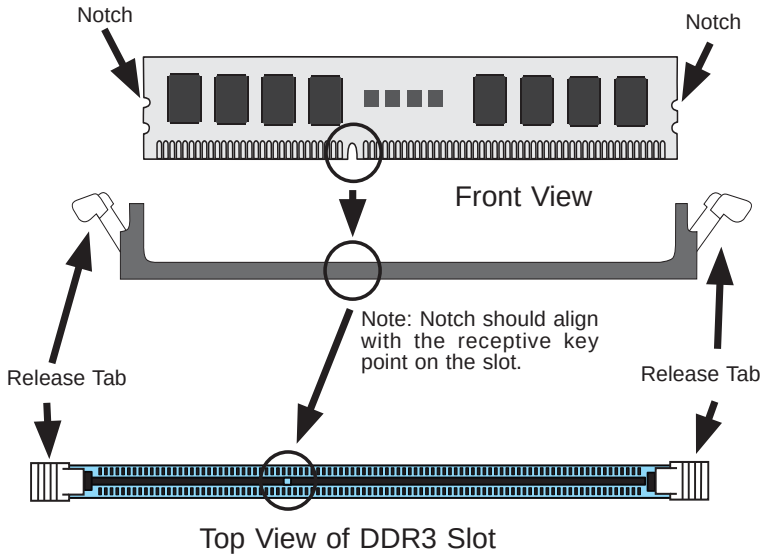
2. Insert each DIMM module vertically into its slot. Pay attention to the notch along the bottom of the module to avoid installing incorrectly (see Figure 5-3).

3. Gently press down on the DIMM module until it snaps into place in the slot. Repeat for all modules.

To Install: Insert module vertically and press down until it snaps into place. Pay attention to the alignment notch at the bottom.

To Remove:
Use your thumbs to gently push the release tabs near both ends of the module. This should release it from the slot.

Figure 5-3. DIMM Installation



For memory to work properly, populate according to the tables below.

Processors and their Corresponding Memory Modules								
CPU#	**Corresponding DIMM Modules**							
CPU 1	P1-DIMMA1	P1-DIMMB1	P1-DIMMC1	P1-DIMMD1	P1-DIMMA2	P1-DIMMB2	P1-DIMMC2	P1-DIMMD2
CPU2	P2-DIMME1	P2-DIMMF1	P2-DIMMG1	P2-DIMMH1	P2-DIMME2	P2-DIMM F2	P2-DIMMG2	P2-DIMMH2

Populating Memory for Optimal Performance	
Number of CPUs+DIMMs	**CPU and Memory Population Configuration Table**
1 CPU & 2 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1
1 CPU & 4 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1, P1-DIMMC1/P1-DIMMD1
1 CPU & 5~8 DIMMs	CPU1 P1-DIMMA1/P1-DIMMB1, P1-DIMMC1/P1-DIMMD1 + Any memory pairs in P1-DIMMA2/P1-DIMMB2/P1-DIMMC2/P1-DIMMD2 slots
2 CPUs & 4 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1, P2-DIMME1/P2-DIMMF1
2 CPUs & 6 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1
2 CPUs & 8 DIMMs	CPU1 + CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1
2 CPUs & 10~16 DIMMs	CPU1/CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1 + Any memory pairs in P1, P2 DIMM slots
2 CPUs & 16 DIMMs	CPU1/CPU2 P1-DIMMA1/P1-DIMMB1/P1-DIMMC1/P1-DIMMD1, P2-DIMME1/P2-DIMMF1/P2-DIMMG1/P2-DIMMH1,P1-DIMMA2/P1-DIMMB2/P1-DIMMC2/P1-DIMMD2, P2-DIMME2/P2-DIMMF2/P2-DIMMG2/P2-DIMMH2

UDIMM Memory Support			
Ranks Per DIMM & Data Width	**Memory Capacity Per DIMM (Note 1)**		
SRx8 Non-ECC	1GB	2GB	4GB
DRx8 Non-ECC	2GB	4GB	8GB
SRx16 Non-ECC	512MB	1GB	2GB
SRx8 ECC	1GB	2GB	4GB
DRx8 ECC	2GB	4GB	8GB
Note: 1Gb/2Gb/4Gb DRAMs are supported; however, only 2Gb and 4Gb DRAMs are validated.			

RDIMM Memory Support			
Ranks Per DIMM & Data Width	**Memory Capacity Per DIMM (Note 1)**		
SRx8	1GB	2GB	4GB
DRx8	2GB	4GB	8GB
SRx4	2GB	4GB	8GB
DRx4	4GB	8GB	16GB
QRx4	8GB	16GB	32GB
QRx8	4GB	8GB	16GB
Notes: 1. 1Gb/2Gb/4Gb DRAMs are supported; however, only 2Gb and 4Gb DRAMs are validated. 2. QR RDIMMs are supported but not validated. Memory testing are limited to system level testing. Signal integrity testing in interoperability testing are not performed. The passing QR RDIMMs will be posted on the website.			

LRDIMM Memory Support		
Ranks Per DIMM & Data Width (Note 1)	**Memory Capacity Per DIMM (Note 2)**	
QRx4 (DDP) (Note 4)	16GB	32GB
QRx8 (P) (Note 5)	8GB	16GB
Notes: 1. Physical Rank is used to calculate DIMM capacity. 2. Only 2Gb/4Gb DRAMs are supported and validated. 4. The speeds listed are estimated only and will be verified through simulation. 4. DDP is for Dual Die Package DRAM stacking. 5. "P" Means "Planer Monolithic DRAM Die."		

Notes

- For optimal memory performance, please install DIMMs in pairs (with an even number of DIMMs installed).
- All channels in a system will run at the fastest common frequency.

5-6 Adding PCI Add-On Cards

The 6037R-72RFT can accommodate seven full-sized PCI add-on cards.

Installing an Add-on Card

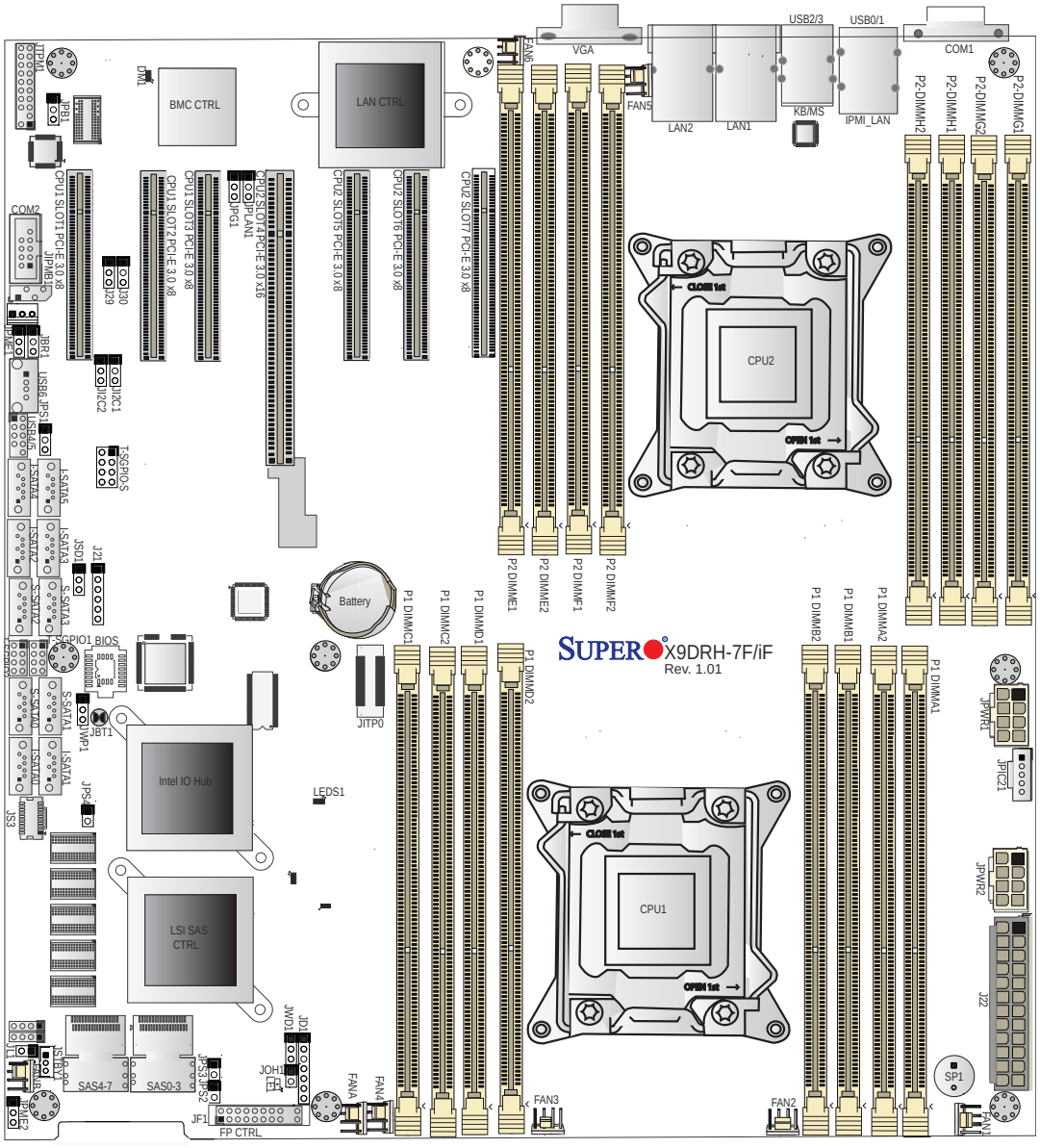
1. Begin by removing the shield for the PCI slot you wish to populate.

2. Fully seat the card into the slot, pushing down with your thumbs evenly on both sides of the card.

3. Finish by using a screw to secure the top of the card shield to the chassis. The PCI slot shields protect the serverboard and its components from EMI and aid in proper ventilation, so make sure there is always a shield covering each unused slot.

5-7 Serverboard Details

Figure 5-4. X9DRH-7TF Layout
(not drawn to scale)



Note: jumpers not indicated are for test purposes only and should not have their settings changed.

X9DRH-7TF Quick Reference

Jumper	Description	Default Setting
JBT1	Clear CMOS	See Section 5-9
JI²C1/JI²C2	SMB to PCI-E Slots	Pins 2-3 (Normal)
JPB1	BMC Enable/Disable	Pins 1-2 (Enabled)
JPG1	VGA Enable/Disable	Pins 1-2 (Enabled)
JPLAN1	LAN1/LAN2 Enable/Disable	Pins 1-2 (Enabled)
JPS1	SAS Enable/Disable	Pins 1-2 (Enabled)
JWD	Watch Dog	Pins 1-2 (Reset)

Connector	Description
CPU1 Slot1~3	CPU1 Slot1/Slot2/Slot3 PCI-E 3.0 x8 Slots
CPU2 Slot4	CPU2 Slot4 PCI-E 3.0 x16 Slot
CPU2 Slot5~7	CPU2 Slot6/Slot7 PCI-E 3.0 x8 Slots
COM1/COM2	Backplane COM Port1/Front Accessible COM2 Header
FAN1~FAN6, FANA/B	CPU/System Fan Headers (Fans 1~6) & IO Slot Fan Headers (FANA/FANB)
I-SATA 0/1	SATA 3.0 Ports 0/1 (Available for RAID 0, RAID 1 only, used in conjunction with T-SPGIO1)
I-SATA 2~5	Intel SB SATA 2.0 Connectors: 2/4 (T-SGPIO1) and 4/5 (T-SGPIO2) (Available for RAID 0, 1, 5, 10)
J22	ATX 24-Pin Power Connector
JD1	Speaker/Power LED Indicator
JF1	Control Panel Header
JIPMB1	4-pin External BMC I²C Header (for an IPMI Card)
JL1	Chassis Intrusion Header
JOH1	Overheat/Fan Fail LED Header
JPI²C1	Power Supply SMBbus I²C Header
JPWR1/JPWR2	12V 8-Pin Power Connectors
JS3	SAS Battery (Optional)
JSD1	SATA DOM (Disk On Module) Power Header
JSTBY1	Standby Power
JTPM1	TPM (Trusted Platform Module)/Port 80 Header
JLAN1/2	10 Gb LAN Ports 1/2
(IPMI) LAN	Dedicated IPMI LAN
(S-)SATA 0~3	SATA 2.0 Ports 0~3 from SCU (Available for RAID 0, 1, 5, 10 used in conjunction with T-SPGIOS)
SAS 0~3, 4~7	SAS Ports 0~3, 4~7

SP1	Onboard Buzzer (Internal Speaker)
T-SGPIO1	Serial Link General_Purpose IO Headers (used in conjunction with I-SATA 0~3)
T-SGPIO2	Serial Link General_Purpose IO Header (used in conjunction with I-SATA 4/5)
T-SGPIO-S	Serial Link General_Purpose IO Header (used in conjunction with S-SATA 0~3)
USB 0/1, 2/3	Back Panel USB 0/1, 2/3
USB4/5	Front Panel Accessible USB 4/5 Headers
USB 6	Front Panel Type A USB 6 Port
VGA	Backpanel VGA Port

LED	Description	State	Status
DM1	BMC Heartbeat LED	Green: Blinking	Normal
LE1	Power LED	Green: On	On
LEDS1	SAS LED	Green: Blinking	Normal

5-8 Connector Definitions

Power Connectors

A 24-pin main power supply connector(J22) and two 8-pin power connectors (JPWR1/JPWR2) are provided on the serverboard. These power connectors meet the SSI EPS 12V specification. These power connectors must be connected to your power supply. See the table on the right for pin definitions.

ATX Power 24-pin Connector Pin Definitions

Pin#	Definition	Pin #	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res (NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

Secondary Power Connector

JPWR1 and JPWR2 must also be connected to the power supply. See the table on the right for pin definitions.

+12V 8-pin Power Pin Definitions

Pins	Definition
1 - 4	Ground
5 - 8	+12V

Required Connection

Power Button

The Power On connection is on pins 1 and 2 of JF1. These should be connected to the chassis power button. See the table on the right for pin definitions.

Power Button Pin Definitions (JF1)

Pin#	Definition
1	Power Signal
2	Ground

Reset Button

The Reset Button connection is located on pins 3 and 4 of JF1 and attaches to the reset switch on the computer chassis. See the table on the right for pin definitions.

Reset Button Pin Definitions (JF1)

Pin#	Definition
3	Reset
4	Ground

Power Fail LED

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table on the right for pin definitions.

PWR Fail LED Pin Definitions (JF1)	
Pin#	Definition
5	Vcc
6	Ground

Overheat/Fan Fail LED (OH)

Connect an LED to the OH connection on pins 7 and 8 of JF1 to provide advanced warning of chassis overheating. Refer to the table on the right for pin definitions.

OH/Fan Fail LED Pin Definitions (JF1)	
Pin#	Definition
7	Vcc
8	Ground

OH/Fan Fail Indicator Status	
State	Definition
Off	Normal
On	Overheat
Flashing	Fan Fail

NIC2 (JLAN2) LED

The LED connections for JLAN2 are on pins 9 and 10 of JF1. Attach an LED cable to display network activity. See the table on the right for pin definitions.

NIC2 LED Pin Definitions (JF1)	
Pin#	Definition
9	Vcc
10	Ground

NIC1 (JLAN1) LED

The LED connections for JLAN1 are on pins 11 and 12 of JF1. Attach an LED cable to display network activity. See the table on the right for pin definitions.

NIC1 LED Pin Definitions (JF1)	
Pin#	Definition
11	Vcc
12	Ground

HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. This LED is used to display all SAS and SATA activity. See the table on the right for pin definitions.

HDD LED Pin Definitions (JF1)	
Pin#	Definition
13	Vcc
14	HD Active

Power On LED

The Power On LED connector is located on pins 15 and 16 of JF1 (use JLED for a 3-pin connector). This connection is used to provide LED indication of power being supplied to the system. See the table on the right for pin definitions.

Power LED Pin Definitions (JF1)

Pin#	Definition
15	5V Stby
16	Control

NMI Button

The non-maskable interrupt button header is located on pins 19 and 20 of JF1. Refer to the table on the right for pin definitions.

NMI Button Pin Definitions (JF1)

Pin#	Definition
19	Control
20	Ground

Fan Headers

There are six fan headers on the serverboard (Fan 1~Fan 6, Fan A/Fan B), all of which are 4-pin fans. Pins 1-3 of the fan headers are backward compatible with the traditional 3-pin fans. (Fan speed control is supported with 4-pin fans only.) See the table on the right for pin definitions. The onboard fan speeds are controlled by IPMI.

Fan Header Pin Definitions

Pin#	Definition
1	Ground
2	+12V
3	Tachometer
4	PWR Modulation

T-SGPIO1/2/T-SGPIO-S Headers

Two SGPIO (Serial Link General Purpose Input/Output) headers are located at T-SGPIO1/2 to support I-SATA 0~5 ports. Additionally, T-SGPIO-S supports S-SATA 0~3 ports. These headers support a Serial Link interface for onboard SATA connections. See the table on the right for pin definitions.

T-SGPIO Pin Definitions

Pin#	Definition	Pin	Definition
1	NC	2	NC
3	Ground	4	Data
5	Load	6	Ground
7	Clock	8	NC

NC= No Connection

Chassis Intrusion

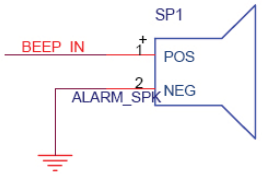
The Chassis Intrusion header is designated JL1. Attach an appropriate cable from the chassis to inform you of a chassis intrusion when the chassis is opened

Chassis Intrusion Pin Definitions	
Pin#	Definition
1	Intrusion Input
2	Ground

Internal Speaker

The Internal Speaker, located at SP1, can be used to provide audible indications for various beep codes. See the table on the right for pin definitions.

Internal Buzzer (SP1) Pin Definition		
Pin#		Definitions
Pin 1	Pos. (+)	Beep In
Pin 2	Neg. (-)	Alarm Speaker



Overheat/Fan Fail LED

The JOH1 header may be connected to an LED indicator to provide warnings of chassis overheating or fan failure. Refer to the table on right for pin definitions.

OH/Fan Fail LED Status	
State	Message
Solid	Overheat
Blinking	Fan Fail

TPM Header/Port 80 Header

A Trusted Platform Module/Port 80 header is located at JTPM1 to provide TPM support and Port 80 connection. Use this header to enhance system performance and data security. See the table on the right for pin definitions.

TPM/Port 80 Header Pin Definitions			
Pin #	Definition	Pin #	Definition
1	LCLK	2	GND
3	LFRAME#	4	<(KEY)>
5	LRESET#	6	+5V (X)
7	LAD 3	8	LAD 2
9	+3.3V	10	LAD1
11	LAD0	12	GND
13	SMB_CLK4	14	SMB_DAT4
15	+3V_DUAL	16	SERIRQ
17	GND	18	CLKRUN# (X)
19	LPCPD#	20	LDRQ# (X)

Standby Power

The Standby Power header is located at JSTBY1 on the serverboard. See the table on the right for pin definitions. (You must also have a cable to use this feature.)

Standby Power Pin Definitions

Pin#	Definition
1	+5V Standby
2	Ground
3	Wake-up

Power SMB (I^2C) Connector

Power System Management Bus (I^2C) Connector (JPI^2C1) monitors power supply, fan and system temperatures. See the table on the right for pin definitions.

PWR SMB Pin Definitions

Pin#	Definition
1	Clock
2	Data
3	PWR Fail
4	Ground
5	+3.3V

IPMB

A System Management Bus header for IPMI 2.0 is located at JIPMB1. Connect the appropriate cable here to use the IPMB I^2C connection on your system.

IPMB Header Pin Definitions

Pin#	Definition
1	Data
2	Ground
3	Clock
4	No Connection

SATA DOM Power Connector

A power connector for SATA DOM (Disk On Module) devices is located at JSD1. Connect an appropriate cable here to provide power support for your SATA DOM devices.

DOM PWR Pin Definitions

Pin#	Definition
1	+5V
2	Ground
3	Ground

SAS Battery

A SAS battery (JS3) provides power backup support for the cached data of onboard SAS devices during power outages. Cache data can be retained for up to 48 hours.

Universal Serial Bus (USB)

Four Universal Serial Bus ports (USB 0/1, 2/3) are located on the I/O back panel. In addition, three USB headers, located close to the I-SATA ports, provide two front-accessible USB connections (USB 4/5). A Type A connector (USB 6) also supports front panel USB connections. (Cables are not included). See the tables on the right for pin definitions.

Backplane USB (USB 0/1, 2/3) Pin Definitions

Pin#	Definition
1	+5V
2	PO-
3	PO+
4	Ground
5	NA

FP USB (4/5, 6) Pin Definitions

USB 4, 6 Pin #	Definition	USB 5 Pin #	Definition
1	+5V	1	+5V
2	PO-	2	PO-
3	PO+	3	PO+
4	Ground	4	Ground
5	NC	5	Key

(NC= No connection)

Serial Ports

Two COM connections (COM1 & COM2) are located on the serverboard. COM1 is located on the rear I/O panel. COM2, located next to the IPMB header, is used to provide front access support. See the table on the right for pin definitions.

Serial COM) Ports Pin Definitions

Pin #	Definition	Pin #	Definition
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	Ground	10	N/A

Ethernet Ports

Two Gigabit Ethernet ports (JLAN1/2) are located on the I/O backplane to provide internet connections. The JLAN1/JLAN2 ports support 1GLAN connections. In addition, a dedicated IPMI LAN port, located above the USB 0/1 ports on the backplane, provides KVM support for IPMI 2.0. All these ports accept RJ45 type cables.

Note: Please refer to the LED Indicator Section for LAN LED information.

LAN Ports Pin Definition

Pin#	Definition		
1	P2V5SB	10	SGND
2	TD0+	11	Act LED
3	TD0-	12	P3V3SB
4	TD1+	13	Link 100 LED (Yellow, +3V3SB)
5	TD1-	14	Link 1000 LED (Yellow, +3V3SB)
6	TD2+	15	Ground
7	TD2-	16	Ground
8	TD3+	17	Ground
9	TD3-	18	Ground

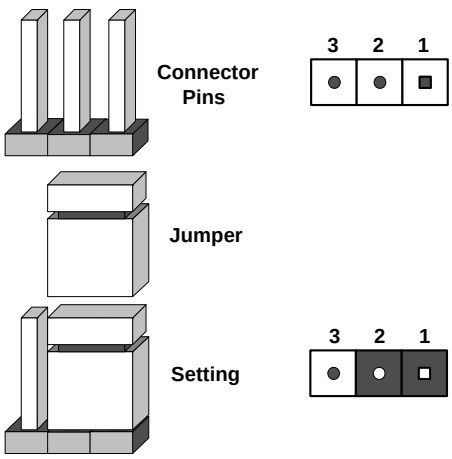
(NC: No Connection)

5-9 Jumper Settings

Explanation of Jumpers

To modify the operation of the serverboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the serverboard layout pages for jumper locations.

Note: On a two-pin jumper, "Closed" means the jumper is on both pins and "Open" means the jumper is either on only one pin or completely removed.



CMOS Clear

JBT1 is used to clear CMOS (which will also clear any passwords). Instead of pins, this jumper consists of contact pads to prevent accidentally clearing the contents of CMOS.

To clear CMOS,

1. First power down the system and unplug the power cord(s).
2. With the power disconnected, short the CMOS pads with a metal object such as a small screwdriver.
3. Remove the screwdriver (or shorting device).
4. Reconnect the power cord(s) and power on the system.

Note: *Do not use the PW_ON connector to clear CMOS*.

JLAN1/JLAN2 Enable/Disable

Use JPLAN1 to enable/disable LAN Ports 1/2. See the table on the right for jumper settings. The default setting is Enabled.

JLAN1/2 Enable/Disable Jumper Settings	
Jumper Setting	Definition
1-2	Enabled (default)
2-3	Disabled

Watch Dog Enable/Disable

Jumper JWD controls the Watch Dog function. Watch Dog is a system monitor that can reboot the system when a software application hangs. Jumping pins 1-2 will cause WD to reset the system if an application hangs. Jumping pins 2-3 will generate a non-maskable interrupt signal for the application that hangs. See the table on the right for jumper settings. Watch Dog must also be enabled in BIOS.

Watch Dog Jumper Settings

Jumper Setting	Definition
Pins 1-2	Reset (default)
Pins 2-3	NMI
Open	Disabled

VGA Enable/Disable

JPG1 allows you to enable or disable the VGA port. The default position is on pins 1 and 2 to enable VGA. See the table on the right for jumper settings.

VGA Enable/Disable Jumper Settings

Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

BMC Enable

Jumper JPB1 allows you to enable the embedded the Winbond BMC (Baseboard Management) Controller to provide IPMI 2.O/KVM support on the serverboard. See the table on the right for jumper settings.

BMC Enable Jumper Settings

Jumper Setting	Definition
Pins 1-2	BMC Enable
Pins 2-3	Normal (Default)

SAS Enable/Disable

Jumper JPS1 allows you to enable or disable the onboard SAS connections. The default setting is Enabled. See the table on the right for jumper settings.

SAS Enable/Disbale Jumper Settings

Jumper Setting	Definition
1-2	SAS Enabled
2-3	SAS Disabled

I^2C Bus to PCI-Exp. Slots

Jumpers JI^2C1 and JI^2C2 allow you to connect the System Management Bus (I^2C) to the PCI-Express slots. The default setting is Disabled. Both jumpers must be set to the same setting See the table on the right for jumper settings.

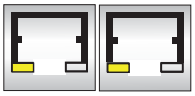
I^2C to PCI-E Slots Jumper Settings

Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

5-10 Onboard Indicators

LAN LEDs

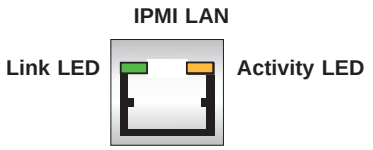
The Ethernet ports (located beside the VGA port) have two LEDs. On each port, the yellow LED flashes to indicate activity while the other LED may be green, amber or off to indicate the speed of the connection. See the table on the right for the functions associated with the connection speed LED.



JLAN1/2 LED (Connection Speed Indicator)	
LED Color	Definition
Off	NC or 10 Mb/s
Green	100 Mb/s
Amber	1 Gb/s

IPMI Dedicated LAN LEDs

An additional IPMI Dedicated LAN is also located on the I/O backplane. The amber LED on the right indicates activity, while the green LED on the left indicates the speed of the connection. See the table at right for more information.



IPMI LAN Link LED (Left) & Activity LED (Right)		
LED	Status	Definition
Link (Left)	Green: Solid	100 Mb/s
Activity (Right)	Amber: Blinking	Active

Onboard Power LED (LE1)

An Onboard Power LED is located at LE1. This LED Indicator is lit when the system is on. Be sure to unplug the power cable before removing or adding any components. See the table on the right for more details.

Onboard PWR LED Indicator (LE1) LED Settings	
LED Color	Status
Off	System Off (PWR cable not connected)
Green	System On
Green: Flashing Quickly	ACPI S1 State
Green: Flashing Slowly	ACPI S3 (STR) State

SAS Heartbeat LED

LEDS1 is a SAS Heartbeat LED. When LEDS1 is blinking, the SAS ports are functioning normally. See the table at right.

SAS LED Status	
Color/State	Definition
Green: Blinking	BMC: Normal

BMC Heartbeat LED

A BMC Heartbeat LED is located at D1 on the serverboard. When D1 is blinking, BMC is functioning normally.

BMC Heartbeat LED Status	
Color/State	Definition
Green: Blinking	BMC: Normal

5-11 SAS and SATA Ports

SATA Ports

There are ten Serial ATA Ports (I-SATA0~I-SATA 5) located on the serverboard, including eight SATA2 ports (I-SATA2~5, S-SATA0~3) and two SATA3 ports (I-SATA0~1). See the table on the right for pin definitions.

SATA Port Pin Definitions

Pin#	Definition	Pin	Definition
1	Ground	2	TXP
3	TXN	4	Ground
5	RXN	6	RXP
7	Ground		

SAS Ports

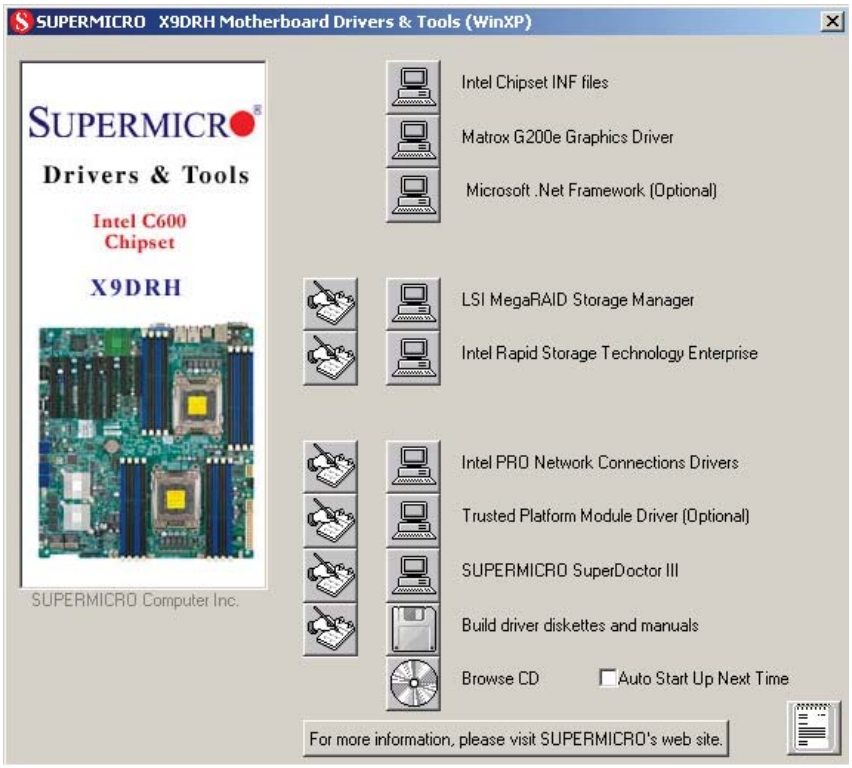
Eight Serial Attached SCSI Ports (SAS 0~3, 4~7) a provided on the X9DRH-7F to provide serial link connections. These ports are supported by the Intel C602 PCH. See the table on the right for pin definitions.

SAS Port Pin Definitions

Pin#	Definition	Pin	Definition
1	Ground	2	TXP
3	TXN	4	Ground
5	RXN	6	RXP
7	Ground		

5-12 Installing Software

After the hardware has been installed, you should first install the operating system and then the drivers. The necessary drivers are all included on the Supermicro CDs that came packaged with your serverboard.



Driver/Tool Installation Display Screen

Note: Click the icons showing a hand writing on paper to view the readme files for each item. Click the computer icons to the right of these items to install each item (from top to the bottom) one at a time. **After installing each item, you must re-boot the system before moving on to the next item on the list.** The bottom icon with a CD on it allows you to view the entire contents of the CD.

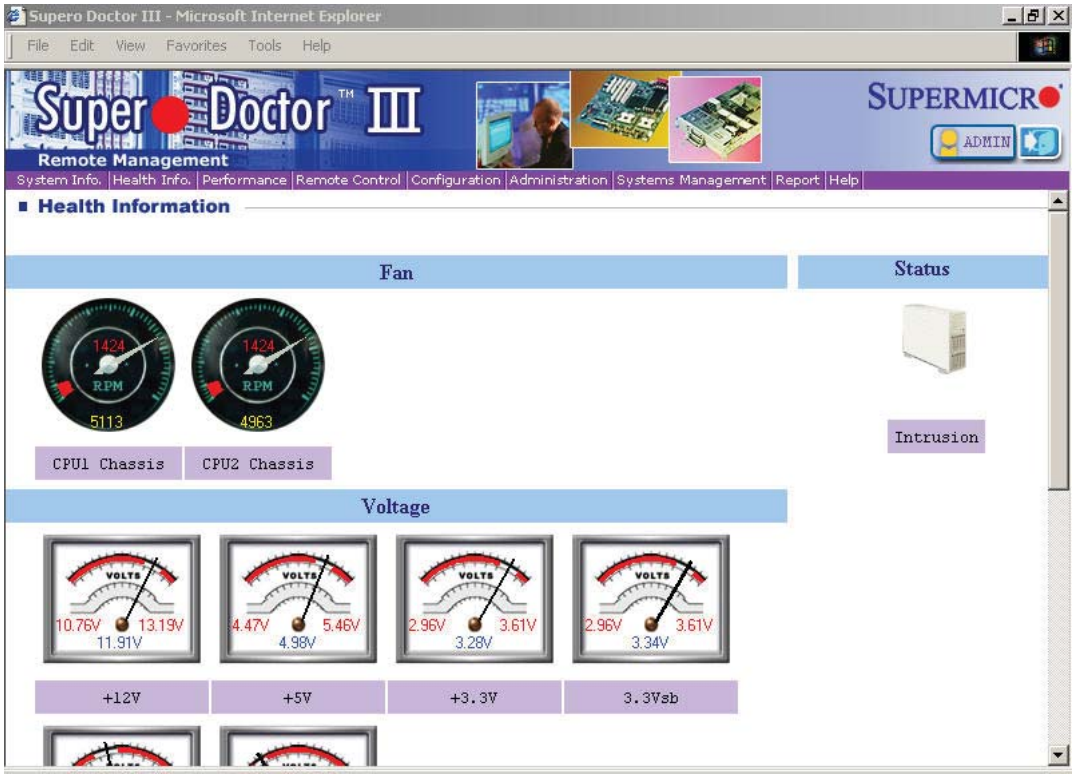
Supero Doctor III

The Supero Doctor III program is a web-based management tool that supports remote management capability. It includes Remote and Local Management tools. The local management is called SD III Client. The Supero Doctor III program included on the CD-ROM that came with your serverboard allows you to monitor the environment and operations of your system. Supero Doctor III displays crucial system information such as CPU temperature, system voltages and fan status. See the Figure below for a display of the Supero Doctor III interface.

Note: The default User Name and Password for SuperDoctor III is ADMIN / ADMIN.

Note: When SuperDoctor III is first installed, it adopts the temperature threshold settings that have been set in BIOS. Any subsequent changes to these thresholds must be made within Super Doctor, as the Super Doctor settings override the BIOS settings. To set the BIOS temperature threshold settings again, you would first need to uninstall SuperDoctor III.

Supero Doctor III Interface Display Screen (Health Information)



Supero Doctor III Interface Display Screen (Remote Control)

Note: SD III Software Revision 1.0 can be downloaded from our Web Site at: ftp://ftp.supermicro.com/utility/Supero_Doctor_III/. You can also download the SDIII User's Guide at: <http://www.supermicro.com/PRODUCT/Manuals/SDIII/UserGuide.pdf>. For Linux, we will recommend using Supero Doctor II.

Notes

Chapter 6

Advanced Chassis Setup

This chapter covers the steps required to install components and perform maintenance on the SC835TQ-R920B chassis. For component installation, follow the steps in the order given to eliminate the most common problems encountered. If some steps are unnecessary, skip ahead to the step that follows.

Tools Required: The only tool you will need to install components and perform maintenance is a Philips screwdriver.

6-1 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to any printed circuit boards (PCBs), it is important to handle them very carefully. The following measures are generally sufficient to protect your equipment from ESD damage.

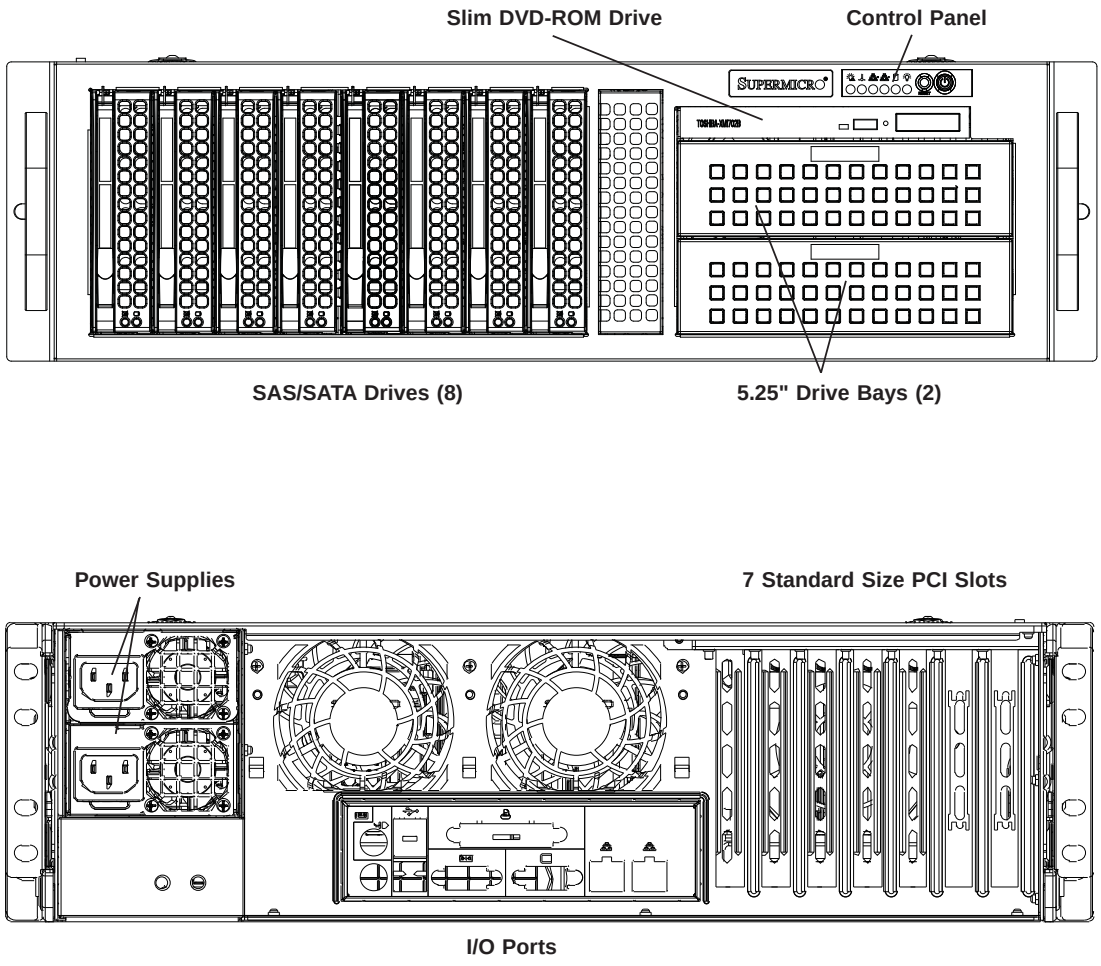
Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing any board from its antistatic bag.
- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the serverboard, add-on cards and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the serverboard.

Unpacking

The serverboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure the person handling it is static protected.

Figure 6-1. Front and Rear Chassis Views



6-2 Control Panel

The control panel (located on the front of the chassis) must be connected to the JF1 connector on the serverboard to provide you with system status indications. A ribbon cable has bundled these wires together to simplify the connection. Connect the cable from JF1 on the serverboard to the Control Panel PCB (printed circuit board). Make sure the red wire plugs into pin 1 on both connectors. Pull all excess cabling out of the airflow path. The LEDs inform you of system status.

See Chapter 3 for details on the LEDs and the control panel buttons. Details on JF1 can be found in Chapter 5.

6-3 Accessing the Inside of the System

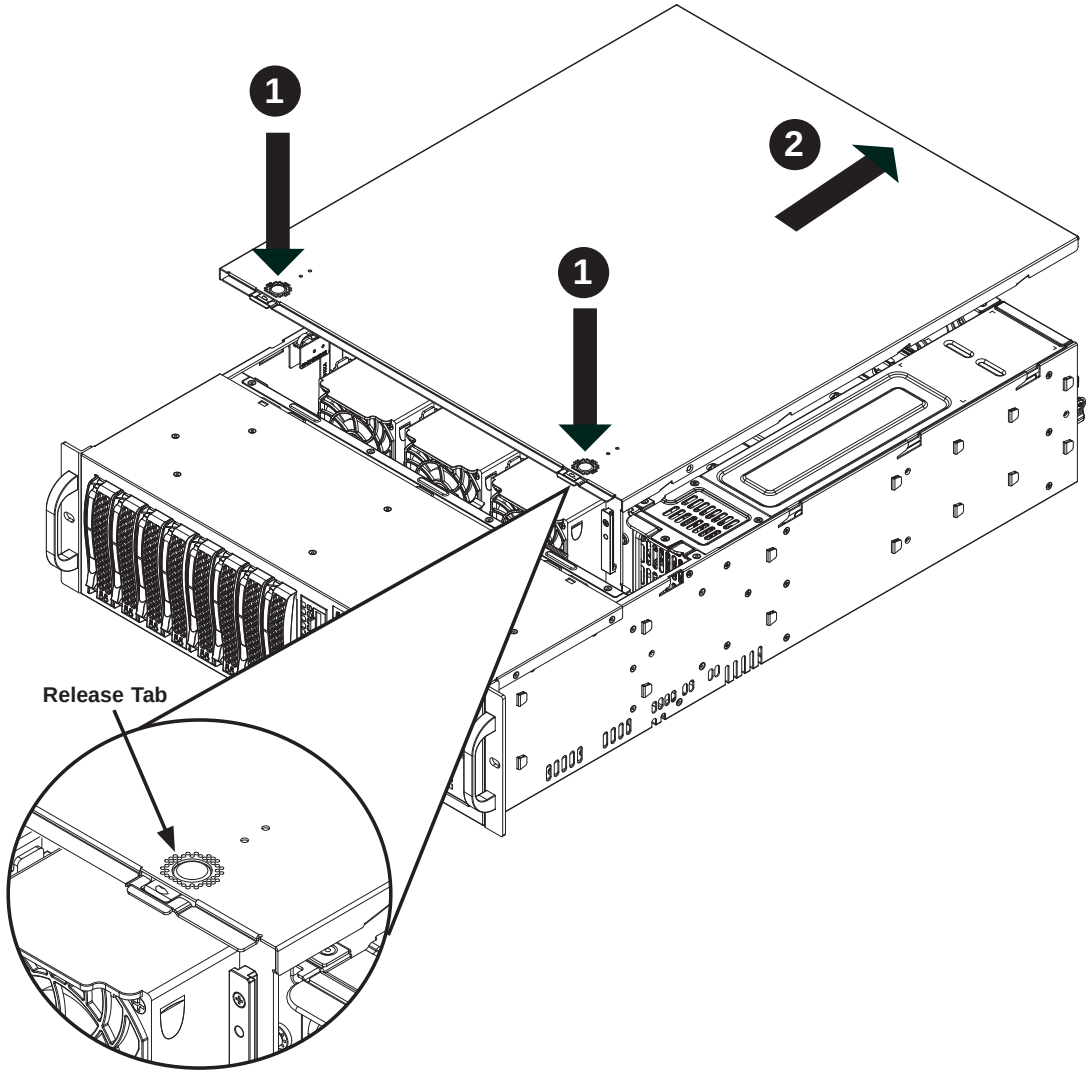
Performing maintenance on componenets such as fans requires access to the inside of the server system. Follow the steps below to remove the top/left side cover to gain access to the inside of the 6037R-72RFT.

If the system has been installed to a rack, carefully pull it out on the rails until the top cover is exposed.

1. Press the release tabs to remove the cover from the locked position. Press both tabs at the same time. If necessary, you may need to remove the chassis cover screw.

2. Once the top cover is released from the locked position, slide the cover toward the rear of the chassis and lift the cover off the unit.

Figure 2-5. Accessing the Inside of the System



6-3 System Fans

Five 8-cm hot-swap fans (two are rear exhaust fans) provide the cooling for the system. It is very important that the chassis top cover is properly installed and making a good seal in order for the cooling air to circulate properly through the chassis and cool the components.

System Fan Failure

Fan speed is controlled by system temperature via a BIOS setting. If a fan fails, the remaining fans will ramp up to full speed and the overheat/fan fail LED on the control panel will turn on. Replace any failed fan at your earliest convenience with the same type and model (the system can continue to run with a failed fan). Remove the top chassis cover while the system is still running to determine which of the fans has failed.

Replacing System Fans

Removing a Fan

System power does not need to be removed since the fans are hot-pluggable. Replace the failed fan with an identical 8-cm 12 volt fan (available from Supermicro).

1. Open the chassis and locate the faulty fan. Never run the server for an extended period of time with the chassis open.

2. Press the release tab on the fan and pull the fan upward.

3. Slide the new fan into the fan housing. Make sure the power connectors are correctly aligned. The new fan will immediately activate.

Installing the Rear Fans

The rear fans must be installed after the serverboard and air shroud setup.

Installing Rear System Fans

1. Confirm that the air shroud is correctly placed.

2. Slide the rear fan into the slot as illustrated. The fan release tab should be on the side closest to the power supply.

3. Make sure that the fan is secure in the fan housing and the housing is correctly connected to the power supply.

Figure 6-2. Replacing a Rear Fan

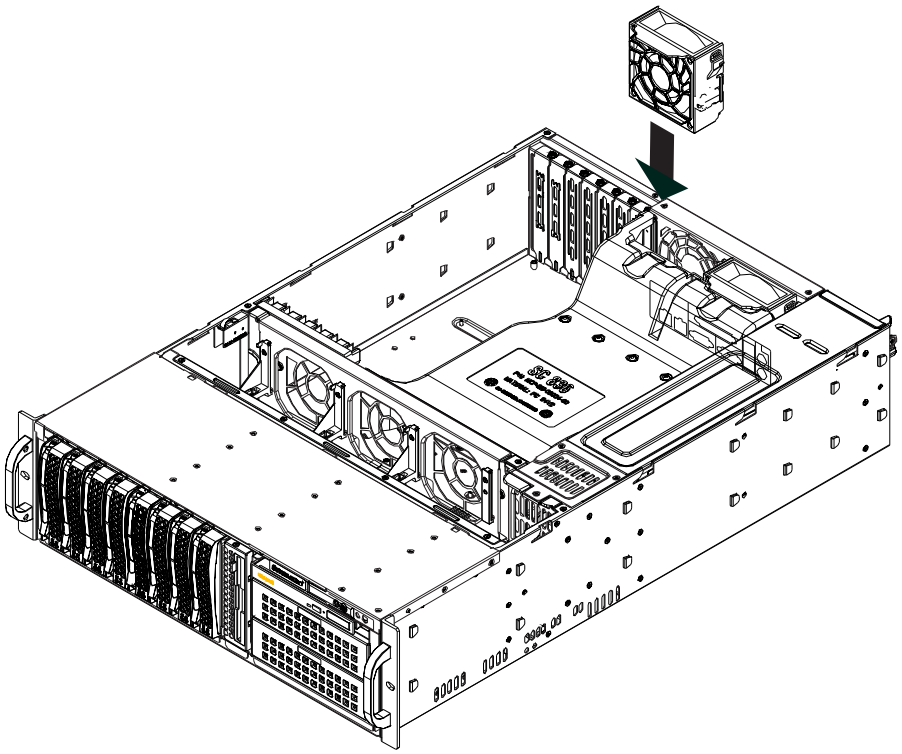
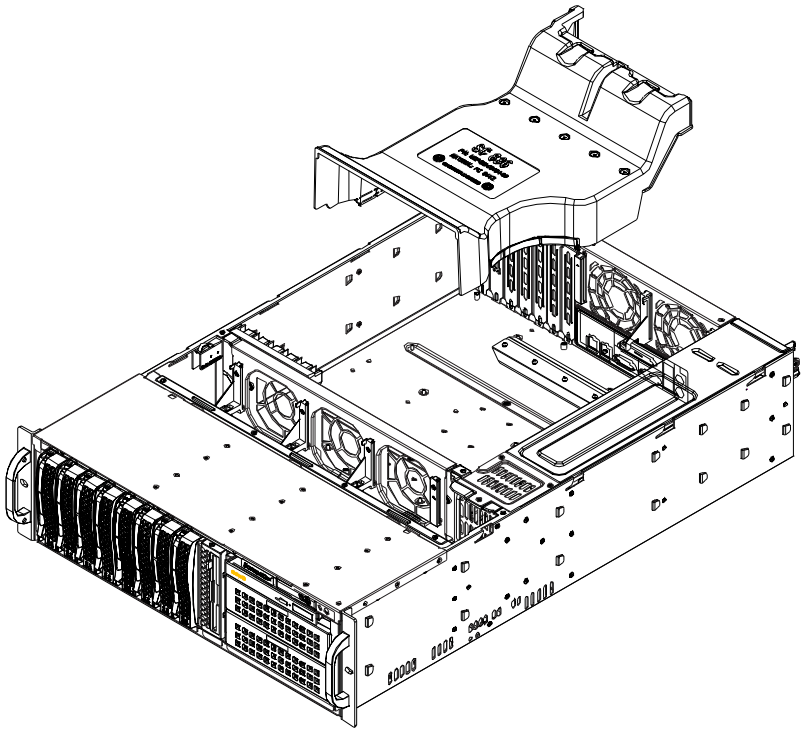


Figure 6-3. Installing the Air Shroud



Air Shroud

Air shrouds concentrate airflow to maximize fan efficiency. The SC835 chassis air shroud does not require screws to set up.

Installing the Air Shroud

1. Remove the chassis cover. If necessary, remove the rear fans.

2. Place the air shroud in the chassis (see Figure 6-3). The shroud aligns with the fan holders and covers two of the front fans with two of the rear fans. Make sure the air shroud aligns completely with the chassis

6-4 Drive Bay Installation/Removal

Accessing the Drive Bays

SAS/SATA Drives: You do not need to access the inside of the chassis or remove power to replace or swap SAS/SATA drives. Proceed to the next step for instructions. You must use standard 3.5" SAS/SATA drives in the system.

Note: Refer to the following ftp site for setup guidelines: <ftp://ftp.supermicro.com/driver/SAS/LSI/LSI_SAS_EmbMRAID_SWUG.pdf> and Supermicro's web site for additional inmformation < http://www.supermicro.com/support/manuals/>.

DVD-ROM: For installing/removing the DVD-ROM drive, you will need to gain access to the inside of the server by removing the top cover of the chassis. Proceed to the "DVD-ROM Installation" section later in this chapter for instructions.

Hard Drive Backplane

The hard drives plug into a backplane that provides power, drive ID and bus termination. A RAID controller can be used with the backplane to provide data security. The operating system you use must have RAID support to enable the hot-swap capability of the hard drives. The backplane is already preconfigured, so no jumper or switch configurations are required.

SAS/SATA Drive Installation

These drives are mounted in drive carriers to simplify their installation and removal from the chassis. The carriers also help promote proper airflow for the drives. For this reason, even empty carriers without hard drives installed must remain in the chassis.

Installing a Hard Drive (Figures 6-4 and 6-5)

1. Remove the two screws securing the dummy drive to the drive tray.
2. Lift the dummy drive out of the drive tray.
3. Place the hard drive tray on a flat, stable surface such as a desk, table, or work bench.
4. Slide the hard drive into the tray with the printed circuit board side facing down.
5. Carefully align the mounting holes in the hard drive and the tray. Make sure the bottom of the hard drive and bottom of the hard drive tray are flush.
6. Secure the hard drive using all six screws.
7. Replace the drive tray into the chassis. Make sure to close the drive tray using the drive tray handle.

Note: Your operating system must have RAID support to enable the hot-plug capability of the drives.

Use caution when working around the backplane. Do not touch the backplane with any metal objects and make sure no ribbon cables touch the backplane or obstruct the holes, which aid in proper airflow.

Important: Regardless of how many hard drives are installed, all drive carriers must remain in the drive bays to maintain proper airflow.

Figure 6-4. Removing the Dummy Drive from the Carrier

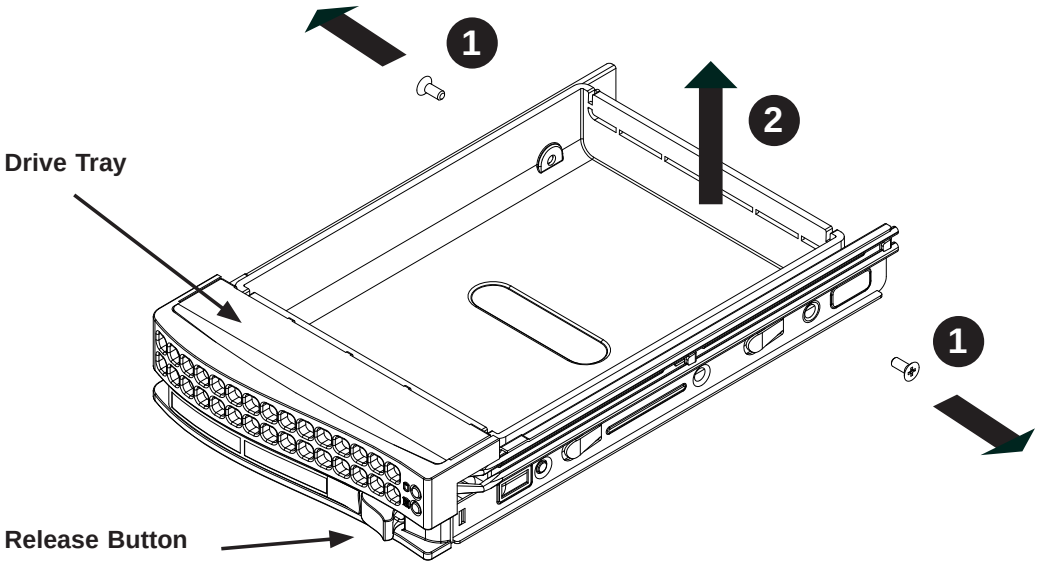
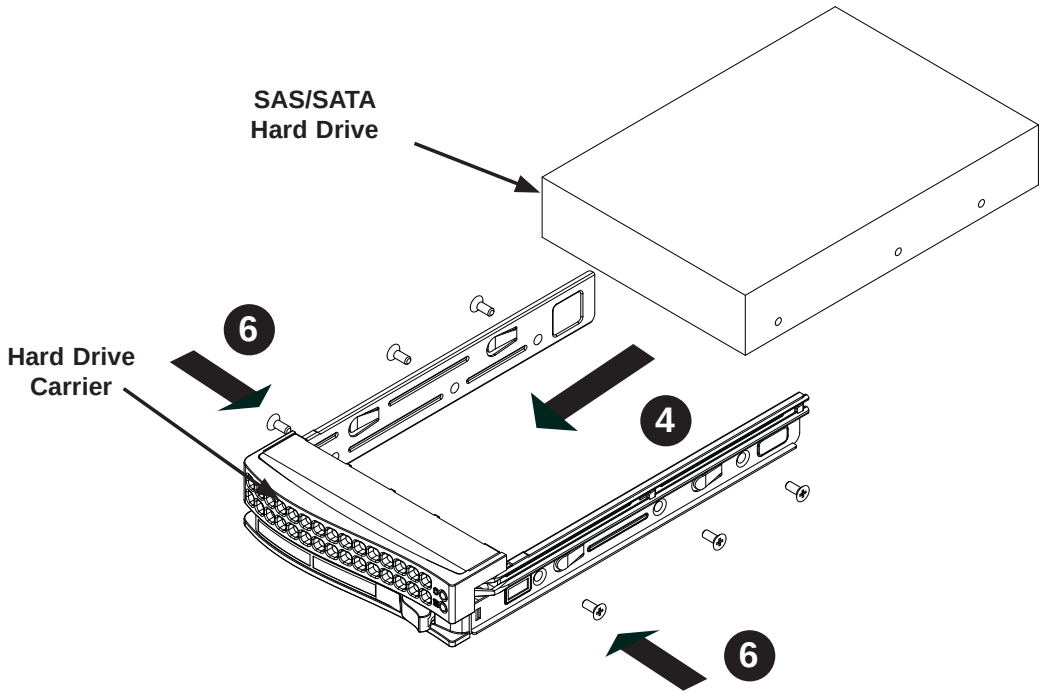


Figure 6-5. Installing a SAS or SATA Drive to a Carrier



Use a hard, stable surface when installing the hard drive

DVD-ROM Drive Installation

The SC835 chassis model supports a slim DVD-ROM drive. Use these instructions in this section in the event that you must replace any of these components.

Installing a DVD-ROM Drive (Figure 6-6)

1. Power down and unplug the system

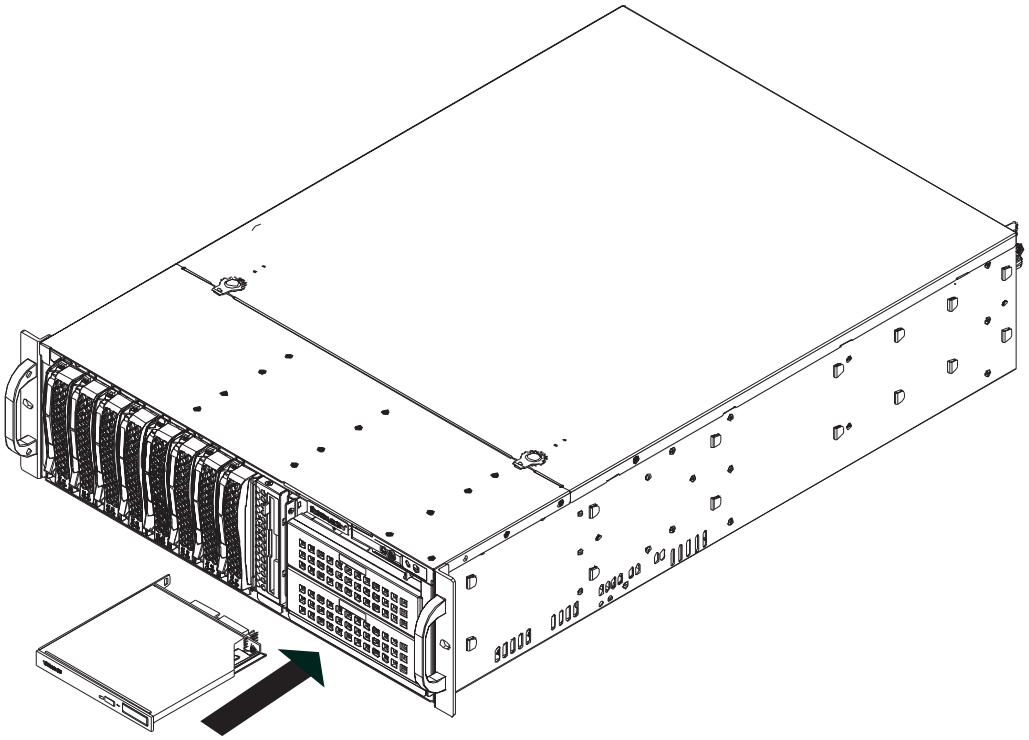
2. Remove the chassis cover.

3. **If you are not installing a new front port panel:** remove the mini-bezel from the drive bay The mini-bezel is the small grating that covers the drive bay. Remove this by simply pulling it out of the bay.
   If you are installing a new front port panel: remove the old drive by depressing the release tab, then pulling the drive out of the chassis.

4. Insert the new drive unit in the slot until the tab locks into place.

5. Connect the data and power cables to the backplane and, if necessary, serverboard.

Figure 6-6. Installing the DVD-ROM Drive



6-5 Power Supply

The SuperServer 6037R-72RFT has a 920 watt redundant power supply consisting of two power modules. Each power supply module has an auto-switching capability, which enables it to automatically sense and operate at a 100V - 240V input voltage.

Power Supply Failure

If either of the two power supply modules fail, the other module will take the full load and allow the system to continue operation without interruption. The PWR Fail LED will illuminate and remain on until the failed unit has been replaced. Replacement units can be ordered directly from Supermicro. The power supply units have a hot-swap capability, meaning you can replace the failed unit without powering down the system.

Replacing the Power Supply

You do not need to shut down the system to replace a power supply unit. The backup power supply module will keep the system up and running while you replace the failed hot-swap unit. Replace with the same model (see part number in the Appendix), which can be ordered directly from Supermicro.

Replacing the Power Supply

1. The SC835 chassis includes a redundant power supply (at least two power modules), you can leave the server running if you remove only one power supply at a time.
2. Unplug the power supply that you will replace.
3. Push the release tab (on the back of the power supply) as illustrated.
4. Pull the power supply out using the handle provided.
5. Replace the failed power module with the same model.
6. Push the new power supply module into the power bay until you hear a click.
7. Plug the AC power cord back into the module and power up the server.

Chapter 7

BIOS

7-1 Introduction

This chapter describes the AMI BIOS Setup utility for the X9DRH-7TF. It also provides the instructions on how to navigate the AMI BIOS Setup utility screens. The AMI ROM BIOS is stored in a Flash EEPROM and can be easily updated.

Starting BIOS Setup Utility

To enter the AMI BIOS Setup utility screens, press the <Del> key while the system is booting up.

Note: In most cases, the <Del> key is used to invoke the AMI BIOS setup screen. There are a few cases when other keys are used, such as <F3>, <F4>, etc.

Each main BIOS menu option is described in this manual. The Main BIOS setup menu screen has two main frames. The left frame displays all the options that can be configured. Grayed-out options cannot be configured. Options in blue can be configured by the user. The right frame displays the key legend. Above the key legend is an area reserved for a text message. When an option is selected in the left frame, it is highlighted in white. Often a text message will accompany it.

Note: The AMI BIOS has default text messages built in. The manufacturer retains the option to include, omit, or change any of these text messages.

The AMI BIOS Setup utility uses a key-based navigation system called "hot keys." Most of the AMI BIOS setup utility "hot keys" can be used at any time during setup navigation. These keys include <F3>, <F4>, <Enter>, <ESC>, arrow keys, etc.

Note 1: Options printed in **Bold** are default settings.

Note 2: <F3> is used to lad optimal default settings. <F4> is used to save the current settings and exit the setup utility.

How To Change the Configuration Data

The configuration data that determines the system parameters may be changed by entering the AMI BIOS Setup utility. This Setup utility can be accessed by pressing <F2> at the appropriate time during system boot.

Note: For AMI UEFI BIOS Recovery, please refer to the UEFI BIOS Recovery User Guide posted @http://www.supermicro.com/support/manuals/.

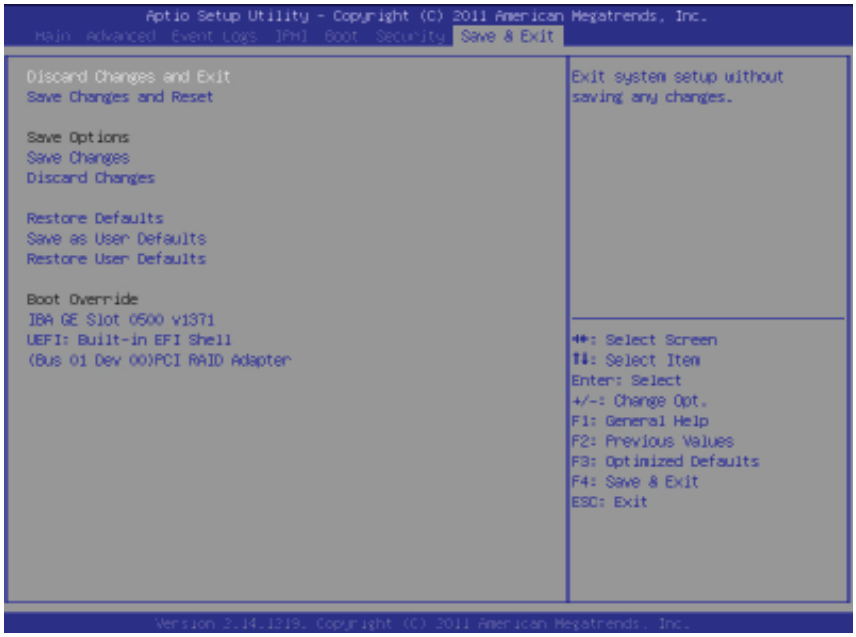
Starting the Setup Utility

Normally, the only visible Power-On Self-Test (POST) routine is the memory test. As the memory is being tested, press the <F2> key to enter the main menu of the AMI BIOS Setup utility. From the main menu, you can access the other setup screens. An AMI BIOS identification string is displayed at the left bottom corner of the screen below the copyright message.

Warning! Do not upgrade the BIOS unless your system has a BIOS-related issue. Flashing the wrong BIOS can cause irreparable damage to the system. In no event shall the manufacturer be liable for direct, indirect, special, incidental, or consequential damage arising from a BIOS update. If you have to update the BIOS, do not shut down or reset the system while the BIOS is being updated to avoid possible boot failure.

7-2 Main Setup

When you first enter the AMI BIOS Setup utility, you will enter the Main setup screen. You can always return to the Main setup screen by selecting the Main tab on the top of the screen. The Main BIOS Setup screen is shown below.



The AMI BIOS main menu displays the following information:

System Time/System Date

Use this option to change the system time and date. Highlight *System Time* or *System Date* using the arrow keys. Enter new values through the keyboard and press <Enter>. Press the <Tab> key to move between fields. The date must be entered in Day MM/DD/YY format. The time is entered in HH:MM:SS format. (**Note:** The time is in the 24-hour format. For example, 5:30 P.M. appears as 17:30:00.)

X9DRH-7TF/7F/iTF/iF

SMC Version

This item displays the SMC Version of the BIOS used in the system.

SMC Build Date

This item displays the day and time when this version of BIOS was built.

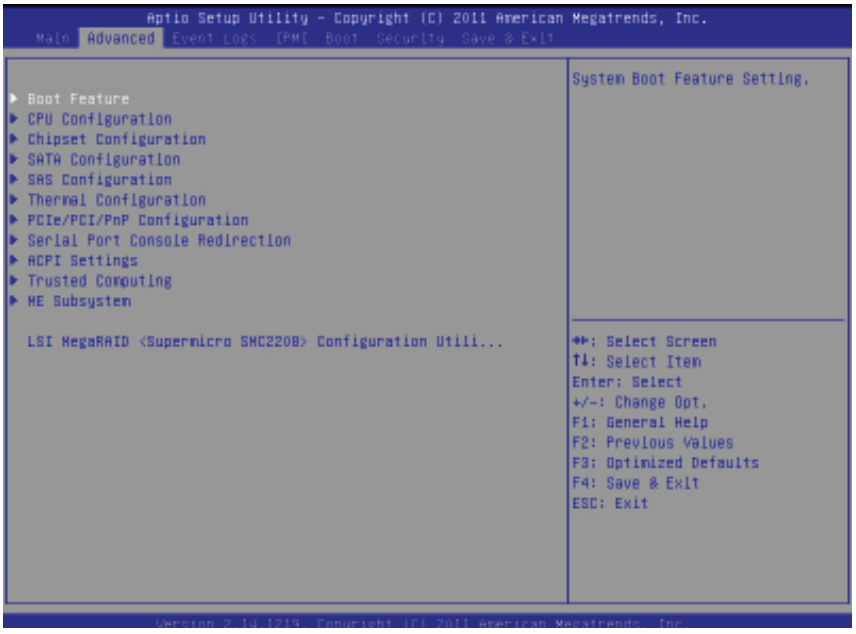
Memory Information

Total Memory

This displays the amount of memory that is available in the system.

7-3 Advanced Setup Configurations

Use the arrow keys to select Advanced and press <Enter> to access the following submenu items:



▶Boot Feature

Quiet Boot

Set this value to allow the bootup screen options to be modified between POST messages or the OEM logo. Select Disabled to allow the computer system to display the POST messages. Select Enabled to allow the computer system to display the OEM logo. The default setting is **Enabled**.

AddOn ROM Display Mode

This sets the display mode for the Option ROM. Select Keep Current to use the current AddOn ROM Display setting. Select Force BIOS to use the Option ROM display mode set by the system BIOS. The options are **Force BIOS** and Keep Current.

Bootup Num-Lock

Use this feature to set the Power-on state for the Numlock key. The options are Off and **On**.

Wait For 'F1' If Error

Select Enabled force the system to wait until the 'F1' key is pressed when an error occurs. The options are Disabled and **Enabled**.

Interrupt 19 Capture

Interrupt 19 is the software interrupt that handles the boot disk function. When this item is set to Enabled, the ROM BIOS of the host adaptors will "capture" Interrupt 19 at boot and allow the drives that are attached to these host adaptors to function as bootable disks. If this item is set to Disabled, the ROM BIOS of the host adaptors will not capture Interrupt 19, and the drives attached to these adaptors will not function as bootable devices. The options are **Enabled** and Disabled.

Power Configuration

Watch Dog Function

If enabled, the Watch Dog timer will allow the system to reboot when it is inactive for more than 5 minutes. The options are Enabled and **Disabled.** This setting must be used in conjunction with the Watch Dog jumper.

Power Button Function

If this feature is set to Instant Off, the system will power off immediately as soon as the user presses the power button. Select 4 Second Override for the system

to power off when the user presses the power button for 4 seconds or longer. The options are **Instant Off** and 4 Seconds Override.

Restore on AC Power Loss

Use this feature to set the power state after a power outage. Select Stay Off for the system power to remain off after a power loss. Select Power On for the system power to be turned on after a power loss. Select Last State to allow the system to resume its last state before a power loss. The options are Power On, Stay Off and **Last State**.

▶CPU Configuration

This submenu displays the information of the CPU as detected by the BIOS. It also allows the user to configure CPU settings.

▶Socket 0 CPU Information

This submenu displays the following information regarding the CPU installed in Socket 0.

- Type of CPU
- CPU Signature
- Microcode Patch
- CPU Stepping
- Maximum CPU Speed
- Minimum CPU Speed
- Processor Cores
- Intel HT(Hyper-Threading) Technology
- Intel VT-x (Virtualization) Technology
- L1 Data Cache
- L1 Code Cache
- L2 Cache
- L3 Cache

▶Socket 1 CPU Information

This item displays if a CPU is installed in Socket 1.

CPU Speed

This item displays the speed of the CPU installed in Socket 1.

64-bit

This item indicates if the CPU installed in Socket 1 supports 64-bit technology.

Hyper-threading

Select Enabled to support Intel Hyper-threading Technology to enhance CPU performance. The options are **Enabled** and Disabled.

Active Processor Cores

Set to Enabled to use a processor's Second Core and beyond. (Please refer to Intel's web site for more information.) The options are **All,** 1, 2, 4, and 6.

Limit CPUID Maximum

This feature allows the user to set the maximum CPU ID value. Enable this function to boot the legacy operating systems that cannot support processors with extended CPUID functions. The options are Enabled and **Disabled** (for the Windows OS).

Execute-Disable Bit Capability (Available if supported by the OS & the CPU)

Set to Enabled to enable the Execute Disable Bit which will allow the processor to designate areas in the system memory where an application code can execute and where it cannot, thus preventing a worm or a virus from flooding illegal codes to overwhelm the processor or damage the system during an attack. The default is **Enabled**. (Refer to Intel and Microsoft Web Sites for more information.)

Hardware Prefetcher (Available when supported by the CPU)

If set to Enabled, the hardware prefetcher will prefetch streams of data and instructions from the main memory to the L2 cache to improve CPU performance. The options are Disabled and **Enabled**.

Adjacent Cache Line Prefetch (Available when supported by the CPU)

If this feature is set to Disabled, The CPU prefetches the cache line for 64 bytes. If this feature is set to Enabled the CPU fetches both cache lines for 128 bytes as comprised. The options are Disabled and **Enabled**.

DCU Streamer Prefetcher (Available when supported by the CPU)

Select Enabled to support Data Cache Unite (DCU) prefetch to speed up data accessing and processing in the DCU to enhance CPU performance. The options are Disabled and **Enabled**.

DCU IP Prefetcher

Select Enabled for DCU (Data Cache Unit) IP Prefetcher support, which will prefetch IP addresses to improve network connectivity and system performance. The options are **Enabled** and Disabled.

Intel® Virtualization Technology (Available when supported by the CPU)

Select Enabled to support Intel Virtualization Technology, which will allow one platform to run multiple operating systems and applications in independent partitions, creating multiple "virtual" systems in one physical computer. The options are **Enabled** and Disabled.

Note: If there is any change to this setting, you will need to power off and restart the system for the change to take effect. Please refer to Intel's website for detailed information.

Clock Spread Spectrum

Select Enabled to enable Clock Spectrum support, which will allow the BIOS to monitor and attempt to reduce the level of Electromagnetic Interference caused by the components whenever needed. The options are **Disabled** and Enabled.

▶CPU Power Management Configuration

This submenu allows the user to configure the following CPU Power Management settings.

Power Technology

Select Energy Efficiency to support power-saving mode. Select Custom to customize system power settings. Select Disabled to disable power -saving settings. The options are Disable, **Energy Efficient** and Custom. If Custom is selected, the following options become available:

EIST

EIST (Enhanced Intel SpeedStep Technology) allows the system to automatically adjust processor voltage and core frequency in an effort to reduce power consumption and heat dissipation. **Please refer to Intel's web site for detailed information.** The options are Disabled and **Enabled**.

Turbo Mode

This feature allows processor cores to run faster than marked frequency in specific conditions. The options are Disabled and **Enabled.**

P-STATE Coordination

This feature selects the type of coordination for the P-State of the processor. P-State is a processor operational state that reduces the processor's voltage and frequency. This makes the processor more energy efficient, resulting in further gains. The options are **HW_ALL**, SW_ALL and SW-ANY.

CPU C3 Report (Available when Power Technology is set to Custom)

Select Enabled to allow the BIOS to report the CPU C3 State (ACPI C2) to the operating system. During the CPU C3 State, the CPU clock generator is turned off. The options are Enabled and **Disabled.**

CPU C6 Report (Available when Power Technology is set to Custom)

Select Enabled to allow the BIOS to report the CPU C6 State (ACPI C3) to the operating system. During the CPU C6 State, the power to all cache is turned off. The options are **Enabled** and Disabled.

CPU C7 Report (Available when Power Technology is set to Custom)

Select Enabled to allow the BIOS to report the CPU C7 State (ACPI C3) to the operating system. CPU C7 State is a processor-specific low C-State. The options are Enabled and **Disabled**.

Package C State Limit

If set to Auto, the AMI BIOS will automatically set the limit on the C-State package register. The options are C0, C2, C6, C7, and **No Limit**.

Energy Performance

This setting allows the user to adjust the fan speed based on performance (maximum cooling) or energy efficiency (maximum energy savings). The options are Performance, **Balanced Performance**, Balanced Energy, and Energy Efficient.

Factory Long Duration Power Limit

This item displays the power limit set by the manufacturer during which long duration power is maintained.

Long Duration Power Limit

This item displays the power limit set by the user during which long duration power is maintained.

Factory Long Duration Maintained

This item displays the period of time set by the manufacturer during which long duration power is maintained.

Long Duration Maintained

This item displays the period of time during which long duration power is maintained.

Recommended Short Duration Power

This item displays the short duration power settings recommended by the manufacturer.

Short Duration Power Limit

This item displays the period of time during which short duration power is maintained.

▶Chipset Configuration

▶North Bridge

This feature allows the user to configure the settings for the Intel North Bridge.

▶IOH (IO Hub) Configuration

Intel VT-d

Select Enabled to enable Intel Virtualization Technology support for Direct I/O VT-d by reporting the I/O device assignments to the VWM (Virtual Working Memory) through the DMAR ACPI Tables. This feature offers fully-protected I/O resource sharing across Intel platforms, providing greater reliability, security and availability in networking and data-sharing. The options are **Enabled** and Disabled.

Intel® I/OAT

The Intel I/OAT (I/O Acceleration Technology) significantly reduces CPU overhead by leveraging CPU architectural improvements, freeing the system resource for other tasks. The options are **Disabled** and Enabled.

DCA Support

Select Enabled to use Intel's DCA (Direct Cache Access) Technology to improve data transfer efficiency. The options are **Enabled** and Disabled.

IOH 0 PCIe Port Bifurcation Control

This submenu allows the user to configure the following 8 PCIe Port Bifurcation Control settings for the IOH 0 PCI-Exp port. This feature determines how to distribute the available PCI-Express lanes to the PCI-E Root Ports.

IOU1-PCIe Port

This feature allows the user to set the PCI-Exp bus speed between IOU1 and PCI-e port. The options are x4x4 and **x8**.

Port 1A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 1A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 1A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 1A. The options are GEN1, GEN2**,** and **GEN3**.

IOU2 - PCIe Port

This feature allows the user to set the PCI-Exp bus speed between IOU2 and PCIe port. The options are x4x4x4x4, x4x4x8, x8x4x4, **x8x8**, and x16.

Port 2A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 2A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 2A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 2A. The options are GEN1, GEN2**,** and **GEN3**.

Port 2C Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 2C. Select GEN2 to enable PCI-Exp Generation 2 support for Port 2C. Select GEN3 to enable PCI-Exp Generation 3 support for Port 2C. The options are GEN1, GEN2**,** and **GEN3**.

IOU3 - PCIe Port

This feature allows the user to set the PCI-Exp bus speed between IOU3 and PCIe port. The options are x4x4x4x4, x4x4x8, x8x4x4, **x8x8**, and x16.

Port 3A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 3A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 3A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 3A. The options are GEN1, GEN2, and **GEN3**.

Port 3C Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 3C. Select GEN2 to enable PCI-Exp Generation 2 support for Port 3C. Select GEN3 to enable PCI-Exp Generation 3 support for Port 3C. The options are GEN1, **GEN2,** and GEN3.

IOH 1 PCIe Port Bifurcation Control

This submenu allows the user to configure the following 6 PCIe Port Bifurcation Control settings for the IOH 1 PCI-Exp port. This feature determines how to distribute the available PCI-Express lanes to the PCI-E Root Ports.

IOU1-PCIe Port

This feature allows the user to set the PCI-Exp bus speed between IOU1 and PCI-e port. The options are x4x4 and **x8**.

Port 1A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 1A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 1A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 1A. The options are GEN1, GEN2**,** and **GEN3**.

IOU2 - PCIe Port

This feature allows the user to set the PCI-Exp bus speed between IOU2 and PCIe port. The options are x4x4x4x4, x4x4x8, x8x4x4, **x8x8**, and x16.

Port 2A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 2A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 2A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 2A. The options are GEN1, GEN2**,** and **GEN3**.

Port 2C Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 2C. Select GEN2 to enable PCI-Exp Generation 2 support for Port 2C. Select GEN3 to enable PCI-Exp Generation 3 support for Port 2C. The options are GEN1, GEN2**,** and **GEN3**.

IOU3 - PCIe Port

This feature allows the user to set the PCI-Exp bus speed between IOU3 and PCIe port. The options are x4x4x4x4, x4x4x8, x8x4x4, x8x8, and **x16**.

Port 3A Link Speed

Select GEN1 to enable PCI-Exp Generation 1 support for Port 3A. Select GEN2 to enable PCI-Exp Generation 2 support for Port 3A. Select GEN3 to enable PCI-Exp Generation 3 support for Port 3A. The options are GEN1, GEN2**,** and **GEN3**.

▶QPI Configuration

Current QPI Link

This item displays the current status of the QPI Link.

Current QPI Frequency

This item displays the current frequency of the QPI Link.

QPI (Quick Path Interconnect) Link Speed Mode

Use this feature to select data transfer speed for QPI Link connections. The options are **Fast** and Slow.

QPI Link Frequency Select

Use this feature to select the desired QPI frequency. The options are **Auto**, 6.4 GT/s, 7.2 GT/s, and 8.0 GT/s.

▶DIMM Configuration

- **Current Memory Mode**: This item displays the current memory mode.
- **Current Memory Speed**: This item displays the current memory speed.
- **Mirroring**: This item displays if memory mirroring is supported by the motherboard.
- **Sparing**: This item displays if memory sparing can be supported by the motherboard.

▶DIMM Information

CPU Socket 1 DIMM Information/ CPU Socket 2 DIMM Information

The status of the memory modules detected by the BIOS will be displayed.

Memory Mode

When Independent is selected, all DIMMs are available to the operating system. When Mirroring is selected, the motherboard maintains two identical copies of all data in memory for data backup. When Lockstep is selected, the motherboard uses two areas of memory to run the same set of operations in parallel. The options are **Independent**, Mirroring, Lockstep and Sparing.

Spare Err Threshold (Available if Sparing is selected for Memory Mode)

This item allows the user to determing the amount of correctable ECC errors that can accumulate before an event log is recorded. The default setting is **15**.

DRAM RAPL BWLIMIT

This item sets the limits on the average power consumption and the bandwidth of a DRAM module in operation so that the OS can manage power consumption and energy budget of hardware more effectively within a certain window of time. The options are 0, **1**, 8, and 16

Perfmon and DFX Devices

A PerfMon device monitors the activities of a remote system such as disk usage, memory consumption, and CPU load which will allow an IT administrator to maximize the performance of each computer within the network. A DFX device, usually in the form of a USB adaptor, can be used to enhance audio performance. Select Unhide to display the Perfmon and DXF devices installed in the system. The options are **HIDE** and UNHIDE.

DRAM RAPL Mode

RAPL which stands for Running Average Power Limit is a feature that provides mechanisms to enforce power consumption limits on supported processors The options are DRAM RAPL MODE0 , **DRAM RAPL MODE1**, and Disabled.

MPST Support

Select Enabled to enable the Message Processing Subscriber Terminal which is used to process short messages. The options are **Disabled** and Enabled.

DDR Speed

Use this feature to force a DDR3 memory module to run at a frequency other than what is indicated in the system specification. The options are **Auto**, Force DDR3-800, Force DDR3-1066, Force DDR3-1333, Force DOR3-1600 and Force SPD.

Channel Interleaving

This feature selects from the different channel interleaving methods. The options are **Auto**, 1 Way, 2 Way, 3, Way, and 4 Way.

Rank Interleaving

This feature allows the user to select a rank memory interleaving method. The options are **Auto**, 1 Way, 2 Way, 4, Way, and 8 Way.

Patrol Scrub

Patrol Scrubbing is a process that allows the CPU to correct correctable memory errors detected on a memory module and send the correction to the requestor (the original source). When this item is set to Enabled, the IO hub will read and write back one cache line every 16K cycles, if there is no delay caused by internal processing. By using this method, roughly 64 GB of memory behind the IO hub will be scrubbed every day. The options are **Enabled** and Disabled.

Demand Scrub

Demand Scrubbing is a process that allows the CPU to correct correctable memory errors found on a memory module. When the CPU or I/O issues a demand-read command, and the read data from memory turns out to be a correctable error, the error is corrected and sent to the requestor (the original source). Memory is updated as well. Select Enabled to use Demand Scrubbing for ECC memory correction. The options are Enabled and **Disabled**.

Data Scrambling

Select Enabled to enable data scrubbing and ensure data security and integrity. The options are **Disabled** and Enabled.

DRAM RAPL

RAPL which stands for Running Average Power Limit is a feature that provides mechanisms to enforce power consumption limits on supported processors The options are Mode 0**, MODE1**, and Disabled.

Device Tagging

Select Enabled to support device tagging. The options are **Disabled** and Enabled.

Thermal Throttling

Throttling improves reliability and reduces power consumption in the processor via automatic voltage control during processor idle states. The options are Disabled and **CLTT** (Closed Loop Thermal Throttling).

OLTT Peak BW %

Use this feature to set a percentage of the peak bandwidth allowed for OLTT. Enter a number between 25 to 100 (%).The default setting is **50**.

▶South Bridge

This feature allows the user to configure the settings for the Intel PCH chip.

PCH Information

This feature displays the following PCH information.

- **Name**: This item displays the name of the PCH chip.
- **Stepping**: This item displays the status of the PCH stepping.

All USB Devices

Select Enabled to enable all onboard USB devices. The options are **Enabled** and Disabled. When set to enabled, EHCI Controller 1 and 2 will appear below.

EHCI Controller 1/ EHCI Controller 2

Select Enabled to enable Enhanced Host Interface (EHCI) Controller 1 or Controller 2 to improve overall platform performance. The options are **Enabled** and Disabled.

Legacy USB Support (Available when USB Functions is not Disabled)

Select Enabled to support legacy USB devices. Select Auto to disable legacy support if USB devices are not present. Select Disable to have USB devices available for EFI (Extensive Firmware Interface) applications only. The settings are **Enabled,** Disabled and Auto.

Port 60/64 Emulation

Select Enabled to enable I/O port 60h/64h emulation support for the legacy USB keyboard so that it can be fully supported by the operating systems that do not recognize a USB device. The options are Disabled and **Enabled**.

EHCI Hand-off

Select Enabled to enable support for operating systems that do not support Enhanced Host Controller Interface (EHCI) hand-off. When enabled, EHCI ownership change will be claimed by the EHCI driver. The options are **Disabled** and Enabled.

▶SATA Configuration

When this submenu is selected, the AMI BIOS automatically detects the presence of IDE or SATA devices and displays the following items.

SATA Port0~SATA Port5

The AMI BIOS displays the status of each SATA port as detected by the BIOS.

SATA Mode

Use this feature to configure SATA mode for a selected SATA port. The options are Disabled, IDE Mode, **AHCI Mode**, and RAID Mode. The following are displayed depending on your selection:

IDE Mode

The following items are displayed when IDE Mode is selected:

Serial-ATA (SATA) Controller 0~1

Use this feature to activate or deactivate the SATA controller, and set the compatibility mode. The options are Enhanced and Compatible. The default for Controller 0 is **Compatible**. The default for Controller 1 is **Enhanced**.

AHCI Mode

The following items are displayed when the AHCI Mode is selected:

Aggressive Link Power Management

Select Enabled to enable Aggressive Link Power Management to support Cougar Point B0 stepping and beyond. The options are **Enabled** and Disabled.

Port 0~Port 5 Hot Plug

Select Enabled to enable hot-plug support for a port specified by the user so that the user is allowed to change a hardware component or a device without shutting down the system. The options are **Enabled** and Disabled.

Staggered Spin-up

Select Enabled to enable Staggered Spin-up support to prevent excessive power consumption caused by multiple HDDs spinning-up simultaneously. The options are Enabled and **Disabled**.

RAID Mode

The following items are displayed when RAID Mode is selected:

Port 0~5 Hot Plug

Select Enabled to enable hot-plug support for a port specified by the user. The options are **Enabled** and Disabled.

▶SAS Configuration

If a SAS port is detected in the system, the following items will be displayed.

SCU Devices

Select Enabled to enable support for PCH SCU (System Configuration Utility) devices. The options are Disabled and **Enabled**.

OnChip SAS Oprom

Select Enabled to support the onboard SAS Option ROM to boot up the system via a storage device if a SAS device is installed. The options are Disabled and **Enabled**.

SCU Port 0~3

The SCU devices detected by the BIOS will be displayed.

▶PCIe/PCI/PnP Configuration

This submenu allows the user to configure the following PCIe/PCI/PnP settings.

PCI ROM Priority

Use this feature to select the Option ROM to boot up the system when there are multiple Option ROMs available in the system. The options are **Legacy ROM** and EFI Compatible ROM.

PCI Latency Timer

Use this feature to set the latency timer of each PCI device installed on a PCI bus. Select 64 to set the PCI latency to 64 PCI clock cycles. The options are 32, **64**, 96, 128, 160, 192, 224, and 248.

Above 4G Decoding (Available if the system supports 64-bit PCI decoding)

Select Enabled to decode a PCI device that supports 64-bit in the space above 4G Address. The options are Enabled and **Disabled**.

PERR# Generation

Select Enabled to allow a PCI device to generate a PERR number for a PCI Bus Signal Error Event. The options are Enabled and **Disabled**.

SERR# Generation

Select Enabled to allow a PCI device to generate a SERR number for a PCI Bus Signal Error Event. The options are Enabled and **Disabled**.

Maximum Payload

This feature selects the setting for the PCIE maximum payload size. The options are **Auto,** 128 Bytes**,** 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes.

Maximum Read Request

This feature selects the setting for the PCIE maximum Read Request size. The options are **Auto,** 128 Bytes**,** 256 Bytes, 512 Bytes, 1024 Bytes, 2048 Bytes, and 4096 Bytes**.**

ASPM Support

This feature allows the user to set the Active State Power Management (ASPM) level for a PCI-E device. Select Force L0 to force all PCI-E links to operate at L0 state. Select Auto to allow the system BIOS to automatically set the ASPM level for the system. Select Disabled to disable ASPM support. The options are **Disabled,** Auto, and Force L0s.

CPU1 Slot 1 PCI-E x8 OPROM/CPU1 Slot 2 PCI-E x8 OPROM/CPU1 Slot 3 PCI-E x8 OPROM/CPU2 Slot 4 PCI-E x16 OPROM/CPU2 Slot 5 PCI-E x8 OPROM/CPU2 Slot 6 PCI-E x8 OPROM/CPU2 Slot 7 PCI-E x8 OPROM

Select Enabled to enable Option ROM support to boot the computer using a network interface from the slots specified above. The options are **Enabled** and Disabled.

Onboard LAN Option ROM Select

This feature selects whether to load the iSCSI or PXE onboard LAN option ROM. The options are iSCSI and **PXE**.

Load Onboard LAN1 Option ROM/Load Onboard LAN2 Option ROM

Select Enabled to enable the onboard LAN1 Option ROM~LAN2 Option ROM. This is to boot the computer using a network device. The default setting for LAN1 Option ROM is **Enabled**. The default setting for LAN2 Option ROM is **Disabled**.

Load Onboard SAS Option ROM

Select Enabled to use the SAS Option ROM to boot the computer using a network device. The options are **Enabled** and Disabled.

VGA Priority

Use this feature to specify which graphics controller to be used as the primary boot device. The options are **Onboard** and Offboard (VGA).

Network Stack

Select Enabled to enable PXE (Preboot Execution Environment) or UEFI (Unified Extensible Firmware Interface) for network stack support. The options are Enabled and **Disabled**. Note that if Enabled, IPV4 and IPV6 PXE support will be disabled.

▶Serial Port Console Redirection

These submenus allow the user to configure the following Console Redirection settings for a COM Port 0 or COM Port 1 as specified by the user.

COM1/COM2

Console Redirection

Select Enabled to use a COM Port selected by the user for Console Redirection. The options are **Enabled** and Disabled.

▶Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, **VT100+**, and VT-UTF8.

Bits Per Second

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 38400, 57600, and **115200** (bits per second).

Data Bits

Use this feature to set the data transmission size for Console Redirection. The options are 7 and **8** (Bits).

Parity

A parity bit can be sent along with regular data bits to detect data transmission errors. Select Even if the parity bit is set to 0, and the number of 1's in data bits is even. Select Odd if the parity bit is set to 0, and the number of 1's in data bits is odd. Select None if you do not want to send a parity bit with your data bits in

transmission. Select Mark to add mark as a parity bit to be sent along with the data bits. Select Space to add a Space as a parity bit to be sent with your data bits. The options are **None**, Even, Odd, Mark, and Space.

Stop Bits

A stop bit indicates the end of a serial data packet. Select 1 Stop Bit for standard serial data communication. Select 2 Stop Bits if slower devices are used. The options are **1** and 2.

Flow Control

This feature allows the user to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None** and Hardware RTS/CTS.

VT-UTF8 Combo Key Support

Select Enabled to enable VT-UTF8 Combination Key support for ANSI/VT100 terminals. The options are **Enabled** and Disabled.

Recorder Mode

Select Enabled to capture the data displayed on a terminal and send it as text messages to a remote server. The options are **Disabled** and Enabled.

Resolution 100x31

Select Enabled for extended-terminal resolution support. The options are Disabled and **Enabled**.

Legacy OS Redirection

Use this feature to select the number of rows and columns used in Console Redirection for legacy OS support. The options are 80x24 and **80x25**.

Putty Keypad

Use this feature to select function key and keypad setting on Putty. The options are **VT100**, LINUX, XTERMR6, SCO, ESCN, and VT400.

Serial Port for Out-of-Band Management/Windows Emergency Management Services (EMS)

This item allows the user to configure Console Redirection settings to support Out-of-Band Serial Port management.

Console Redirection

Select Enabled to use a COM Port selected by the user for Console Redirection. The options are **Enabled** and Disabled.

▶Console Redirection Settings

This feature allows the user to specify how the host computer will exchange data with the client computer, which is the remote computer used by the user.

Out-of-Band-Mgmt Port

Use this feature to select the port for out-of-band management. The options are COM1 and **COM2**.

Terminal Type

This feature allows the user to select the target terminal emulation type for Console Redirection. Select VT100 to use the ASCII character set. Select VT100+ to add color and function key support. Select ANSI to use the extended ASCII character set. Select VT-UTF8 to use UTF8 encoding to map Unicode characters into one or more bytes. The options are ANSI, VT100, **VT100+**, and VT-UTF8.

Bits Per Second

This item sets the transmission speed for a serial port used in Console Redirection. Make sure that the same speed is used in the host computer and the client computer. A lower transmission speed may be required for long and busy lines. The options are 9600, 19200, 57600, and **115200** (bits per second).

Flow Control

This feature allows the user to set the flow control for Console Redirection to prevent data loss caused by buffer overflow. Send a "Stop" signal to stop sending data when the receiving buffer is full. Send a "Start" signal to start sending data when the receiving buffer is empty. The options are **None,** Hardware RTS/CTS, and Software Xon/Xoff.

▶ACPI Settings

Use this feature to configure Advanced Configuration and Power Interface (ACPI) power management settings for your system.

ACPI Sleep State

Use this feature to select the ACPI State when the system is in sleep mode. Select S1 (CPU Stop Clock) to erase all CPU caches and stop executing instructions. Power to the CPU(s) and RAM is maintained, but RAM is refreshed. Select Suspend

to use power-reduced mode. Power will only be supplied to limited components (such as RAMs) to maintain the most critical functions of the system. The options are **S1 (CPU Stop Clock)** and Suspend Disabled.

Numa

This feature enables the Non-Uniform Memory Access ACPI support. The options are **Enabled** and Disabled.

High Precision Timer

Select Enabled to activate the High Precision Event Timer (HPET) that produces periodic interrupts at a much higher frequency than a Real-time Clock (RTC) does in synchronizing multimedia streams, providing smooth playback, reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in the CPU. The High Performance Event Timer is used to replace the 8254 Programmable Interval Timer. The options are **Enabled** and Disabled.

▶Trusted Computing (Available if a TPM device is installed)

Configuration

TPM Support

Select Enabled on this item and enable the TPM jumper on the motherboard to allow TPM support to improve data integrity and network security. The options are Enabled and **Disabled**.

Current Status Information: This item displays the information regarding the current TPM status.

TPM Enable Status

If a security device is detected by the BIOS, this item displays the status of TPM Support to indicate if TPM is currently enabled or disabled.

TPM Active Status

If a security device is detected by the BIOS, this item displays the status of TPM Support to indicate if TPM is currently active or deactivated.

TPM Owner Status

If a security device is detected by the BIOS, this item displays the status of TPM Ownership.

▶Intel TXT(LT-SX) Configuration

This feature indicates if the following hardware components support the Intel TXT (Trusted Execution Technology), which helps protect against software-based attacks and ensures protection, confidentiality and integrity of data stored or created on the system.

- **CPU/Chipset TXT Feature** - Displays status of TXT Feature support.
- **TXT Support** - Indicates if TXT support is enabled or disabled. The default setting is **Disabled**.
- **Intel TXT Dependencies** - Displays a list of features that must be supported (and enabled) before Intel TXT(LT-SX) configuration can be enabled.

▶ME (Management Engine) Subsystem

Intel ME Subsystem Configuration

This feature displays the following ME Subsystem Configuration settings.

ME Subsystem

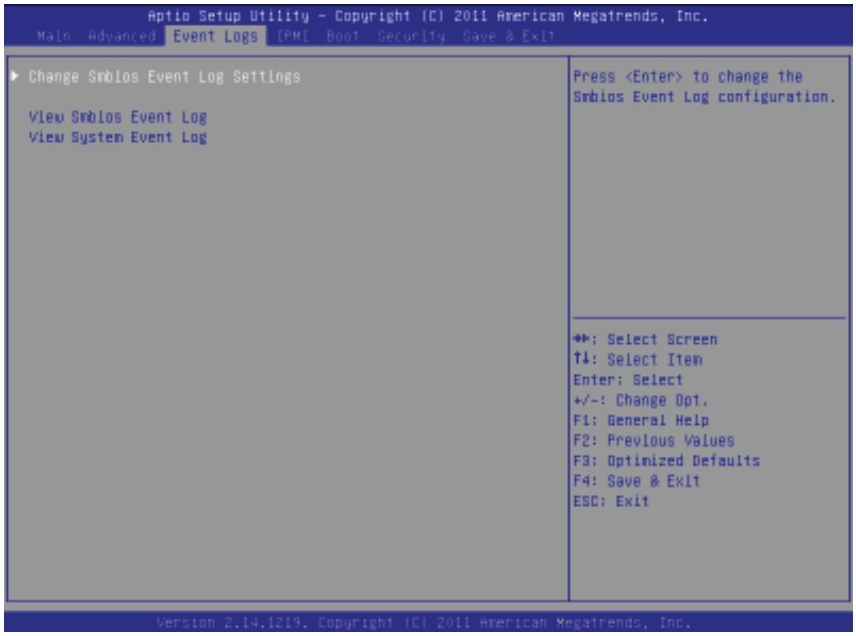
Select Enabled to support Intel Management Engine (ME) Subsystem, a small power computer subsystem that performs various tasks in the background. The options are **Enabled** and Disabled.

When ME Subsystem is enabled, the following items will display.

- **ME BIOS Interface Version**
- **ME Version**

7-4 Event Logs

Use this menu to configure Event Log settings.



▶Change SmBIOS Event Log Settings

Enabling/Disabling Options

Smbios Event Log

Change this item to enable or disable all features of the Smbios Event Logging during boot. The options are **Enabled** and Disabled.

Erasing Settings

Erase Event Log

This option erases all logged events. The options are **No**, Yes, Next reset, and Yes, Every reset.

When Log is Full

This option automatically clears the Event Log memory of all messages when it is full. The options are **Do Nothing** and Erase Immediately.

Log System Boot Event

This option toggles the System Boot Event logging to enabled or disabled. The options are **Disabled** and Enabled.

MECI

The Multiple Event Count Increment (MECI) counter counts the number of occurences a duplicate event must happen before the MECI counter is incremented. Enter a number between 1 to 255. The default setting is **1**.

METW

The Multiple Event Time Window (METW) defines number of minutes must pass between duplicate log events before MECI is incremented. Enter a number between 0 to 99. The default setting is **60**.

View Smbios Event Log

This feature displays the contents of the SmBIOS Event Log.

View System Event Log

This feature displays the contents of the System Event Log.

7-5 IPMI

Use this menu to configure Intelligent Platform Management Interface (IPMI) settings.

▶System Event Log

Enabling/Disabling Options

SEL Components

Select Enabled for all system event logging at bootup. The options are **Enabled** and Disabled.

Erasing Settings

Erase SEL

Select 'Yes, On next reset' to erase all system event logs upon next system reboot. Select 'Yes, On every reset' to erase all system event logs upon each system reboot. Select No to keep all system event logs after each system reboot. The options are **No**, Yes, On next reset, and Yes, On every reset.

When SEL is Full

This feature allows the user to decide what the BIOS should do when the system event log is full. Select Erase Immediately to erase all events in the log when the system event log is full. The options are **Do Nothing** and Erase Immediately.

Log EFI Status Codes

Select Enabled to log EFI (Extensible Firmware Interface) Status Codes, Error Codes or Progress Codes. The options are Disabled and **Enabled**.

> **Note**: After making changes on a setting, be sure to reboot the system for the changes to take effect.

▶BMC Network Configuration

LAN Channel 1: This feature allows the user to configure the settings for LAN Channel 1.

Update IPMI LAN Configuration

This feature allows the user to decide if the BIOS should configure the IPMI setting at next system boot. The options are **No** and Yes. If the option is set to Yes, the user is allowed to configure the IPMI settings at next system boot:

Configuration Address Source

This feature allows the user to select the source of the IP address for this computer. If Static is selected, you will need to know the IP address of this computer and enter it to the system manually in the field below. The options are Static and **DHCP**.

Station IP Address

This item displays the Station IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

Subnet Mask

This item displays the sub-network that this computer belongs to. The value of each three-digit number separated by dots should not exceed 255.

Station MAC Address

This item displays the Station Mac address for this computer. Mac addresses are 6 two-digit hexadecimal numbers.

Gateway IP Address

This item displays the Gateway IP address for this computer. This should be in decimal and in dotted quad form (i.e., 192.168.10.253).

7-6 Boot

This menu allows the user to configure the following boot settings for the system.

Boot Option Priorities

Boot Option #1/ Boot Option #2/ Boot Option #3

Use this feature to specify the sequence of boot device priority.

Network Device BBS Priorities, Hard Drive BBS Priorities

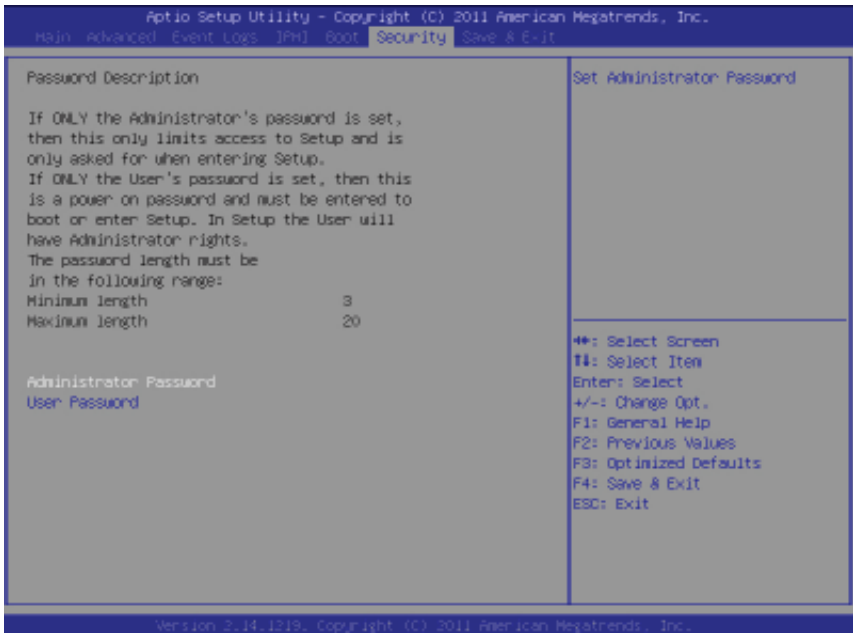
This option sets the order of the legacy network devices and Hard Disks detected by the motherboard.

▶Delete Boot Option

This feature allows the user to delete a previously defined boot device from which the system boots during startup.

7-7 Security

This menu allows the user to configure the following security settings for the system.



Administrator Password

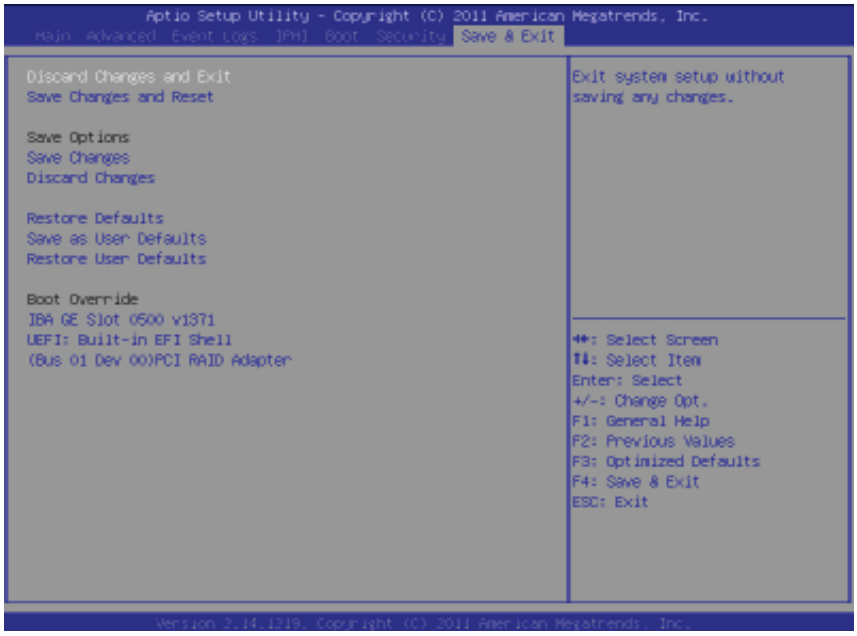
Use this feature to set the Administrator Password which is required to enter the BIOS setup utility. The length of the password should be from 3 characters to 8 characters long.

User Password

Use this feature to set a User Password which is required to log into the system and to enter the BIOS setup utility. The length of the password should be from 3 characters to 8 characters long.

7-8 Save & Exit

This menu allows the user to configure the Save and Exit settings for the system.



Discard Changes and Exit

Select this option to quit the BIOS Setup without making any permanent changes to the system configuration, and reboot the computer. Select Discard Changes and Exit, and press <Enter>. When the dialog box appears, asking you if you want to exit the BIOS setup without saving, click **Yes** to quit BIOS without saving the changes, or click No to quit the BIOS and save changes.

Save Changes and Reset

When you have completed the system configuration changes, select this option to save the changes and reboot the computer, so that the new system configuration parameters can take effect. Select Save Changes and Exit, and press <Enter>.

When the dialog box appears, asking you if you want to exit the BIOS setup without saving, click **Yes** to quit BIOS without saving the changes, or click No to quit the BIOS and save changes.

Save Options

Save Changes

Select this option and press <Enter> to save all changes you've done so far and return to the AMI BIOS utility Program. This will not reset (reboot) the system. When the dialog box appears, asking you if you want to save configuration, click **Yes** to save the changes, or click No to return to the BIOS without making changes.

Discard Changes

Select this feature and press <Enter> to discard all the changes and return to the BIOS setup. When the dialog box appears, asking you if you want to load previous values, click **Yes** to load the values previous saved, or click No to keep the changes you've made so far.

Restore Defaults

Select this feature and press <Enter> to load the default settings that help optimize system performance. When the dialog box appears, asking you if you want to load the defaults, click **Yes** to load the default settings, or click No to abandon defaults.

Save As User Defaults

Select this feature and press <Enter> to save the current settings as the user's defaults. When the dialog box appears, asking you if you want to save values as user's defaults, click **Yes** to save the current values as user's default settings, or click No to keep the defaults previously saved as the user's defaults.

Restore User Defaults

Select this feature and press <Enter> to load the user's defaults previously saved in the system. When the dialog box appears, asking you if you want to restore user's defaults, click **Yes** to restore the user's defaults previously saved in the system, or click No to abandon the user's defaults that were previously saved.

Boot Override

This feature allows the user to enter a new setting to overwrite the original setting that was saved for the listed devices.

Appendix A

BIOS Error Beep Codes

During the POST (Power-On Self-Test) routines, which are performed each time the system is powered on, errors may occur.

Non-fatal errors are those which, in most cases, allow the system to continue the boot-up process. The error messages normally appear on the screen.

Fatal errors are those which will not allow the system to continue the boot-up procedure. If a fatal error occurs, you should consult with your system manufacturer for possible repairs.

These fatal errors are usually communicated through a series of audible beeps. The numbers on the fatal error list (on the following page) correspond to the number of beeps for the corresponding error. All errors listed, with the exception of Beep Code 8, are fatal errors.

Beep Code	Error Message	Description
1 beep	Refresh	Circuits have been reset (Ready to power up)
5 short beeps and 1 long beep	Memory error	No memory detected in the system
5 beeps	No Con-In or Con-Out devices	Con-In includes USB or PS/2 keyboard, PCI or serial console redirection, IPMI KVM or SOL. Con-Out includes video controller, PCI or serial console redirection, IPMI SOL.
1 Continuous beep	System OH	System Overheat

Notes

Appendix B

System Specifications

Processors

Single or dual Intel® E5-2600 Series (Socket R) processors in LGA 2011 sockets (both CPUs must be of the same type)

Note: Please refer to our web site for a complete listing of supported processors.

Chipset

Intel C600 chipset

BIOS

16 Mb AMI® SPI Flash ROM

Memory Capacity

Sixteen DIMM Slots supporting up to 512 GB of ECC registered/unbuffered DDR3-1600/1066/800 memory

Note: See Section 5-5 for details.

SAS Controller

LSI 2208 SAS controller for eight SAS ports

SATA Controller

Intel chipset-based SATA controller for six SATA ports

Drive Bays

Eight hot-swap drive bays to house eight SATA or SAS drives

Peripheral Drive Bays

One slim DVD-ROM drive

Expansion Slots

Six PCI-E 3.0 x8 (in x16 slots) and one PCI-E 3.0 x16 slots

Serverboard

X9DRH-7TF
Dimensions: 12 x 13 in (305 x 330 mm)

Chassis

SC835TQ-R920B (3U rackmount)
Dimensions: (WxHxD) 17.7 x 5.2 x 25.6 in. (450 x 132 x 650 mm)

Weight

Gross (Bare Bone): 75 lbs. (34 kg.)

System Cooling

Three 8-cm system fans and two 8-cm rear exhaust fans

System Input Requirements

AC Input Voltage: 100 - 240V AC auto-range
Rated Input Current: 13 - 4A max
Rated Input Frequency: 50 to 60 Hz

Power Supply

Rated Output Power: 920W (Part# PWS-920P-1R)
Rated Output Voltages: +12V (75A), +5Vsb (4A)

Operating Environment

Operating Temperature: 10º to 35º C (50º to 95º F)
Non-operating Temperature: -40º to 70º C (-40º to 158º F)
Operating Relative Humidity: 20% to 95% (non-condensing)
Non-operating Relative Humidity: 5 to 95% (non-condensing)

Regulatory Compliance

Electromagnetic Emissions: FCC Class A, EN 55022 Class A, EN 61000-3-2/-3-3, CISPR 22 Class A

Electromagnetic Immunity: EN 55024/CISPR 24, (EN 61000-4-2, EN 61000-4-3, EN 61000-4-4, EN 61000-4-5, EN 61000-4-6, EN 61000-4-8, EN 61000-4-11)

Safety: CSA/EN/IEC/UL 60950-1 Compliant, UL or CSA Listed (USA and Canada), CE Marking (Europe)

California Best Management Practices Regulations for Perchlorate Materials: This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. “Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate”

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