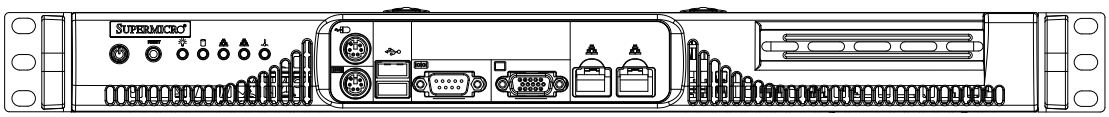
SUPERMICRO®

SUPERSERVER 5015B-MF



USER'S MANUAL

Revision 1.0a

The information in this User's Manual has been carefully reviewed and is believed to be accurate. The vendor assumes no responsibility for any inaccuracies that may be contained in this document, makes no commitment to update or to keep current the information in this manual, or to notify any person or organization of the updates. **Please Note: For the most up-to-date version of this manual, please see our web site at www.supermicro.com.**

Super Micro Computer, Inc. ("Supermicro") reserves the right to make changes to the product described in this manual at any time and without notice. This product, including software, if any, and documentation may not, in whole or in part, be copied, photocopied, reproduced, translated or reduced to any medium or machine without prior written consent.

IN NO EVENT WILL SUPERMICRO BE LIABLE FOR DIRECT, INDIRECT, SPECIAL, INCIDENTAL, SPECULATIVE OR CONSEQUENTIAL DAMAGES ARISING FROM THE USE OR INABILITY TO USE THIS PRODUCT OR DOCUMENTATION, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGES. IN PARTICULAR, SUPERMICRO SHALL NOT HAVE LIABILITY FOR ANY HARDWARE, SOFTWARE, OR DATA STORED OR USED WITH THE PRODUCT, INCLUDING THE COSTS OF REPAIRING, REPLACING, INTEGRATING, INSTALLING OR RECOVERING SUCH HARDWARE, SOFTWARE, OR DATA.

Any disputes arising between manufacturer and customer shall be governed by the laws of Santa Clara County in the State of California, USA. The State of California, County of Santa Clara shall be the exclusive venue for the resolution of any such disputes. Super Micro's total liability for all claims will not exceed the price paid for the hardware product.

FCC Statement: This equipment has been tested and found to comply with the limits for a Class A digital device pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference when the equipment is operated in a commercial environment. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the manufacturer's instruction manual, may cause harmful interference with radio communications. Operation of this equipment in a residential area is likely to cause harmful interference, in which case you will be required to correct the interference at your own expense.

California Best Management Practices Regulations for Perchlorate Materials: This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. "Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate"

WARNING: Handling of lead solder materials used in this product may expose you to lead, a chemical known to the State of California to cause birth defects and other reproductive harm.

Manual Revision 1.0a
Release Date: September 30, 2009

Preface

About This Manual

This manual is written for professional system integrators and PC technicians. It provides information for the installation and use of the SuperServer 5015B-MF. Installation and maintainance should be performed by experienced technicians only.

The SuperServer 5015B-MF is a high-end single processor 1U rackmount server based on the SC513L-260 server chassis and the X7SBi motherboard. The X7SBi supports single Intel® Xeon® 3000 Series/3200 Series Processor at system bus speeds of 1333/1066/800 MHz.

Manual Organization

Chapter 1: Introduction

The first chapter provides a checklist of the main components included with the server system and describes the main features of the Super X7SBi motherboard and the SC513L-260 chassis.

Chapter 2: Server Installation

This chapter describes the steps necessary to install the SuperServer 5015B-MF into a rack and check out the server configuration prior to powering up the system. If your server was ordered without the processor and memory components, this chapter will refer you to the appropriate sections of the manual for their installation.

Chapter 3: System Interface

Refer to this chapter for details on the system interface, which includes the functions and information provided by the control panel on the chassis as well as other LEDs located throughout the system.

Chapter 4: System Safety

You should thoroughly familiarize yourself with this chapter for a general overview of safety precautions that should be followed when installing and servicing the SuperServer 5015B-MF.

Chapter 5: Advanced Motherboard Setup

Chapter 5 provides detailed information on the X7SBi motherboard, including the locations and functions of connectors, headers and jumpers. Refer to this chapter when adding or removing processors or main memory and when reconfiguring the motherboard.

Chapter 6: Advanced Chassis Setup

Refer to Chapter 6 for detailed information on the SC513L-260 1U rackmount server chassis. You should follow the procedures given in this chapter when installing, removing or reconfiguring Serial ATA or peripheral drives and when replacing system power supply units and cooling fans.

Chapter 7: BIOS

The BIOS chapter includes an introduction to BIOS and provides detailed information on running the CMOS Setup Utility.

Appendix A: BIOS POST Messages

Appendix B: BIOS POST Codes

Appendix C: System Specifications

Notes

Table of Contents

Chapter 1

Introduction

1-1 Overview

The Supermicro SuperServer 5015B-MF is a high-end single processor, 1U rack-mount server with state-of-the-art features. The 5015B-MF is comprised of two main subsystems: the SC513L-260 1U chassis and the X7SBi motherboard. Please refer to our web site for information on operating systems that have been certified for use with the 5015B-MF.

In addition to the mainboard and chassis, various hardware components may have been included with the 5015B-MF, as listed below.

- One CPU heatsink (SNK-P0016P)
- Two (2) 10-cm blower fans (FAN-0059)
- One (1) air shroud (MCP-310-51301-0B)
- One (1) SATA cable (CBL-0061L)
- One (1) riser card for PCI-Express x8 add-on card (CSE-RR1U-ELi)
- One (1) riser card for 3.3V PCI-X add-on card (CSE-RR1U-Xi)
- Rackmount hardware with screws (CSE-PT8)
- One (1) CD containing drivers and utilities
- SuperServer 5015B-MF User's Manual

1-2 Motherboard Features

At the heart of the SuperServer 5015B-MF lies the X7SBi, a single processor motherboard based upon Intel's E3210 chipset. Below are the main features of the X7SBi.

Processor

The X7SBi supports single Intel Xeon 3200/3000 Series LGA775 processors at system bus speeds of 1333, 1066 and 800 MHz. Please refer to the motherboard specifications pages on our web site for updates on supported processors.

Memory

The X7SBi has four 240-pin DIMM slots that can support up to 8 GB of unbuffered ECC DDR2-800/667 SDRAM.

Onboard SATA

A SATA controller is built in to the ICH9R portion of the chipset to provide support for a six port, 3 Gb/sec Serial ATA subsystem, which is RAID 0, 1, 5 and 10 supported. The SATA drives are hot-swappable units. **Note:** The operating system you use must have RAID support to enable the hot-swap capability and RAID function of the SATA drives.

PCI Expansion Slots

The X7SBi has one universal PCI-Express x8 slot, one 64-bit 133 MHz PCI-X slot and one PCI 33 MHz slot.

Onboard Controllers/Ports

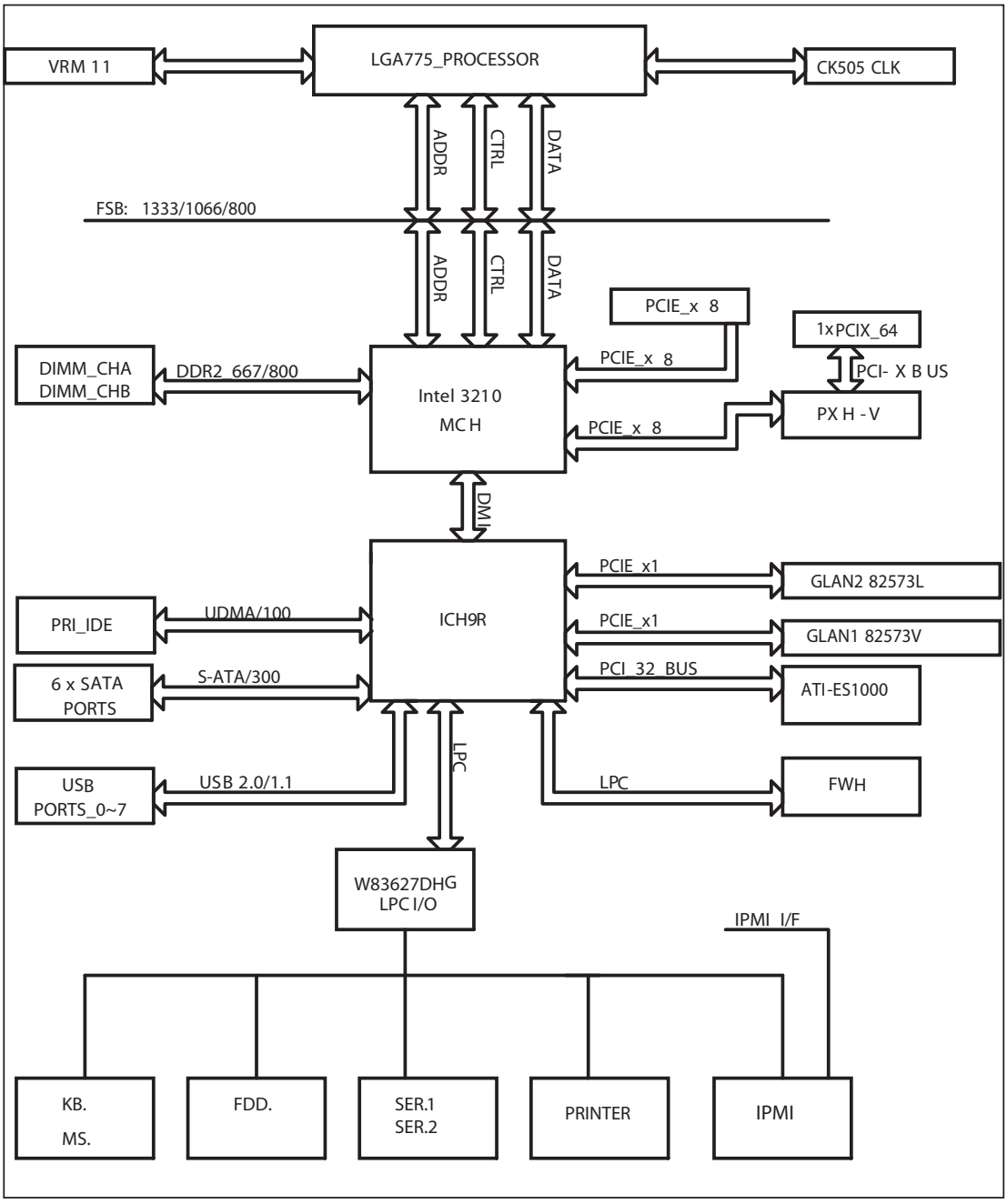
An onboard IDE controller supports one floppy drive and up to two Ultra ATA 100 hard drives or ATAPI devices. Onboard I/O backpanel ports include one COM port, a VGA port, two USB ports, PS/2 mouse and keyboard ports and two Gigabit LAN (NIC) ports.

Other Features

Other onboard features that promote system health include voltage monitors, a chassis intrusion header, auto-switching voltage regulators, chassis and CPU overheat sensors, virus protection and BIOS rescue.

Figure 1-1. Intel 3210 Chipset: System Block Diagram

Note: This is a general block diagram. Please see Chapter 5 for details.



1-3 Server Chassis Features

The SC513L-260 features a unique design that places all I/O ports at the front of the chassis for easy access. The following is a general outline of the main features of the SC513L-260 chassis.

System Power

When configured as a SuperServer 5015B-MF, the SC513L-260 chassis includes a single 260W power supply.

Serial ATA Subsystem

The SC513L-260 chassis was designed to support one internal 3.5" Serial ATA hard drive. This Serial ATA drive is not hot-swappable; power must be removed from the system before installing or replacing. ATA/100 IDE drives can be alternately supported.

Note: The operating system you use must have RAID support to enable the hot-swap capability of the Serial ATA drives.

Control Panel

The control panel on the SC513L-260 provides important system monitoring and control information. LEDs indicate power on, network activity, hard disk drive activity and system overheat conditions. The control panel also includes a main power button and a system reset button.

Front I/O Panel

The I/O panel for the SC513L-260 is located on the front side of the chassis. It provides one COM port, two USB ports, PS/2 mouse and keyboard ports, a graphics port and two Gb Ethernet ports.

Cooling System

The SC513L-260 chassis has an innovative cooling design that features one 10-cm blower-type system cooling fan. The blower fan plugs into a chassis fan header on the motherboard and an air shroud channels the airflow to efficiently cool the processor area.

A fan speed control setting in BIOS allows fan speed to be determined by system temperature [the recommended setting is 3-pn (Server)].

1-4 Contacting Supermicro

Headquarters

Address:	Super Micro Computer, Inc.
	980 Rock Ave.
	San Jose, CA 95131 U.S.A.
Tel:	+1 (408) 503-8000
Fax:	+1 (408) 503-8008
Email:	marketing@supermicro.com (General Information)
	support@supermicro.com (Technical Support)
Web Site:	www.supermicro.com

Europe

Address:	Super Micro Computer B.V.
	Het Sterrenbeeld 28, 5215 ML
	's-Hertogenbosch, The Netherlands
Tel:	+31 (0) 73-6400390
Fax:	+31 (0) 73-6416525
Email:	sales@supermicro.nl (General Information)
	support@supermicro.nl (Technical Support)
	rma@supermicro.nl (Customer Support)

Asia-Pacific

Address:	Super Micro Computer, Inc.
	4F, No. 232-1, Liancheng Rd.
	Chung-Ho 235, Taipei County
	Taiwan, R.O.C.
Tel:	+886-(2) 8226-3990
Fax:	+886-(2) 8226-3991
Web Site:	www.supermicro.com.tw
Technical Support:	
Email:	support@supermicro.com.tw
Tel:	886-2-8228-1366, ext.132 or 139

Notes

Chapter 2

Server Installation

2-1 Overview

This chapter provides a quick setup checklist to get your SuperServer 5015B-MF up and running. Following the steps in the order given should enable you to have the system operational within a minimal amount of time. This quick setup assumes that your 5015B-MF system has come to you with the processor and memory preinstalled. If your system is not already fully integrated with a motherboard, processor, system memory etc., please turn to the chapter or section noted in each step for details on installing specific components.

2-2 Unpacking the System

You should inspect the box the SuperServer 5015B-MF was shipped in and note if it was damaged in any way. If the server itself shows damage, you should file a damage claim with the carrier who delivered it.

Decide on a suitable location for the rack unit that will hold the SuperServer 5015B-MF. It should be situated in a clean, dust-free area that is well ventilated. Avoid areas where heat, electrical noise and electromagnetic fields are generated. You will also need it placed near a grounded power outlet. Read the Rack and Server Precautions in the next section.

2-3 Preparing for Setup

The SuperServer 5015B-MF does not ship with a rack rail hardware package as the system can be rack mounted without the use of rails. An optional rack rail package is available if you wish to order from Supermicro. Follow the steps in the order given to complete the installation process in a minimal amount of time. Please read this section in its entirety before you begin the installation procedure outlined in the sections that follow.

Choosing a Setup Location

- Leave enough clearance in front of the rack to enable you to open the front door completely (~25 inches) and approximately 30 inches of clearance in the back of the rack to allow for sufficient airflow and ease in servicing.This product is for

installation only in a Restricted Access Location (dedicated equipment rooms, service closets and the like).

- This product is not suitable for use with visual display work place devices acccording to §2 of the the German Ordinance for Work with Visual Display Units.

Warnings and Precautions!

Rack Precautions

- Ensure that the leveling jacks on the bottom of the rack are fully extended to the floor with the full weight of the rack resting on them.
- In single rack installation, stabilizers should be attached to the rack. In multiple rack installations, the racks should be coupled together.
- Always make sure the rack is stable before extending a component from the rack.
- You should extend only one component at a time - extending two or more simultaneously may cause the rack to become unstable.

Server Precautions

- Review the electrical and general safety precautions in Chapter 4.
- Determine the placement of each component in the rack *before* you install the rails.
- Install the heaviest server components on the bottom of the rack first, and then work up.
- Use a regulating uninterruptible power supply (UPS) to protect the server from power surges, voltage spikes and to keep your system operating in case of a power failure.
- Allow the hot plug SATA drives and power supply modules to cool before touching them.
- Always keep the rack's front door and all panels and components on the servers closed when not servicing to maintain proper cooling.

Rack Mounting Considerations

Ambient Operating Temperature

If installed in a closed or multi-unit rack assembly, the ambient operating temperature of the rack environment may be greater than the ambient temperature of the room. Therefore, consideration should be given to installing the equipment in an environment compatible with the manufacturer's maximum rated ambient temperature (Tmra).

Reduced Airflow

Equipment should be mounted into a rack so that the amount of airflow required for safe operation is not compromised.

Mechanical Loading

Equipment should be mounted into a rack so that a hazardous condition does not arise due to uneven mechanical loading.

Circuit Overloading

Consideration should be given to the connection of the equipment to the power supply circuitry and the effect that any possible overloading of circuits might have on overcurrent protection and power supply wiring. Appropriate consideration of equipment nameplate ratings should be used when addressing this concern.

Reliable Ground

A reliable ground must be maintained at all times. To ensure this, the rack itself should be grounded. Particular attention should be given to power supply connections other than the direct connections to the branch circuit (i.e. the use of power strips, etc.).

2-4 Installing the System into a Rack

This section provides information on installing the SuperServer 5015B-MF into a rack unit. If the system has already been mounted into a rack, you can skip ahead to Sections 2-5 and 2-6.

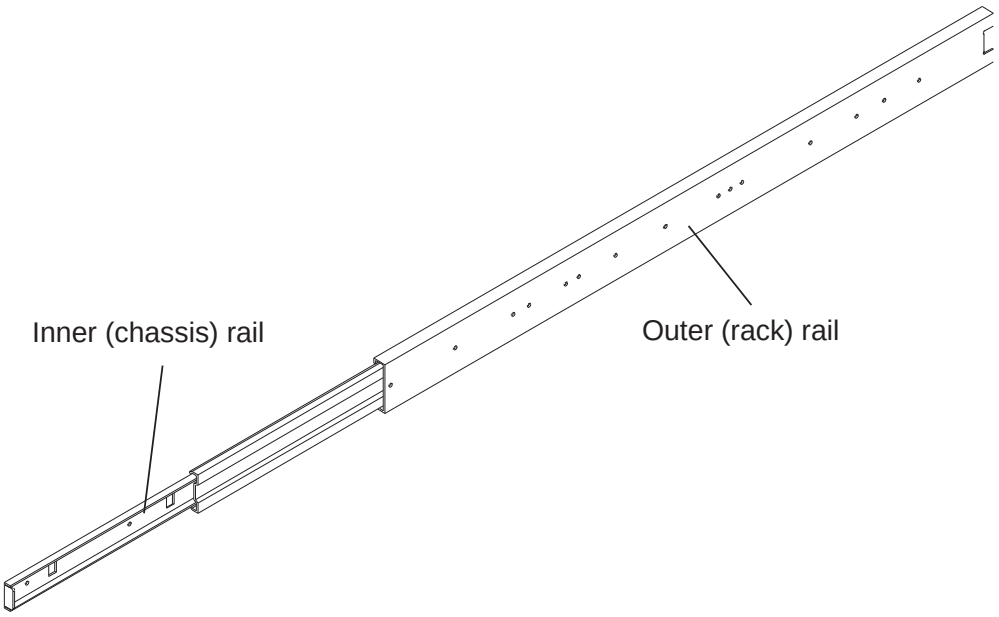
Basic Installation Procedure

The 5015B-MF server comes with two rack mounting brackets, which are located on each side at the front of the chassis. To mount the system into a rack, simply screw these brackets directly to the front of the rack (two screws for each bracket). You may also detach the brackets from the chassis and move them back approx. 4 inches, where you will see a group of three holes to secure them to. You may then secure them to the rack as just described. Note that installing in this manner may prevent you from closing the rack door.

Installing with Optional Rackmount Kit (CSE-PT8)

This section is only for customers that have the optional rack mount kit.. Each of these assemblies consist of two sections: an inner rail that secures to the chassis and an outer rail that secures directly to the rack itself (see Figure 2-1). This is a guideline for installing the unit into a rack with the optional rack kit. You should also refer to the installation instructions that came with the rack unit you are using. Be

Figure 2-1. Identifying the Rail Sections



aware that there are a variety of rack units on the market, which may mean the assembly procedure will differ slightly.

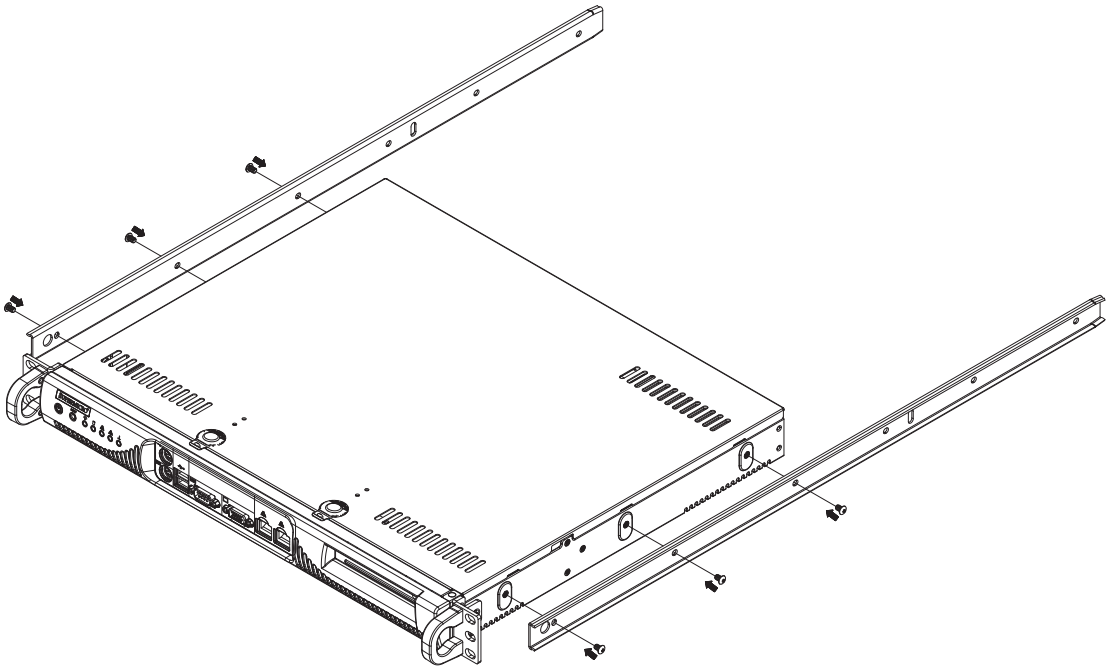
The two rail sections must be detached from each other prior to installation. Do this by depressing the locking tab on the inner rail to release it from its locked position then pull the two rails completely apart. Do this for both the left and right side rack rail assemblies.

Installing the Chassis Rails

Position the fixed chassis rail sections you just removed along the side of the chassis so that the three screw holes line up with the corresponding holes on the chassis. Note that these two rails are left/right specific. Screw the rail securely to the side of the chassis (see Figure 2-2) with the screws provided in the rackmount kit. Repeat this procedure for the other rail on the other side of the chassis. You will also need to attach the rail brackets when installing into a telco rack.

Locking Tabs: As you have seen, both chassis rails have a locking tab, which serves two functions. The first is to lock the server into place when installed and pushed fully into the rack, which is its normal position. Secondly, these tabs also lock the server in place when fully extended from the rack. This prevents the server from coming completely out of the rack when you pull it out for servicing.

Figure 2-2. Installing Chassis Rails



Installing the Rack Rails

Locate a pair of front (short) and rear (long) brackets in the rackmount kit. The brackets are marked with arrows to distinguish front from rear. Secure the front and rear brackets to the rack rails using the screws and washers provided, but do not fully tighten. Determine where you want to place the SuperServer 5015B-MF in the rack (see Rack and Server Precautions in Section 2-3). Adjust the length of the rails according to the depth of the rack. Now tighten the screws holding the brackets to the rails.

Next, secure the rail to the rack with the screws provided. Repeat this procedure to attach the other rail to the other side of the rack, making sure that both are at the exact same height.

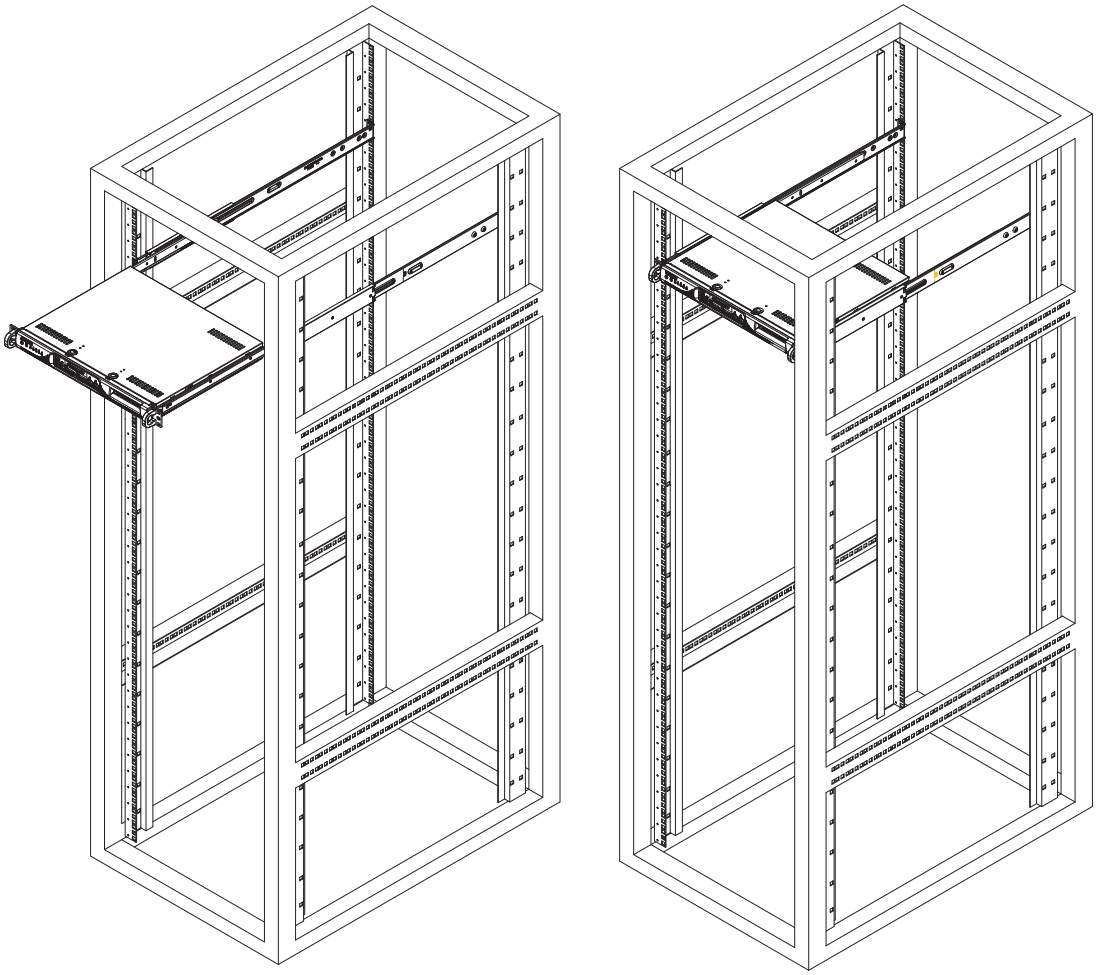
Installing the Server into the Rack

You should now have rails attached to both the chassis and the rack unit. The next step is to install the server into the rack. Do this by lining up the rear of the chassis rails with the front of the rack rails. Slide the chassis rails into the rack rails, keeping the pressure even on both sides (you may have to depress the locking tabs when inserting). The server may not slide in easily when installed for the first time; some adjustment to the rails might be necessary. See Figure 2-3.

When the server has been pushed completely into the rack, you should hear the locking tabs "click".

When removing the server from a rack, you will need to release the locking tabs on both rails to pull the unit completely out of the rack.

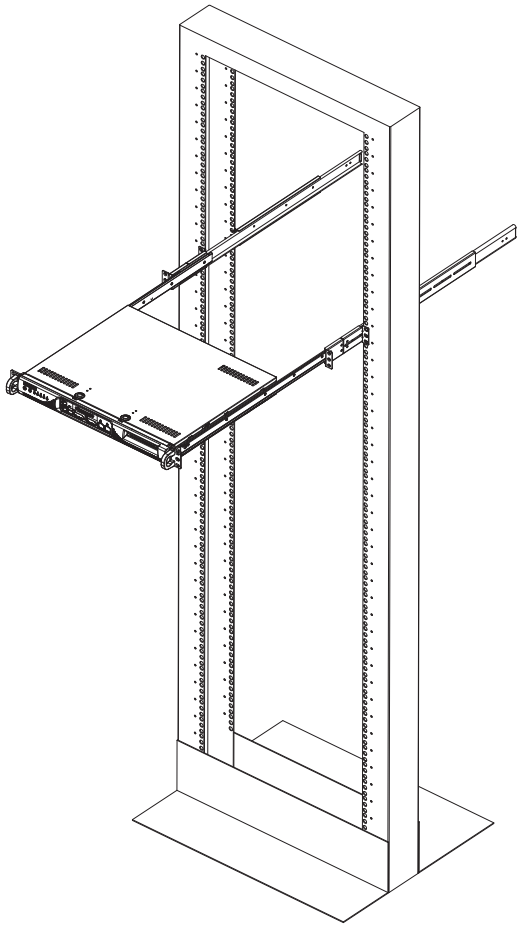
Figure 2-3. Installing the Server into a Rack



Installing the Server into a Telco Rack

To install the SuperServer 5015B-MF into a Telco type rack, use two L-shaped brackets on either side of the chassis (four total). First, determine how far the server will extend out the front of the rack. Larger chassis should be positioned to balance the weight between front and back. If a bezel is included on your server, remove it. Then attach the two front brackets to each side of the chassis, then the two rear brackets positioned with just enough space to accommodate the width of the rack. Finish by sliding the chassis into the rack and tightening the brackets to the rack. See Figure 2-4.

Figure 2-4. Installing the Server into a Telco Rack



2-5 Checking the Motherboard Setup

After you install the 5015B-MF in the rack, you will need to open the unit to make sure the motherboard is properly installed and all the connections have been made.

Accessing the Inside of the System

1. If you installed with rails, grasp the two handles on either side and pull the unit straight out until it locks (you will hear a "click").

2. Remove the screw at the back of the chassis that secures the top cover.

3. Depress the two buttons on the top of the chassis to release the top cover from the locked position. Push the cover away from you until it stops.

4. Lift the top cover from the chassis to gain full access to the inside of the server. See Figure 2-5.

Checking the Components

1. You may have one processor already installed into the system board. The processor should have its own heatsink attached. See Chapter 5 for instructions on processor installation.

2. Your 5015B-MF server system may have come with system memory already installed. Make sure all DIMMs are fully seated in their slots. For details on adding system memory, refer to Chapter 5.

3. If desired, you can install an add-on card to the system. See Chapter 5 for details on installing a PCI add-on card.

4. Make sure all power and data cables are properly connected and not blocking the airflow. See Chapter 5 for details on cable connections. Also, check the air seals for damage. The air seals are located under the blower fan and beneath the frame cross section that separates the drive bay area from the motherboard area of the chassis.

2-6 Checking the Drive Bay Setup

Next, you should check to make sure the peripheral drives and the Serial ATA drives and Serial ATA backplane have been properly installed and all essential connections have been made.

Checking the Drives

1. To access the internal hard drives, you will need to remove the top chassis cover. See step 1 in the previous section for instructions on removing the top chassis cover.

2. Depending upon your system's configuration, your system may have an IDE or Serial ATA drive already installed. If you need to install an IDE or Serial ATA drive, please refer to the appropriate section in Chapter 6.

Checking the Airflow

1. Airflow is provided by a single 10-cm blower fan. The system component layout was carefully designed to promote sufficient airflow through the small 1U rackmount space.

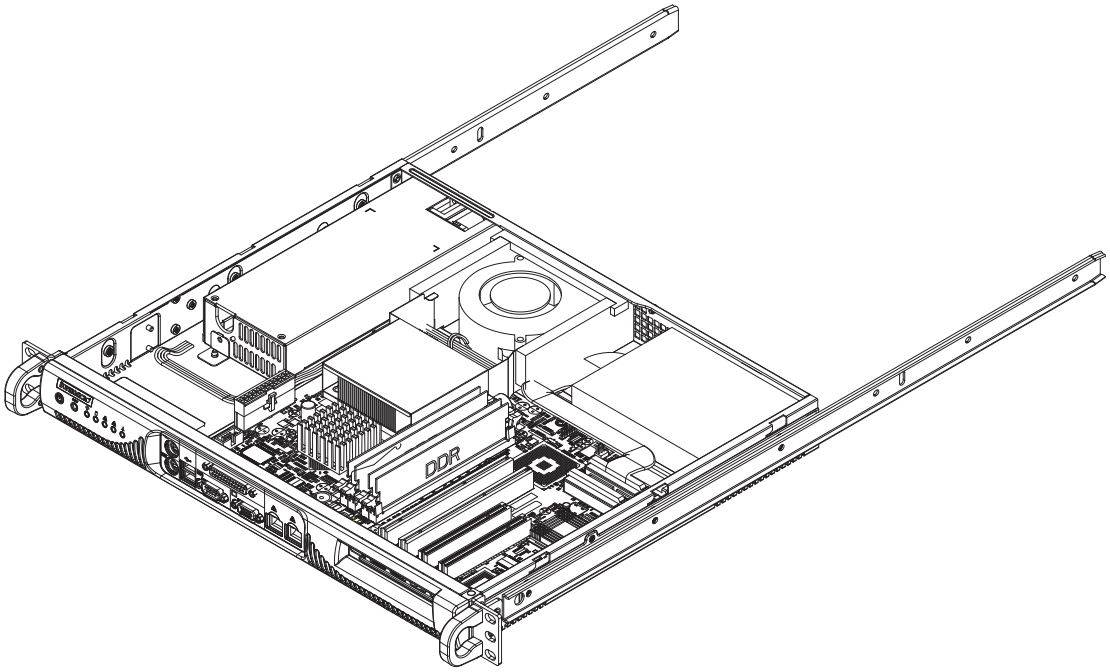
2. Note that all power and data cables have been routed in such a way that they do not block the airflow generated by the fans.

Providing Power

1. Plug the power cord from the power supply unit into a high-quality power strip that offers protection from electrical noise and power surges.

2. It is recommended that you use an uninterruptible power supply (UPS).

Figure 2-5. Accessing the Inside of the System



Chapter 3

System Interface

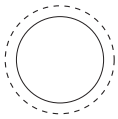
3-1 Overview

There are several LEDs on the control panel to keep you constantly informed of the overall status of the system as well as the activity and health of specific components. There are also two buttons on the chassis control panel. This chapter explains the meanings of all LED indicators and the appropriate response you may need to take.

3-2 Control Panel Buttons

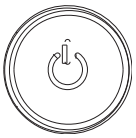
There are two push-buttons located on the front of the chassis. These are (in order from left to right) a reset button and a power on/off button.

RESET



Reset

The reset switch reboots the system.



Power

This is the main power switch, which is used to apply or turn off the main system power. Turning off system power with this button removes the main power but keeps standby power supplied to the system.

3-3 Control Panel LEDs

The control panel located on the front of the chassis has five LEDs. These LEDs provide you with critical information related to different parts of the system. This section explains what each LED indicates when illuminated and any corrective action you may need to take.

Overheat/Fan Fail

When this LED flashes, it indicates a fan failure. When on continuously it indicates an overheat condition, which may be caused by cables obstructing the airflow in the system or the ambient room temperature being too warm. Check the routing of the cables and make sure all fans are present and operating normally. You should also check to make sure that the chassis covers are installed. Finally, verify that the heatsinks are installed properly (see Chapter 5). This LED will remain flashing or on as long as the indicated condition exists.

NIC2

Indicates network activity on GLAN2 when flashing.

NIC1

Indicates network activity on GLAN1 when flashing.

HDD

Channel activity for all hard disk drives. This light indicates SATA drive activity when flashing.

Power

Indicates power is being supplied to the system's power supply unit. This LED should normally be illuminated when the system is operating.

Notes

Chapter 4

System Safety

4-1 Electrical Safety Precautions

Basic electrical safety precautions should be followed to protect yourself from harm and the SuperServer 5015B-MF from damage:

- Be aware of the locations of the power on/off switch on the chassis as well as the room's emergency power-off switch, disconnection switch or electrical outlet. If an electrical accident occurs, you can then quickly remove power from the system.

- Do not work alone when working with high voltage components.

- Power should always be disconnected from the system when removing or installing main system components, such as the serverboard, memory modules and floppy drive. When disconnecting power, you should first power down the system with the operating system first and then unplug the power cords of all the power supply units in the system.

- When working around exposed electrical circuits, another person who is familiar with the power-off controls should be nearby to switch off the power if necessary.

- Use only one hand when working with powered-on electrical equipment. This is to avoid making a complete circuit, which will cause electrical shock. Use extreme caution when using metal tools, which can easily damage any electrical components or circuit boards they come into contact with.

- Do not use mats designed to decrease static electrical discharge as protection from electrical shock. Instead, use rubber mats that have been specifically designed as electrical insulators.

- The power supply power cords must include a grounding plug and must be plugged into grounded electrical outlets.

- This product may be connected to an IT power system. In all cases, make sure that the unit is also reliably connected to Earth (ground).

- Serverboard Battery: **CAUTION** - There is a danger of explosion if the onboard battery is installed upside down, which will reverse its polarites (see Figure 4-1). This battery must be replaced only with the same or an equivalent type recommended by the manufacturer. Dispose of used batteries according to the manufacturer's instructions.

- CD-ROM Laser: **CAUTION** - this server may have come equipped with a CD-ROM drive. To prevent direct exposure to the laser beam and hazardous radiation exposure, do not open the enclosure or use the unit in any unconventional way.

- Mainboard replaceable soldered-in fuses: Self-resetting PTC (Positive Temperature Coefficient) fuses on the mainboard must be replaced by trained service technicians only. The new fuse must be the same or equivalent as the one replaced. Contact technical support for details and support.

4-2 General Safety Precautions

Follow these rules to ensure general safety:

- Keep the area around the 5015B-MF clean and free of clutter.

- The 5015B-MF weighs approximately 16.5 lbs (7.5 kg) when fully loaded. When lifting the system, two people at either end should lift slowly with their feet spread out to distribute the weight. Always keep your back straight and lift with your legs.

- Place the chassis top cover and any system components that have been removed away from the system or on a table so that they won't accidentally be stepped on.

- While working on the system, do not wear loose clothing such as neckties and unbuttoned shirt sleeves, which can come into contact with electrical circuits or be pulled into a cooling fan.

- Remove any jewelry or metal objects from your body, which are excellent metal conductors that can create short circuits and harm you if they come into contact with printed circuit boards or areas where power is present.

- After accessing the inside of the system, close the system back up and secure it to the rack unit with the retention screws after ensuring that all connections have been made.

4-3 ESD Precautions

Electrostatic Discharge (ESD) is generated by two objects with different electrical charges coming into contact with each other. An electrical discharge is created to neutralize this difference, which can damage electronic components and printed circuit boards. The following measures are generally sufficient to neutralize this difference before contact is made to protect your equipment from ESD:

- Use a grounded wrist strap designed to prevent static discharge.

- Keep all components and printed circuit boards (PCBs) in their antistatic bags until ready for use.

- Touch a grounded metal object before removing the board from the antistatic bag.

- Do not let components or PCBs come into contact with your clothing, which may retain a charge even if you are wearing a wrist strap.

- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or contacts.

- When handling chips or modules, avoid touching their pins.

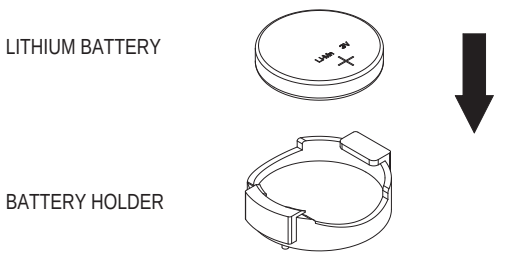
- Put the serverboard and peripherals back into their antistatic bags when not in use.

- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the serverboard.

4-4 Operating Precautions

Care must be taken to assure that the chassis cover is in place when the 5015B-MF is operating to assure proper cooling. Out of warranty damage to the system can occur if this practice is not strictly followed.

Figure 4-1. Installing the Onboard Battery



Please handle used batteries carefully. Do not damage the battery in any way; a damaged battery may release hazardous materials into the environment. Do not discard a used battery in the garbage or a public landfill. Please comply with the regulations set up by your local hazardous waste management agency to dispose of your used battery properly.

Chapter 5

Advanced Serverboard Setup

This chapter covers the steps required to install the X7SBi serverboard into the chassis, connect the data and power cables and install add-on cards. All serverboard jumpers and connections are also described. A layout and quick reference chart are included in this chapter for your reference. Remember to completely close the chassis when you have finished working with the serverboard to better cool and protect the system.

5-1 Handling the Serverboard

Electrostatic discharge (ESD) can damage electronic components. To prevent damage to any printed circuit boards (PCBs), it is important to handle them very carefully (see previous chapter). To prevent the serverboard from bending, keep one hand under the center of the board to support it when handling. The following measures are generally sufficient to protect your equipment from electric static discharge.

Precautions

- Use a grounded wrist strap designed to prevent Electrostatic Discharge (ESD).
- Touch a grounded metal object before removing any board from its antistatic bag.
- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the serverboard, add-on cards and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the serverboard.

Unpacking

The serverboard is shipped in antistatic packaging to avoid electrical static discharge. When unpacking the board, make sure the person handling it is static protected.

5-2 Serverboard Installation

This section explains the first step of physically mounting the X7SBi into the SC513L-260 chassis. Following the steps in the order given will eliminate the most common problems encountered in such an installation. To remove the serverboard, follow the procedure in reverse order.

Installing to the Chassis

1. Access the inside of the system by removing the screws from the back lip of the top cover of the chassis, then pull the cover off.

2. The X7SBi requires a chassis big enough to support a 12" x 9.6" serverboard, such as Supermicro's SC513L-260.

3. Make sure that the I/O ports on the serverboard align properly with their respective holes in the I/O shield at the back of the chassis.

4. Carefully mount the serverboard to the serverboard tray by aligning the board holes with the raised metal standoffs that are visible in the chassis.

5. Insert screws into all the mounting holes on your serverboard that line up with the standoffs and tighten until snug (if you screw them in too tight, you might strip the threads). Metal screws provide an electrical contact to the serverboard ground to provide a continuous ground for the system.

6. Finish by replacing the top cover of the chassis.

5-3 Connecting Cables

Now that the serverboard is installed, the next step is to connect the cables to the board. These include the data cables for the peripherals and control panel and the power cables.

Connecting Data Cables

The cables used to transfer data from the peripheral devices have been carefully routed to prevent them from blocking the flow of cooling air that moves through the system from front to back. If you need to disconnect any of these cables, you should take care to keep them routed as they were originally after reconnecting them (make sure the red wires connect to the pin 1 locations). The following data cables (with their locations noted) should be connected. (See the layout on page 5-10 for connector locations.)

- Control Panel cable (JF1)
- SATA drive data cable (SATA0)

Important! Make sure the the cables do not come into contact with the fans.

Connecting Power Cables

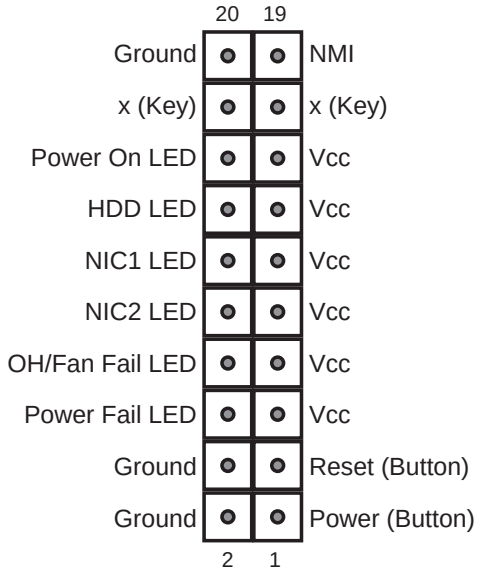
The X7SBi has a 24-pin primary power supply connector (JPW1) for connection to the ATX power supply. In addition, there is an 8-pin processor power connector (JPW2) that must be connected to your power supply. See Section 5-9 for power connector pin definitions.

Connecting the Control Panel

JF1 contains header pins for various front control panel connectors. See Figure 5-1 for the pin locations of the various front control panel buttons and LED indicators.

All JF1 wires have been bundled into a single cable to simplify this connection. Make sure the red wire plugs into pin 1 as marked on the board. The other end connects to the Control Panel PCB board, located just behind the system status LEDs on the chassis. See Chapter 5 for details and pin descriptions.

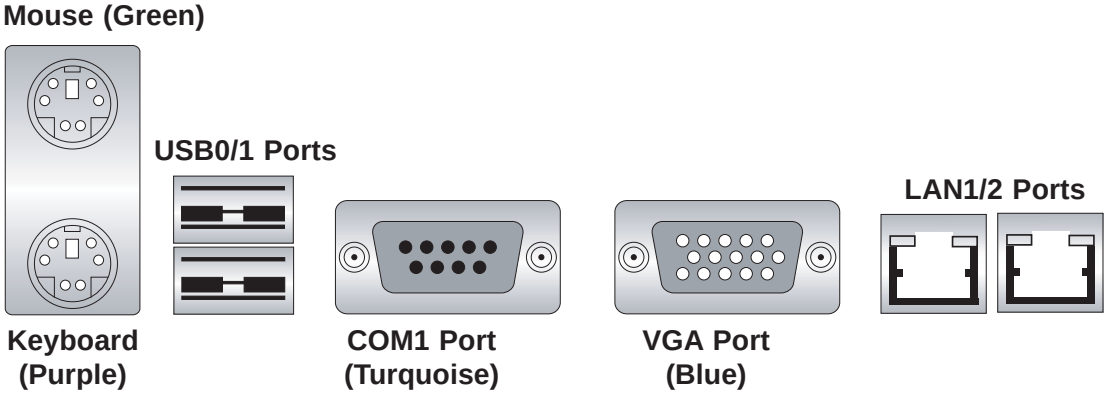
Figure 5-1. Control Panel Header Pins



5-4 I/O Ports

The I/O ports are color coded in conformance with the PC 99 specification. See Figure 5-2 below for the colors and locations of the various I/O ports.

Figure 5-2. I/O Ports



5-5 Installing the Processors and Heatsinks

Avoid placing direct pressure to the top of the processor package. Always remove the power cord first before adding, removing or changing any hardware components.

Notes: Make sure to install the processor into the CPU socket before you install the CPU heatsink.

Intel's boxed Xeon CPU package contains the CPU fan and heatsink assembly. If you buy the CPUs separately, use only Intel-certified heatsinks and fans.

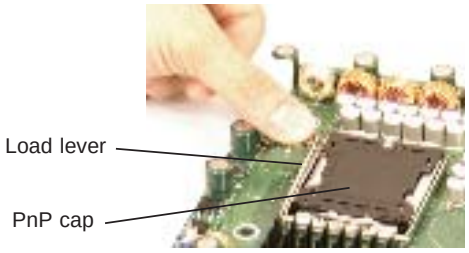
Make sure to install the heatsink backplate and the serverboard into the chassis before you install the CPU heatsink and fan (see below).

Inspect the CPU socket and make sure that the CPU plastic cap is in place and none of the socket pins are bent. Otherwise, contact the retailer immediately.

All graphics shown in this manual are for reference only. The components that came with your serverboard may or may not look exactly the same as the pictures shown in this manual.

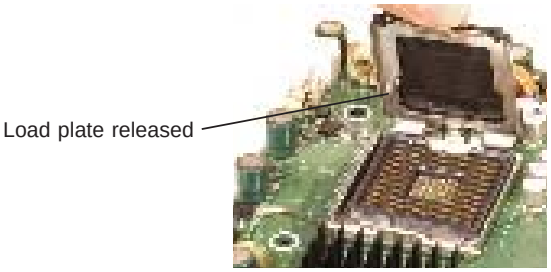
CPU Installation

1. A black PnP cap is attached to the load plate to protect the CPU socket. Press the load lever down and away from the retention clasp to release the load plate from its locked position.



2. Gently lift the load lever to open the load plate.

3. Use your thumb and your index finger to hold the CPU at opposite sides.

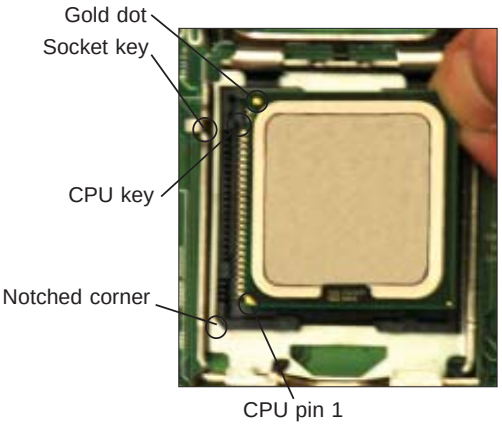


Warning! Make sure you lift the lever completely when installing the CPU; otherwise, damage to the socket or CPU may occur.

4. Align pin1 of the CPU (the corner marked with a triangle) with the notched corner of the CPU socket.

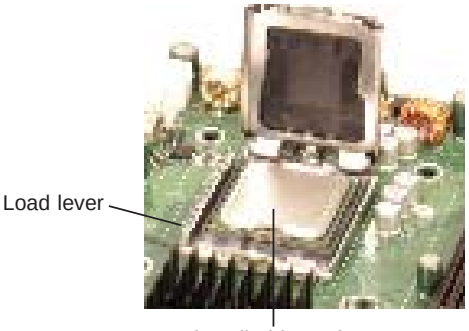
5. Find the corner of the CPU that has a semi-circle cutout below a gold dot (CPU key). This corner should be aligned with the cutout on the socket (socket key).

6. Once aligned, carefully lower the CPU straight down into the socket. Do not drop the CPU on the socket, do not move the CPU horizontally or vertically and do not rub the CPU against any surface or any of the contacts, which may damage the CPU and/or contacts.

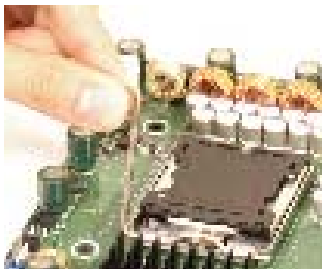


7. With the CPU in the socket, inspect the four corners of the CPU to make sure that it is properly installed.

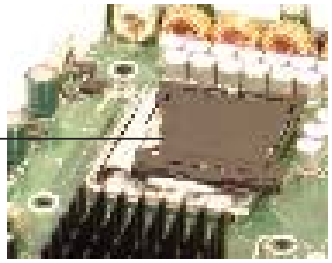
8. Use your thumb to gently push the load lever down until it snaps into the retention clasp.



9. If the CPU is properly installed into the socket, the PnP cap will be automatically released from the load plate when the lever locks. Remove the cap. Repeat steps to install a second CPU if desired.



Warning! Keep the plastic PnP cap. The serverboard must be shipped with the PnP cap properly installed to protect the CPU socket. Shipment without the PnP cap properly installed will void the warranty.



Installation and Removal of the Heatsink

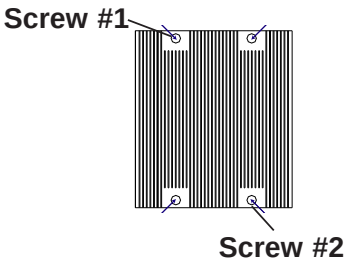
CPU Heatsink Installation

1. Do not apply any thermal grease to the heatsink or the CPU die; the required amount has already been applied.

2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the retention mechanism.

3. Screw in two diagonal screws (i.e. the #1 and the #2 screws) until just snug (do not over-tighten the screws, which may damage the CPU.)

4. Finish the installation by fully tightening all four screws.



Uninstalling the Heatsink

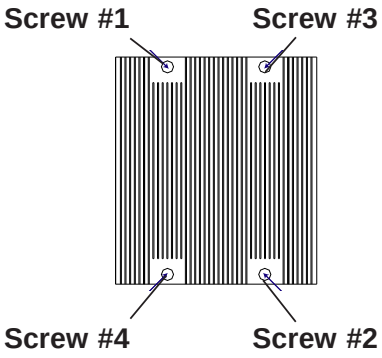
Warning: We do not recommend removing the CPU or the heatsink. However, if you do need to uninstall the heatsink, please follow these instructions to avoid damaging the CPU or the CPU socket.

1. Unscrew and remove the heatsink screws in the sequence shown in the picture on the right.

2. Hold the heatsink as shown in the picture on the right and gently wriggle to loosen it from the CPU. (Do not use excessive force when doing this!)

3. Once the heatsink is loosened, remove it from the CPU socket.

4. Clean the surface of the CPU and the heatsink to get rid of the old thermal grease. Reapply the proper amount of thermal grease before you re-install the heatsink.



5-6 Installing Memory

CAUTION! Exercise extreme care when installing or removing DIMM modules to prevent any possible damage.

Memory Support

The X7SBi supports dual or single channel, ECC/Non-ECC unbuffered DDR2-800/667 SDRAM. Both interleaved and non-interleaved memory are supported, so you may populate any number of DIMM slots. (Populating DIMM#1A/DIMM#2A and/or DIMM#1B/DIMM#2B with memory modules of the same size and type will result in two-way interleaved memory, which is faster than single channel, non-interleaved memory.) Note that when ECC memory is used, it may take 25-40 seconds for the VGA to display.)

Installing Memory Modules

1. Insert each DDR2 memory module vertically into its slot, starting with DIMM #1A. Pay attention to the notch along the bottom of the module to prevent inserting the module incorrectly.

2. Gently press down on the DIMM module until it snaps into place in the slot. Repeat for all modules. (See support information below.)

3. To enhance memory performance, install pairs of memory modules of the same type and of the same, beginning with DIMM #1A and DIMM #2A, then DIMM #1B and DIMM #2B.

Notes

Due to a chipset limitation, 8GB of memory can only be supported by the following operating systems:

- 32-Bit: Windows 2000 Advanced Server, Windows Server 2003 Enterprise Edition;

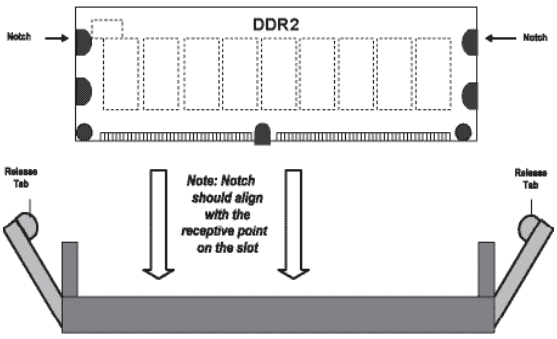
- 64-Bit: Windows Server 2003 Standard x64 Edition, Windows XP Professional x64 Edition, Windows Server 2003 Enterprise x64 Edition

Some old-versions of DDR2-667 may not match Intel's On-Die Temperature requirement and will automatically be downgraded to run at 533 MHz. If this occurs, contact your memory vendor to check the ODT value.

Due to memory allocation to system devices, memory remaining available for operational use will be reduced when 4 GB of RAM is used. The reduction in memory availability is disproportional. (Refer to the Memory Availability Table below for details.)

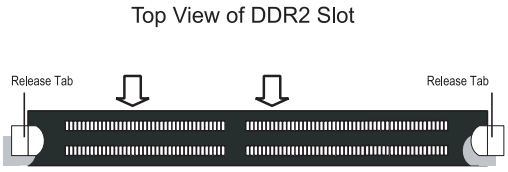
Possible System Memory Allocation & Availability		
System Device	Size	Physical Memory Remaining (4 GB Total System Memory)
Firmware Hub flash memory (System BIOS)	1 MB	3.99
Local APIC	4 KB	3.99
Area Reserved for the chipset	2 MB	3.99
I/O APIC (4 Kbytes)	4 KB	3.99
PCI Enumeration Area 1	256 MB	3.76
PCI Express (256 MB)	256 MB	3.51
PCI Enumeration Area 2 (if needed) -Aligned on 256-MB boundary-	512 MB	3.01
VGA Memory	16 MB	2.85
TSEG	1 MB	2.84
Memory available to System BIOS & OS applications		2.84

Figure 5-3. DIMM Installation



To Install: Insert module vertically and press down until it snaps into place. Pay attention to the bottom notches.

To Remove: Use your thumbs to gently push each release tab outward to free the DIMM from the slot.



5-7 Adding PCI Add-On Cards

The SC513L-260 chassis uses a riser card to accommodate one add-on card (CSE-RR1U-EL for a PCI-E x8 card or CSE-RR1U-X for a PCI-X 3.3V card).

Installing an Add-on Card

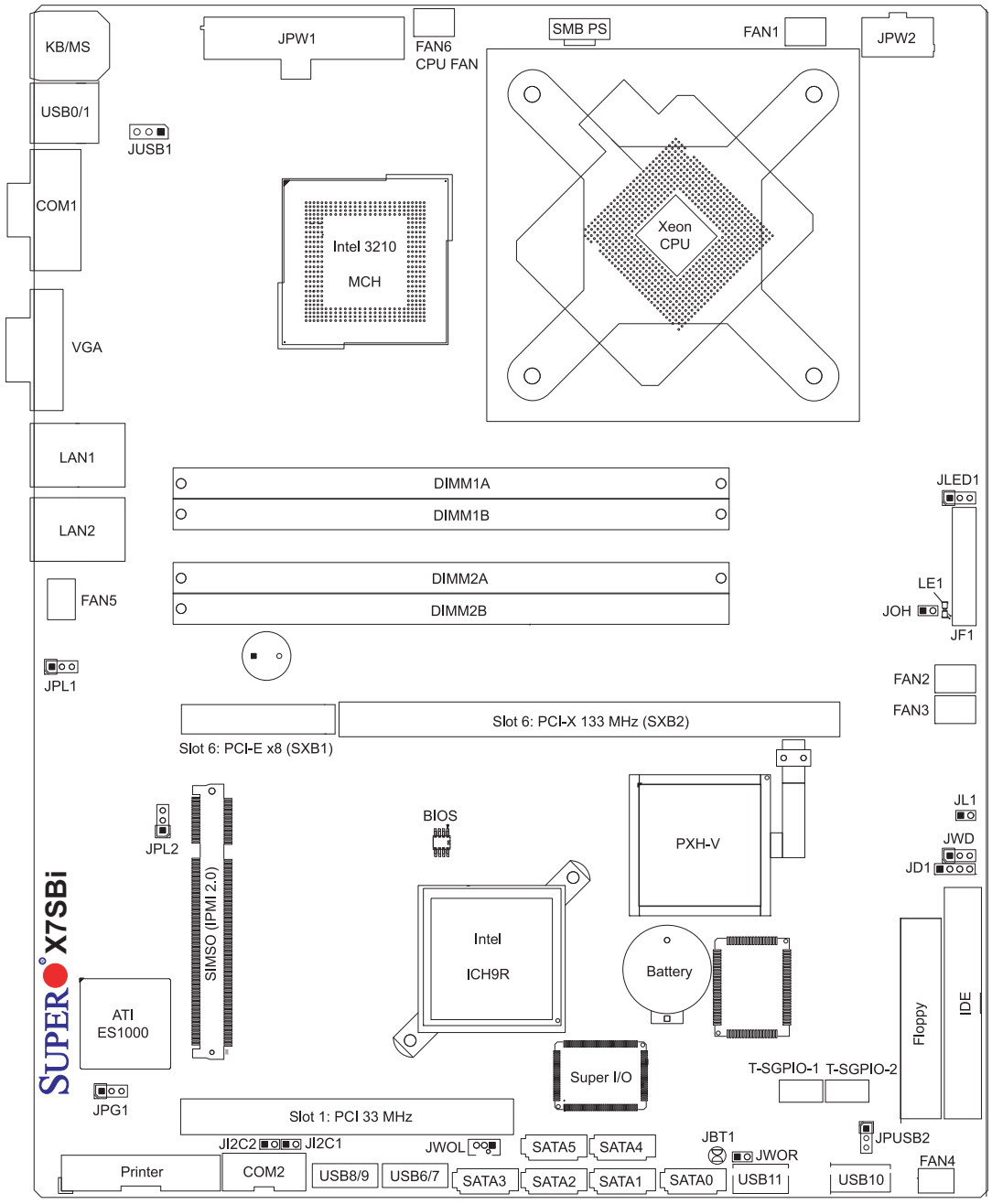
1. After powering down the system, remove the PCI slot shield.

2. Fully seat the riser card into the slot, pushing down with your thumbs evenly on both sides of the card. Seat the expansion card into the riser card.

3. Finish by using a screw to secure the top of the card shield to the chassis. The PCI slot shield protects the serverboard and its components from EMI and aid in proper ventilation, so make sure it is always in place.

5-8 Serverboard Details

Figure 5-4. X7SBi Layout
(not drawn to scale)



Notes

Jumpers not indicated are for testing purposes only.

Slot 6 PCI-Exp. x8 and the PCI-X slots are specially designed for Supermicro's proprietary riser cards only.

X7SBi Quick Reference

Jumper	Description	Default Setting
JBT1	CMOS Clear	(See Section 5-10)
JI²C1/JI²C2	SMB to PCI Slots	Open (Disabled)
JPG1	VGA Enable/Disable	Pins 1-2 (Enabled)
JPL1/JPL2	LAN1/2 Enable/Disable	Pins 1-2 (Enabled)
JPUSB1	Backpanel USB Wake-Up	Pins 1-2 (Enabled)
JPUSB2	Front Access USB Wake-Up	Pins 2-3 (Disabled)
JWD	Watch Dog	Pins 1-2 (Reset)

Connector	Description
COM1/COM2	COM1/COM2 Serial Port/Header
FAN 1-6	Chassis/CPU Fan Headers
Floppy	Floppy Disk Drive Connector
F/P USB6/7, F/P USB8/9	Front Panel Accessible USB Headers
IDE	IDE HDD Connector
JD1	Speaker Header
JF1	Front Panel Connector
JL1	Chassis Intrusion Header
JLED	Power LED Header
JOH	Overheat Warning Header
JPW1	24-pin Main ATX Power Connector
JPW2	+12V 8-pin Secondary Power Connector
JWOL/JWOR	Wake-On-LAN Header/Wake-On-Ring Header
LAN1/2	Gigabit Ethernet (RJ45) Ports
Printer	Parallel (Printer) Port
SATA0 ~ SATA5	SATA Ports
SIMSO	SIMSO IPMI 2.0 Socket
SMB_PS	Power Supply SMBus Connector
T-SGPIO-1/T-SGPIO-2	Serial General Purpose Input/Output Headers
USB0/1	Universal Serial Bus (USB) Ports
USB10, USB11	Onboard Universal Serial Bus (USB) Ports

LED	Description
LE1	Onboard Standby PWR warning LED Indicator

5-9 Connector Definitions

Main ATX Power Supply Connector

The primary power supply connector (JPW1) meets the SSI (Superset ATX) 24-pin specification. Refer to the table on the right for the pin definitions of the ATX 24-pin power connector. You must also connect the 8-pin (JPW2/ JPW3) processor power connectors to your power supply (see below).

ATX Power 24-pin Connector Pin Definitions (JPW1)

Pin#	Definition	Pin #	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res (NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

Secondary Power Connector

JPW2 must also be connected to the power supply. See the table on the right for pin definitions.

+12V 4-pin Power Pin Definitions (JPW2)

Pins	Definition
1 - 4	Ground
5 - 8	+12V

Required Connection

PW_ON Connector

The PW_ON connector is on pins 1 and 2 of JF1. This header should be connected to the chassis power button. See the table on the right for pin definitions.

Power Button Pin Definitions (JF1)

Pin#	Definition
1	PW_ON
2	Ground

Reset Connector

The reset connector is located on pins 3 and 4 of JF1 and attaches to the reset switch on the computer chassis. See the table on the right for pin definitions.

Reset Button Pin Definitions (JF1)

Pin#	Definition
3	Reset
4	Ground

Power Fail LED

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table on the right for pin definitions.

PWR Fail LED Pin Definitions (JF1)

Pin#	Definition
5	Vcc
6	Ground

Overheat/Fan Fail LED (OH)

Connect an LED to the OH connection on pins 7 and 8 of JF1 to provide advanced warning of chassis overheating. Refer to the table on the right for pin definitions.

OH/Fan Fail LED Pin Definitions (JF1)	
Pin#	Definition
7	Vcc
8	Ground

OH/Fan Fail Indicator Status	
State	Definition
Off	Normal
On	Overheat
Flash-ing	Fan Fail

NIC2 (JLAN2) LED

The LED connections for JLAN2 are on pins 9 and 10 of JF1. Attach an LED cable to display network activity. See the table on the right for pin definitions.

NIC2 LED Pin Definitions (JF1)	
Pin#	Definition
9	Vcc
10	Ground

NIC1 (JLAN1) LED

The LED connections for JLAN1 are on pins 11 and 12 of JF1. Attach an LED cable to display network activity. See the table on the right for pin definitions.

NIC1 LED Pin Definitions (JF1)	
Pin#	Definition
11	Vcc
12	Ground

HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. This LED is used to display all IDE and SATA activity. See the table on the right for pin definitions.

HDD LED Pin Definitions (JF1)	
Pin#	Definition
13	Vcc
14	HD Active

Power On LED

The Power On LED connector is located on pins 15 and 16 of JF1 (use JLED for a 3-pin connector). This connection is used to provide LED indication of power being supplied to the system. See the table on the right for pin definitions.

Power LED Pin Definitions (JF1)	
Pin#	Definition
15	5V Stby
16	Control

NMI Button

The non-maskable interrupt button header is located on pins 19 and 20 of JF1. Refer to the table on the right for pin definitions.

NMI Button Pin Definitions (JF1)

Pin#	Definition
19	Control
20	Ground

Fan Headers

There are six fan headers on the serverboard, all of which are 4-pin fans. However, pins 1-3 of the fan headers are backward compatible with the traditional 3-pin fans. See the table on the right for pin definitions. The onboard fan speeds are controlled by Thermal Management (via Hardware Monitoring) under the Advanced Section in the BIOS. The default is disabled. When using Thermal Management setting, please use all 3-pin fans or all 4-pin fans.

Fan Header Pin Definitions (FAN1-8)

Pin#	Definition
1	Ground (Black)
2	+12V (Red)
3	Tachometer
4	PWM Control

Note: Fan 6 is the header for the CPU heatsink fan.

ATX PS/2 Keyboard and PS/2 Mouse Ports

The ATX PS/2 keyboard and the PS/2 mouse are located beside the USB0/1 ports. The mouse port is above the keyboard port. See the table on the right for pin definitions.

PS/2 Keyboard and Mouse Port Pin Definitions (J28)

Pin#	Definition
1	Data
2	NC
3	Ground
4	VCC
5	Clock
6	NC

Serial Ports

Two serial ports are included on the serverboard. COM1 is a backpanel port and COM2 is a header located beside the printer port. See the table on the right for pin definitions.

Serial Port Pin Definitions (COM1/COM2)

Pin #	Definition	Pin #	Definition
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	Ground	10	NC

Chassis Intrusion

The Chassis Intrusion header is designated JL1. Attach an appropriate cable from the chassis to inform you of a chassis intrusion when the chassis is opened

Chassis Intrusion Pin Definitions (JL1)	
Pin#	Definition
1	Intrusion Input
2	Ground

Wake-On-LAN

The Wake-On-LAN header is designated JWOL on the serverboard. See the table on the right for pin definitions. You must also have a LAN card with a Wake-On-LAN connector and cable to use this feature.

Wake-On-LAN Pin Definitions (JWOL)	
Pin#	Definition
1	+5V Standby
2	Ground
3	Wake-up

Wake-On-Ring

The Wake-On-Ring header is designated JWOR. This function allows your computer to receive and be "awakened" by an incoming call when in the suspend state. See the table on the right for pin definitions. You must also have a WOR card and cable to use this feature.

Wake-On-Ring Pin Definitions (JWOR)	
Pin#	Definition
1	Ground (Black)
2	Wake-up

External Speaker/Internal Buzzer

On the JD1 header, pins 1-4 are for an external speaker and pins 3-4 are for the internal speaker. If you wish to use an external speaker, connect it to pins 1-4 to. If you wish to use the onboard speaker, you should close pins 3-4 with a jumper.

Speaker Connector (JD1)	
Pin Setting	Definition
Pins 3-4	Internal Speaker
Pins 1-4	External Speaker

LAN1/2 (Ethernet Ports)

Two Ethernet ports (designated LAN1 and LAN2) are located beside the VGA port on the I/O backplane. These ports accept RJ45 type cables.

Universal Serial Bus (USB)

There are two Universal Serial Bus ports located on the I/O panel and four additional USB headers located on the serverboard. The headers can be used to provide front side USB access (cables not included). See the table on the right for pin definitions.

Universal Serial Bus Pin Definitions (USB)

USB0/1/10/11		USB6/7/8/9	
Pin #	Definition	Pin #	Definition
1	+5V	1	+5V
2	PO-	2	PO-
3	PO+	3	PO+
4	Ground	4	Ground
5	N/A	5	Key

SGPIO Headers

The SGPIO (Serial General Purpose Input/Output) headers are used to communicate with a system-monitoring chip on the backplane. See the table on the right for pin definitions.

SGPIO Header Pin Definitions (T-SGPIO-1/T-SGPIO-2)

Pin#	Definition	Pin	Definition
1	*NC	2	*NC
3	Ground	4	DATA Out
5	Load	6	Ground
7	Clock	8	*NC

NC = No Connection

Power SMBUS Header

A Power SMB header is located at SMBUS_PS. Connect the appropriate cable here to utilize SMB on your system. See the table on the right for pin definitions.

Power SMB Header Pin Definitions (PW4)

Pin#	Definition
1	Clock
2	Data
3	PWR Fail
4	Ground
5	+3.3V

Overheat LED/Fan Fail (JOH1)

The JOH1 header is used to connect an LED to provide warning of chassis overheating. This LED will blink to indicate a fan failure. Refer to the table on right for pin definitions.

OH/Fan Fail LED States

State	Message
Solid	Overheat
Blinking	Fan Fail

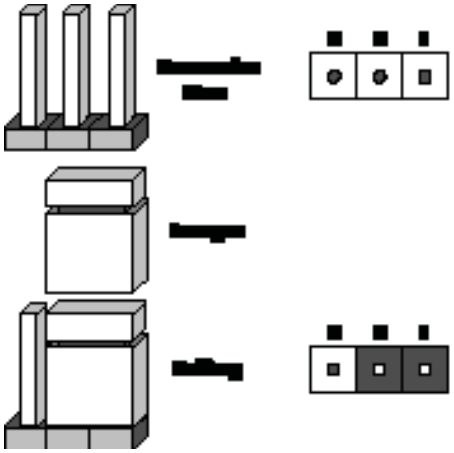
Overheat LED Pin Definitions (JOH1)

Pin#	Definition
1	5vDC
2	OH Active

5-10 Jumper Settings

Explanation of Jumpers

To modify the operation of the serverboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the serverboard layout pages for jumper locations.



Note: On a two-pin jumper, "Closed" means the jumper is on both pins and "Open" means the jumper is either on only one pin or completely removed.

CMOS Clear

JBT1 is used to clear CMOS (which will also clear any passwords). Instead of pins, this jumper consists of contact pads to prevent accidentally clearing the contents of CMOS.

To clear CMOS,

1. First power down the system and unplug the power cord(s).
2. With the power disconnected, short the CMOS pads with a metal object such as a small screwdriver.
3. Remove the screwdriver (or shorting device).
4. Reconnect the power cord(s) and power on the system.

Note: *Do not use the PW_ON connector to clear CMOS*.

VGA Enable/Disable

JPG1 allows you to enable or disable the VGA port. The default position is on pins 1 and 2 to enable VGA. See the table on the right for jumper settings.

VGA Enable/Disable Jumper Settings (JPG1)	
Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

LAN1/2 Enable/Disable

Change the setting of jumper JPL1 and JPL2 to enable or disable the LAN1 and LAN2 Ethernets ports, respectively. See the table on the right for jumper settings. The default setting is enabled.

LAN1/2 En/Disable Jumper Settings (JPL1/JPL2)

Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

Watch Dog Enable/Disable

JWD controls the Watch Dog function. Watch Dog is a system monitor that can reboot the system when a software application hangs. Jumping pins 1-2 will cause WD to reset the system if an application hangs. Jumping pins 2-3 will generate a non-maskable interrupt signal for the application that hangs. See the table on the right for jumper settings. Watch Dog must also be enabled in BIOS.

Note: When enabled, the user needs to write their own application software in order to disable the Watch Dog Timer.

Watch Dog Jumper Settings (JWD)

Jumper Setting	Definition
Pins 1-2	Reset
Pins 2-3	NMI
Open	Disabled

USB Wake-Up

These jumpers allow you to wake up the system by pressing a key on the USB keyboard or by clicking the USB mouse of your system. The JPUSB jumpers are used together with the USB Wake-Up feature in BIOS and both must be enabled to use this feature. See the table on the right for jumper settings. **Notes:** JPUSB1 is for the USB0/1 ports and JPUSB2 is for USB6/7/8/9/10/11.

Default settings are enabled for JPUSB1 and disabled for JPUSB2. Please be sure to remove all other USB devices from the USB ports whose USB jumpers are set to Disabled before the system goes into standby mode.

USB Wake-Up Jumper Settings

Jumper Setting	Definition
Pins 1-2	Enabled
Pins 2-3	Disabled

SMBus to PCI-X/PCI-Exp. Slots

Jumpers JI²C1 and JI²C2 allow you to connect the System Management Bus (I²C) to the PCI-X/PCI-E slots. The default setting is Open (Disabled.) Both jumpers must be set to the same setting See the table on the right for jumper settings.

SMBus to PCI-X/PCI-E Slots Jumper Settings (JI²C1/JI²C2)

Jumper Setting	Definition	
JI²C1: Closed	JI²C2:Closed	Enabled
JI²C1: Open	JI²C2: Open	Disabled

5-11 Onboard Indicators

LAN1/2 LEDs

The Ethernet ports (located beside the VGA port) have two LEDs. On each port, one LED indicates activity while the other LED may be green, amber or off to indicate the speed of the connection. See the table on the right for the functions associated with the connection speed LED.

GLAN1/2 LED (Connection Speed Indicator)

LED Color	Definition
Off	10 Mb/s
Green	100 Mb/s
Amber	1 Gb/s

Onboard Power LED (LE1)

An Onboard Power LED is located at LE1. This LED Indicator is lit when the system is on. Be sure to unplug the power cable before removing or adding any components. See the table on the right for more details.

Onboard Power LED Indicator (LE1)

LED Color	Definition
Off	System Off
Green	System on

5-12 Floppy, SATA, IPMI, IDE and Printer Ports

Floppy Drive Connector

The floppy connector is located behind the mouse/keyboard ports. See the table at right for pin definitions.

Floppy Drive Connector Pin Definitions (Floppy)

Pin#	Definition	Pin #	Definition
1	Ground	2	FDHDIN
3	Ground	4	Reserved
5	Key	6	FDEDIN
7	Ground	8	Index
9	Ground	10	Motor Enable
11	Ground	12	Drive Select B
13	Ground	14	Drive Select B
15	Ground	16	Motor Enable
17	Ground	18	DIR
19	Ground	20	STEP
21	Ground	22	Write Data
23	Ground	24	Write Gate
25	Ground	26	Track 00
27	Ground	28	Write Protect
29	Ground	30	Read Data
31	Ground	32	Side 1 Select
33	Ground	34	Diskette

SATA Ports

There are no jumpers to configure the onboard SATA ports. See the table on the right for pin definitions.

SATA Port Pin Definitions (SATA0~SATA5)

Pin #	Definition
1	Ground
2	TXP
3	TXN
4	Ground
5	RXN
6	RXP
7	Ground

SIMSO

A SIMSO IPMI (Intelligent Platform Management Interface) 2.0 Socket is included on the motherboard.

IDE Connector

An IDE connector is included on the motherboard. See the table on the right pin definitions.

IDE Drive Connectors
Pin Definitions (IDE)

Pin#	Definition	Pin #	Definition
1	Reset IDE	2	Ground
3	Host Data 7	4	Host Data 8
5	Host Data 6	6	Host Data 9
7	Host Data 5	8	Host Data 10
9	Host Data 4	10	Host Data 11
11	Host Data 3	12	Host Data 12
13	Host Data 2	14	Host Data 13
15	Host Data 1	16	Host Data 14
17	Host Data 0	18	Host Data 15
19	Ground	20	Key
21	DRQ3	22	Ground
23	I/O Write	24	Ground
25	I/O Read	26	Ground
27	IOCHRDY	28	BALE
29	DACK3	30	Ground
31	IRQ14	32	IOCS16
33	Addr1	34	Ground
35	Addr0	36	Addr2
37	Chip DDR2 0	38	Chip DDR2 1
39	Activity	40	Ground

Parallel (Printer) Port

The parallel port is located next to COM2 header. See the table on the right for pin definitions.

Parallel Port Connector
Pin Definitions (Printer)

Pin#	Definition	Pin #	Definition
1	Strobe-	2	Auto Feed-
3	Data Bit 0	4	Error-
5	Data Bit 1	6	Init-
7	Data Bit 2	8	SLCT IN-
9	Data Bit 3	10	GND
11	Data Bit 4	12	GND
13	Data Bit 5	14	GND
15	Data Bit 6	16	GND
17	Data Bit 7	18	GND
19	ACK	20	GND
21	BUSY	22	Write Data
23	___	24	Write Gate
25	SLCT	26	NC

Notes

Chapter 6

Advanced Chassis Setup

This chapter covers the steps required to install components and perform maintenance on the SC513L-260 chassis. For component installation, follow the steps in the order given to eliminate the most common problems encountered. If a step is unnecessary, skip ahead to the step that follows.

Tools Required: The only tool you will need to install components and perform maintainance is a Philips screwdriver.

6-1 Static-Sensitive Devices

Electrostatic Discharge (ESD) can damage electronic components. To prevent damage to any printed circuit boards (PCBs), it is important to handle them very carefully. The following measures are generally sufficient to protect your equipment from ESD discharge.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing any board from its antistatic bag.
- Handle a board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard, add-on cards and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure the person handling it is static protected.

Figure 6-1. Chassis Front View

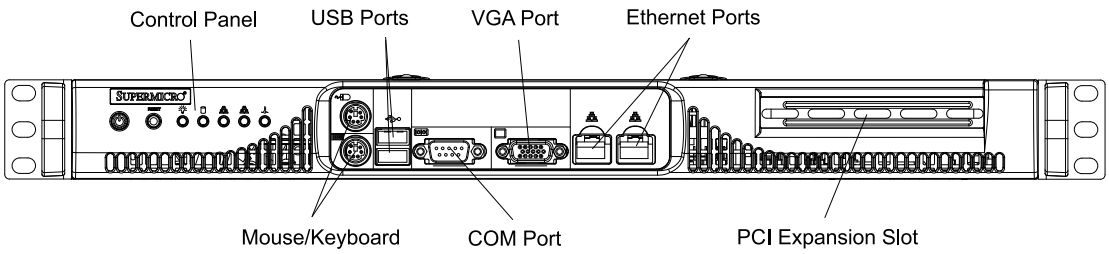
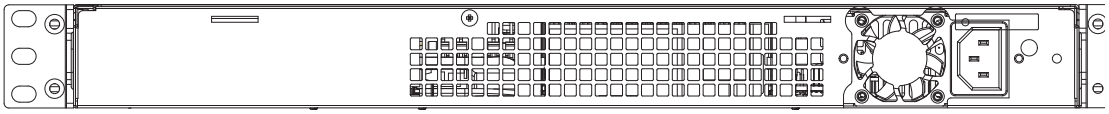


Figure 6-2. Chassis Rear View



6-2 Control Panel

The control panel (located on the front of the chassis) must be connected to the JF1 connector on the motherboard to provide you with system control buttons and status indicators. These wires have been bundled together in a ribbon cable to simplify the connection. Connect the cable from JF1 on the motherboard to the Control Panel PCB (printed circuit board). Make sure the red wire plugs into pin 1 on both connections. Pull all excess cabling out of the airflow path. The LEDs inform you of system status. See Chapter 3 for details on the LEDs and the control panel buttons. Details on JF1 can be found in Chapter 5.

6-3 System Fan

A 10-cm blower fan provides the cooling for the SuperServer 5015B-MF. The chassis includes air seals under the blower fan and at the chassis cross section, which separates the drive bay area from the motherboard area of the chassis to promote better airflow. It is highly important that the air seal is properly installed and making a good seal in order for the cooling air to circulate properly through the chassis.

6-4 Drive Bay Installation/Removal

Accessing the Drive Bay

Hard Drive: The hard drive in the 5015B-MF is not hot-swappable. The system must be shutdown and power removed when the hard drive is installed removed. You must also first remove the cover to access the inside of the system. Proceed to the next section for instructions.

CD-ROM/Floppy Disk Drives: There is no floppy drive or CD-ROM drive included in the 5015B-MF.

The fan can adjust its speed according to the heat level sensed in the system, which results in more efficient and quieter fan operation. Fan speed is controlled by a setting in BIOS (see page 7-11).

System Fan Failure

If the fan fails, you will need to have it replaced with the same type. Contact your vendor or Supermicro for information on replacement fans.

Serial ATA Drive Installation

Accessing the Inside of the System

1. First, release the retention screws that secure the unit to the rack. If you installed with rails, grasp the two handles on either side and pull the unit straight out until it locks (you will hear a "click").

2. Remove the screw at the back of the chassis that secures the top cover.

3. Depress the two buttons on the top of the chassis to release the top cover from the locked position. Push the cover away from you until it stops.

4. Lift the top cover from the chassis to gain full access to the inside of the server. See Figure 2-5.

Installing/Removing a Serial ATA Drive

The hard drive is mounted in a tray at the right rear area of the server. This tray is quipped with rubber grommets to reduce vibration (Figure 6-3, #1).

1. Remove the screws that secure the hard drive to the chassis and then lift it of the standoffs, as shown in Figure 6-4.

2. To remove a hard drive, unscrew the screws that secure it to the tray (Figure 6-3, #3) and then remove the drive.

3. To add a drive, position it in the tray with the printed circuit board side facing down and with the holes in the drive and the holes in the tray aligned. You can install it in either the top or the bottom half of the tray.

4. Secure the drive to the tray with four screws (two on each side), as shown in Figure 6-3.

5. After you have finished installing the hard drive, replace the top cover on the chassis, push the server back into the rack and restore the power.

Figure 6-3. Internal Hard Drive Bay

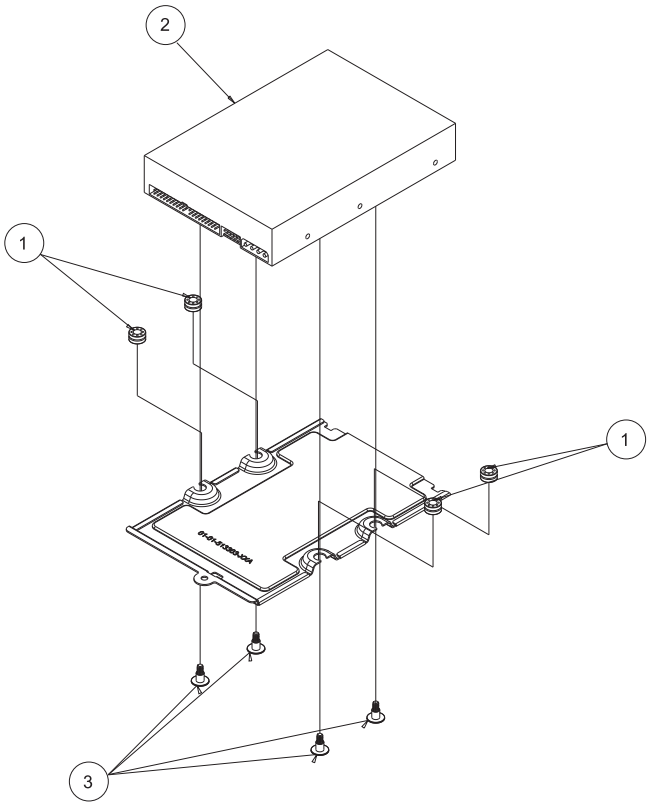
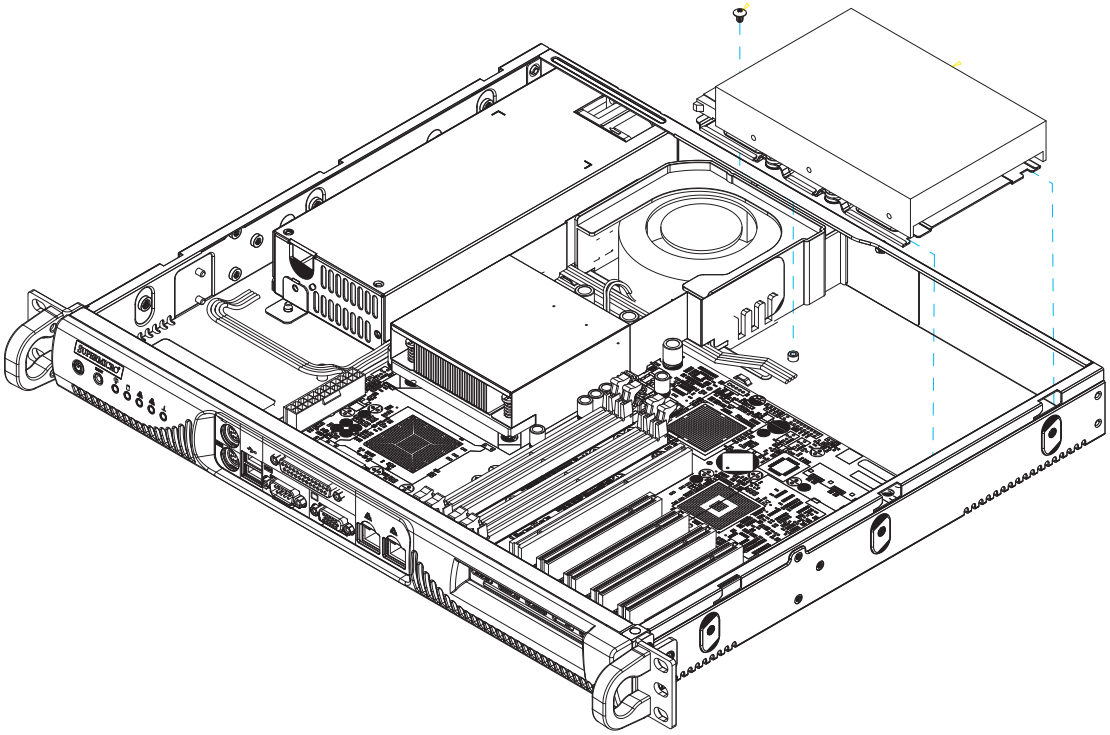


Figure 6-4. Removing/Installing the Hard Drive



6-5 Power Supply

The SuperServer 5015B-MF has a single 260 watt power supply. This power supply has the capability of operating at 100 - 240 input volts. Shutdown the system and then unplug the AC power cord to completely remove power from the system before removing the power supply.

Power Supply Failure

If the power supply unit fails, the system will shut down and you will need to replace the power supply unit. Replacement units can be ordered directly from Supermicro (PWS-0055 - see contact infomation in Chapter 1).

Replacing the Power Supply

1. To access the inside of the chassis, follow the procedure as described on page 6-3.

2. Shut down the system and unplug the power cord.

3. To remove the failed power unit, remove the two screws on the back of the power supply, which secure it to the chassis. You can then lift the unit straight out of the chassis.

4. Replace the failed unit with another unit of the same wattage. It is highly recommended to replace it with the exact same power supply. Carefully insert the new unit into position in the chassis and secure it with the two screws at the rear of the unit.

5. Before reconnecting the power cord, make sure the power switch on the power supply is in the off position. Then reconnect the power cord, replace the chassis top cover and push the unit back into the rack.

6. Finish by depressing the power button on the front of the system.

Chapter 7

BIOS

7-1 Introduction

This chapter describes the Phoenix BIOS™ Setup utility for the X7SBi. The Phoenix ROM BIOS is stored in a flash chip and can be easily upgraded using a floppy disk-based program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of the Supermicro web site <http://www.supermicro.com> for any changes to the BIOS that may not be reflected in this manual.

System BIOS

BIOS is the Basic Input Output System used in all IBM® PC, XT™, AT®, and PS/2® compatible computers. The Phoenix BIOS stores the system parameters, types of disk drives, video displays, etc. in the CMOS. The CMOS memory requires very little electrical power. When the computer is turned off, a backup battery provides power to the CMOS logic, enabling it to retain system parameters. Each time the computer is powered on the computer is configured with the values stored in the CMOS logic by the system BIOS, which gains control at boot up.

How To Change the Configuration Data

The CMOS information that determines the system parameters may be changed by entering the BIOS Setup utility. This Setup utility can be accessed by pressing the <Delete> key at the appropriate time during system boot. (See below.)

Starting the Setup Utility

Normally, the only visible POST (Power On Self Test) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the BIOS Setup utility. From the main menu, you can access the other setup screens, such as the Security and Power menus. Beginning with Section 7-3, detailed descriptions are given for each parameter setting in the Setup utility.

Warning: Do not shut down or reset the system while updating BIOS to prevent possible boot failure.

Note: The SPI BIOS chip used in the X7SBi is not removable. To replace a damaged SPI BIOS chip, please send the motherboard to Supermicro for repair.

7-2 Running Setup

Default settings are in bold text unless otherwise noted.

The BIOS setup options described in this section are selected by choosing the appropriate text from the main BIOS Setup screen. All displayed text is described in this section, although the screen display is often all you need to understand how to set the options (See the next page).

When you first power on the computer, the Phoenix BIOS™ is immediately activated.

While the BIOS is in control, the Setup program can be activated in one of two ways:

1. By pressing <Delete> immediately after turning the system on, or
2. When the message shown below appears briefly at the bottom of the screen during the POST (Power On Self-Test), press the <Delete> key to activate the main Setup menu:

Press the <Delete> key to enter Setup

7-3 Main BIOS Setup

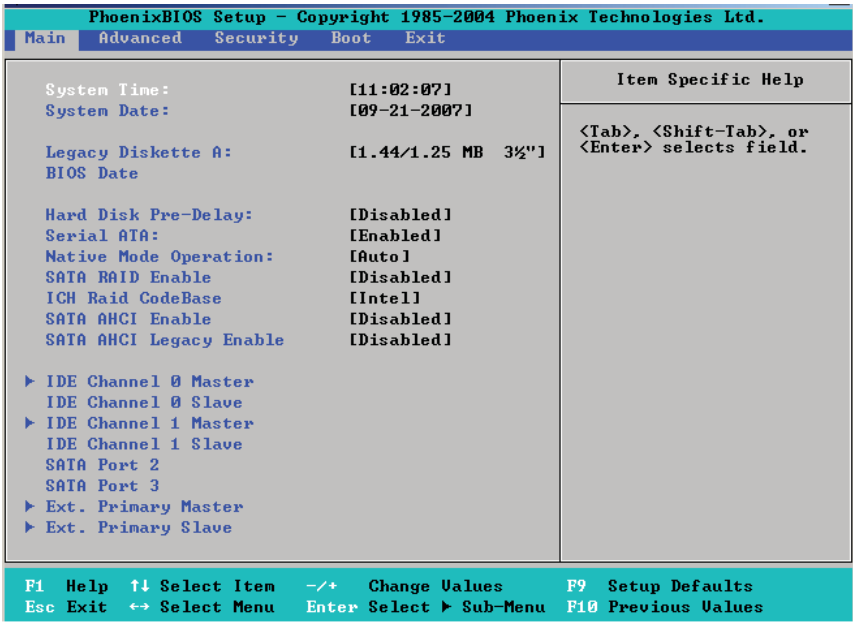
All main Setup options are described in this section. The main BIOS Setup screen is displayed below.

Use the Up/Down arrow keys to move among the different settings in each menu. Use the +/- arrow keys to change the options for each setting.

Press the <Esc> key to exit the CMOS Setup Menu. The next section describes in detail how to navigate through the menus.

Items that use submenus are indicated with the ▶icon. With the item highlighted, press the <Enter> key to access the submenu.

Main BIOS Setup Menu



Main Setup Features

System Time

To set the system date and time, key in the correct information in the appropriate fields. Then press the <Enter> key to save the data.

System Date

Using the arrow keys, highlight the month, day and year fields, and enter the correct data. Press the <Enter> key to save the data.

Legacy Diskette A

This setting allows the user to set the type of floppy disk drive installed as diskette A. The options are Disabled, 360Kb 5.25 in, 1.2MB 5.25 in, 720Kb 3.5 in, **1.44/1.25MB**, 3.5 in and 2.88MB 3.5 in.

BIOS Date

The item displays the date that the BIOS was built.

Hard Disk Pre-Delay

When Enabled, this feature will add a delay to provide time need for HDD self-initialization before the HDD is accessed by the BIOS for the first time. Some HDDs will hang if accessed by the BIOS without proper initialization. The options are Enabled and **Disabled.**

Serial ATA

This setting allows the user to enable or disable the function of Serial ATA. The options are Disabled and **Enabled.**

Native Mode Operation

Select Serial ATA to use the SATA mode, or select Auto to use the Native Mode for ATA. The options are: Serial ATA and **Auto**.

Serial ATA (SATA) RAID Enable

Select Enable to enable Serial ATA RAID Functions. (For the Windows OS environment, use the RAID driver if this feature is set to Enabled. When this item is set to Enabled, the item: ICH RAID Code Base will be available for you to select either Intel or Adaptec Host RAID firmware to be activated. If this item is set to **Disabled**, the item-SATA AHCI Enable will be available.) The options are Enabled and **Disabled**.

ICH RAID Code Base

Select Intel to enable the Intel SATA RAID firmware. Select Adaptec to use the Adaptec HostRAID firmware. The options are **Intel** and Adaptec**.**

SATA AHCI

Select Enable to enable the function of Serial ATA Advanced Host Interface. (Take caution when using this function. This feature is for advanced programmers only. The Enhanced AHCI mode is available when the Windows XP-SP1 OS and the IAA Driver is used.) The options are Enabled and **Disabled**.

SATA AHCI Legacy

Select Enable to use Legacy Mode for SATA Advanced Host Interfacing. When this feature is set to Enabled, SATA Port 5 and SATA Port 6 are disabled. (Take caution when using this function. This feature is for advanced programmers only.) The options are Enabled and **Disabled**.

▶IDE Channel 0 Master/Slave, IDE Channel 1 Master/Slave, SATA Port 2, SATA Port 3

These settings allow the user to set the parameters of IDE Channel 0 Master/ Slave, IDE Channel 1 Master/Slave, SATA Port 2 and SATA Port3 connections. Hit <Enter> to activate the following sub-menu screen for detailed options of these items. Set the correct configurations accordingly. The items included in the sub-menu are:

Type

This option allows the user to select the type of an IDE/SATA device. Select **Auto** to allow the BIOS to automatically configure the parameters of the IDE/SATA device installed on a slot. Enter a number between 1 to 39 to select a predetermined IDE/SATA device. Select User to allow the user to enter the parameters of a HDD/SATA device installed. Select CDROM if a CDROM drive is installed. Select ATAPI if a removable disk drive is installed.

CHS Format

The following items will be displayed by the BIOS:

TYPE: This item displays the type of IDE drive.

Cylinders: This item indicates the number of cylinders detected by the BIOS.

Headers: This item indicates the number of headers.

Sectors: This item displays the number of sectors.

Maximum Capacity: This item displays the maximum storage capacity in the CHS Format.

LBA Format

The following items will be displayed by the BIOS:

Total Sectors: This item displays the number of total sectors available in the LBA Format.

Maximum Capacity: This item displays the maximum capacity in the LBA Format.

Multi-Sector Transfers

This item allows the user to specify the number of sectors per block to be used in multi-sector transfer. The options are **Disabled**, 4 Sectors, 8 Sectors, and 16 Sectors.

LBA Mode Control

This item determines whether the Phoenix BIOS will access a IDE Device via the LBA mode. The options are Enabled and **Disabled.**

32 Bit I/O

This option allows the user to enable or disable the function of 32-bit data transfer. The options are Enabled and **Disabled**.

Transfer Mode

This option allows the user to set the transfer mode. The options are **Standard**, Fast PIO1, Fast PIO2, Fast PIO3, Fast PIO4, FPIO3/DMA1 and FPIO4/DMA2.

Ultra DMA Mode

This option allows the user to configure the Ultra DMA Mode setting. The options are **Disabled**, Mode 0, Mode 1, Mode 2, Mode 3, Mode 4, and Mode 5.

▶Extended Primary Master/Slave

These settings allow the user to set the parameters of Extended Primary Master/ Slave connections. Hit <Enter> to activate the following sub-menu screen for detailed options of these items. Set the correct configurations accordingly. The items included in the sub-menu are:

Type

This option allows the user to select the type of an IDE device. Select **Auto** to allow the BIOS to automatically configure the parameters of the IDE device installed on a slot. Enter a number between 1 to 39 to select a predetermined IDE device. Select User to allow the user to enter the parameters of the HDD installed. Select CDROM if a CDROM drive is installed. Select ATAPI if a removable disk drive is installed.

LBA Format

The following items will be displayed by the BIOS:

Total Sectors: This item displays the number of total sectors available in the LBA Format.

Maximum Capacity: This item displays the maximum capacity in the LBA Format.

Multi-Sector Transfers

This item allows the user to specify the number of sectors per block to be used in multi-sector transfer. The options are **Disabled**, 4 Sectors, 8 Sectors, and 16 Sectors.

LBA Mode Control

This item determines whether the Phoenix BIOS will access a IDE Device via the LBA mode. The options are Enabled and **Disabled.**

32 Bit I/O

This option allows the user to enable or disable the function of 32-bit data transfer. The options are Enabled and **Disabled**.

Transfer Mode

This option allows the user to set the transfer mode. The options are **Standard**, Fast PIO1, Fast PIO2, Fast PIO3, Fast PIO4, FPIO3/DMA1 and FPIO4/DMA2.

Ultra DMA Mode

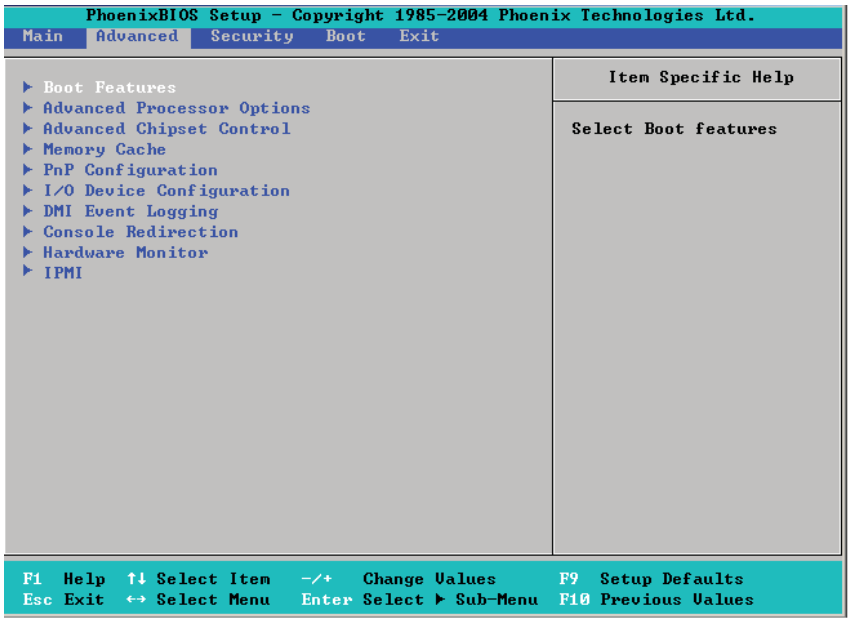
This option allows the user to configure the Ultra DMA Mode setting. The options are **Disabled**, Mode 0, Mode 1, Mode 2, Mode 3, Mode 4, and Mode 5.

System Memory

This display informs you how much system memory is detected in the system.

7-4 Advanced Setup

Choose Advanced from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. The items with a triangle beside them have sub menus that can be accessed by highlighting the item and pressing <Enter>.



▶Boot Features

Access the submenu to make changes to the following settings.

Quiet Mode

This setting allows you to **Enable** or Disable the graphic logo screen during boot-up.

QuickBoot Mode

If enabled, this feature will speed up the POST (Power On Self Test) routine by skipping certain tests after the computer is turned on. The settings are **Enabled**

ACPI Mode

Select Yes to **use** the ACPI (Advanced Configuration and Power Interface) power management feature on your system. The options are **Yes** and No.

Power Button Behavior

If set to **Instant-Off**, the system will power on or power off immediately as soon as the user hits the power button. The options are **Instant-Off** and 4-Second Override.

Resume On Modem Ring

Select On to "wake your system up" when an incoming call is received by your modem. The options are On and **Off**.

Resume On PME#

Select On to "wake your system up" from the PME# of PCI slots. The options are **On** and Off.

PS2 Keyboard (KB)/Mouse Wake Up

Select Enable to "wake your system up" from the S1, S4 or S5 state. If this feature is set to Enabled, you will also need to enable the JPWAKE jumper by closing pins 1-2. (Please refer to Pg. 1-5 and Chapter 2 for more details). The default setting is **Disabled**.

Power Loss Control

This setting allows you to choose how the system will react when power returns after an unexpected loss of power. The options are Stay Off, Power On, and **Last State**.

Watch Dog

If enabled, this option will automatically reset the system if the system is not active for more than 5 minutes. The options are Enabled and **Disabled**.

Summary Screen

This setting allows you to **Enable** or Disable the summary screen which displays the system configuration during bootup.

▶Advanced Processor Options

Access the submenu to make changes to the following settings.

CPU Speed

This is a display that indicates the speed of the installed processor.

Frequency Ratio (Available when supported by the CPU.)

The feature allows the user to set the internal frequency multiplier for the CPU. The default setting is **Default**.

Frequency High Ratio (Available when supported by the CPU.)

The feature allows the user to set high ratio internal frequency multiplier for Intel SpeedStep CPUs. The default setting is **x12**.

Note: If a wrong ratio that is not supported by the CPU is selected, the system may hang. If this happens, clear CMOS to recover the system.)

Hyperthreading (Available when supported by the CPU.)

Set to Enabled to use the Hyperthreading Technology, which will result in increased CPU performance. The options are Disabled and **Enabled.**

Core-Multi-Processing (Available when supported by the CPU.)

Set to Enabled to use a processor's Second Core and beyond. (Please refer to Intel's web site for more information.) The options are Disabled and **Enabled.**

Machine Checking (Available when supported by the CPU.)

Set to Enabled to activate the function of Machine Checking and allow the CPU to detect and report hardware (machine) errors via a set of model-specific registers (MSRs). The options are **Enabled** and Disabled.

Compatible FPU Code (Available when supported by the CPU.)

Set to Enabled to keep the content of the last instruction Operating Code (OP Code) in the floating point (FP) state. The options are **Disabled** and Enabled.

L3 Cache(Available when supported by the CPU.)

Set to Enabled to enable the function of L3 Cache to optimize system and CPU performance. The options are Disabled and **Enabled**.

Thermal Management 2 (Available when supported by the CPU.)

Set to **Enabled** to use Thermal Management 2 (TM2) which will lower CPU voltage and frequency when the CPU temperature reaches a predefined overheat threshold. Set to Disabled to use Thermal Manager 1 (TM1), allowing CPU clocking to be regulated via CPU Internal Clock modulation when the CPU temperature reaches the overheat threshold.

Adjacent Cache Line Prefetch (Available when supported by the CPU.)

The CPU fetches the cache line for 64 bytes if this option is set to Disabled. The CPU fetches both cache lines for 128 bytes as comprised if Enabled. The options are Disabled and **Enabled**.

Set Maximum Ext. CPUID=3

When set to Enabled, the Maximum Extended CPUID will be set to 3. The options are **Disabled** and Enabled.

Echo TPR

Set to **Enabled** to prevent xTPR messages from being sent to the system.The options are Disabled and **Enabled**.

C1 Enhanced Mode (Available when supported by the CPU.)

Set to Enabled to enable Enhanced Halt State to lower CPU voltage/frequency to prevent overheat. The options are Enabled and **Disabled**. **Note:** please refer to Intel's web site for detailed information.

Intel <R> Virtualization Technology (Available when supported by the CPU.)

Select Enabled to use the feature of Virtualization Technology to allow one platform to run multiple operating systems and applications in independent partitions, creating multiple "virtual" systems in one physical computer. The options are Enabled and **Disabled**. **Note**: If there is any change to this setting, you will need to power off and restart the system for the change to take effect. Please refer to Intel's web site for detailed information.

No Execute Mode Memory Protection (Available when supported by the CPU and the OS.)

Set to Enabled to enable Execute Disable Bit and allow the processor to classify areas in memory where an application code can execute and where it cannot, and thus preventing a worm or a virus from inserting and creating a flood of codes to overwhelm the processor or damage the system during an attack.

Note: this feature is available when your OS and your CPU support the function of Execute Disable Bit. The options are Disabled and **Enabled**. **Note**: For more information regarding hardware/software support for this function, please refer to Intel's and Microsoft's web sites.

Enhanced Intel Speed Step Support (Available when supported by the CPU.)

Select Enabled to use the Enhanced Intel SpeedStep Technology and allows the system to automatically adjust processor voltage and core frequency in an effort to reduce power consumption and heat dissipation. The default setting is **GV1/GV3**. Please refer to Intel's web site for detailed information.

▶Advanced Chipset Control

Access the submenu to make changes to the following settings.

Warning: Take Caution when changing the Advanced settings. An incorrect value, a very high DRAM frequency, or an incorrect DRAM timing may cause the system to become unstable. When this occurs, reset the setting to the default setting.

Memory Reclaiming

Select Enable to enable the functionality of Memory Remapping above 4GB. The settings are **Enabled** and Disabled.

Enable VT-d

Select Enable to enable the functionality of the Intel Virtualization Technology for Direct I/O support, which offers fully-protected I/O resource-sharing across the Intel platforms, providing the user with greater reliability, security and availability in networking and data-sharing. The settings are Enabled and **Disabled**.

High Precision Event Time

Select Yes to activate the High Precision Event Timer (HPET), which is capable of producing periodic interrupts at a much higher frequency than a Real-time Clock (RTC) can in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in a CPU. The High Precision Event Timer is used to replace the 8254 Programmable Interval Timer. The options for this feature are Yes and **No**.

Route Port 80h Cycles to

This feature allows the user to decide which bus to send debug information to. The options are Disabled, **PCI** and LPC.

Legacy USB Support

This setting allows you to enable support for Legacy USB devices. The settings are **Enabled** and Disabled.

USB Host Controller 1

This feature allows the user to configure the USB Host Controller setting for USB Host Controller 1 Functions 0, 1, 2, 3, 7. The settings are **Enabled** and Disabled.

USB Host Controller 2

This feature allows the user to configure the USB Host Controller setting for USB Host Controller 2 Functions 0, 1, 2, 7. The settings are **Enabled** and Disabled.

▶Memory Cache

Cache System BIOS Area

This setting allows you to designate a reserve area in the system memory to be used as a System BIOS buffer to allow the BIOS to write (cache) data into this reserved memory area. Select **Write Protect** to enable this function, and this area will be reserved for BIOS ROM access only. Select Uncached to disable this function and make this area available for other devices.

Cache Video BIOS Area

This setting allows you to designate a reserve area in the system memory to be used as a Video BIOS buffer to allow the BIOS to write (cache) data into this reserved memory area. Select **Write Protect** to enable the function and this area will be reserved for Video BIOS ROM access only. Select Uncached to disable this function and make this area available for other devices.

Cache Base 0-512K

If enabled, this feature will allow the data stored in the base memory area: block 0-512K to be cached (written) into a buffer, a storage area in the Static DROM (SDROM) or to be written into the L1, L2 cache inside the CPU to speed up CPU operations . Select Uncached to disable this function. Select Write Through to allow data to be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the base memory area of Block 0-512K. Select Write Back to allow the CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are Uncached, Write Through, Write Protect, and **Write Back**.

Cache Base 512K-640K

If enabled, this feature will allow the data stored in the memory area: 512K-640K to be cached (written) into a buffer, a storage area in the Static DROM (SDROM) or written into the L1, L2, L3 cache inside the CPU to speed up CPU operations. Select Uncached to disable this function. Select Write Through to allow data to be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the base memory area of Block 512K-640K. Select Write Back to allow the CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are Uncached, Write Through, Write Protect, and **Write Back**.

Cache Extended Memory

If enabled, this feature will allow the data stored in the extended memory area to be cached (written) into a buffer, a storage area in the Static DROM (SDROM) or written into the L1, L2, L3 cache inside the CPU to speed up CPU operations. Select Uncached to disable this function. Select Write Through to allow data to be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the extended memory area above 1 MB. Select Write Back to allow the CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are Uncached, Write Through, Write Protect, and **Write Back**.

▶PnP Configuration

Access the submenu to make changes to the following settings for PCI devices.

▶PCI 32 Slot

Access the submenu for each of the settings above to make changes to the following:

Option ROM Scan

When enabled, this setting will initialize the device expansion ROM. The options are **Enabled** and Disabled.

Enable Master

This setting allows you to enable the selected device as the PCI bus master. The options are Enabled and **Disabled**.

Latency Timer

This setting allows you to set the clock rate for Bus Master. A high-priority, high-throughout device may benefit from a greater clock rate. The options are **Default**, 0020h, 0040h, 0060h, 0080h, 00A0h, 00C0h, and 00E0h. For Unix, Novelle and other Operating Systems, please select the option: other. If a drive fails after the installation of a new software, you might want to change this setting and try again. A different OS requires a different Bus Master clock rate**.**

PCI-X(s) Frequency

When set to **Enabled,** this feature allows the user to set the bus frequency for a PCI-X slot for it to work properly. The default setting is **Auto**.

▶PCI-X 133 MHz Slot

Access the submenu for each of the settings above to make changes to the following:

Option ROM Scan

When enabled, this setting will initialize the device expansion ROM. The options are **Enabled** and Disabled.

Enable Master

This setting allows you to enable the selected device as the PCI bus master. The options are Enabled and **Disabled**.

Latency Timer

This setting allows you to set the clock rate for Bus Master. A high-priority, high-throughout device may benefit from a greater clock rate. The options are **Default**, 0020h, 0040h, 0060h, 0080h, 00A0h, 00C0h, and 00E0h. For Unix, Novelle and other Operating Systems, please select the option: other. If a drive fails after the installation of a new software, you might want to change this setting and try again. A different OS requires a different Bus Master clock rate**.**

▶PCI-Exp. x8 Slot

Access the submenu for each of the settings above to make changes to the following:

Option ROM Scan

When enabled, this setting will initialize the device expansion ROM. The options are **Enabled** and Disabled.

Enable Master

This setting allows you to enable the selected device as the PCI bus master. The options are Enabled and **Disabled**.

Latency Timer

This setting allows you to set the clock rate for Bus Master. A high-priority, high-throughout device may benefit from a greater clock rate. The options are **Default**, 0020h, 0040h, 0060h, 0080h, 00A0h, 00C0h, and 00E0h. For Unix, Novelle and other Operating Systems, please select the option: other. If a drive fails after the installation of a new software, you might want to change this setting and try again. A different OS requires a different Bus Master clock rate.

▶Onboard LAN 1/Onboard LAN 2

Access the submenu for each of the settings above to make changes to the following:

Option ROM Scan

When enabled, this setting will initialize the device expansion ROM. The options are **Enabled** and Disabled.

Enable Master

This setting allows you to enable the selected device as the PCI bus master. The options are **Enabled** and Disabled.

Latency Timer

This setting allows you to set the clock rate for Bus Master. A high-priority, high-throughout device may benefit from a greater clock rate. The options are **Default**, 0020h, 0040h, 0060h, 0080h, 00A0h, 00C0h, and 00E0h. For Unix, Novelle and other Operating Systems, please select the option: other. If a drive fails after the installation of a new software, you might want to change this setting and try again. A different OS requires a different Bus Master clock rate**.**

▶I/O Device Configuration

Access the submenu to make changes to the following settings.

KBC Clock Input

This setting allows you to select clock frequency for the keyboard clock. The options are 6MHz, 8MHz, **12MHz,** and 16MHz.

Serial Port A

This setting allows you to assign control of Serial Port A. The options are **Enabled** (user defined), Disabled, and Auto (BIOS- or OS- controlled).

Base I/O Address

This setting allows you to select the base I/O address for Serial Port A. The options are **3F8**, 2F8, 3E8, and 2E8.

Interrupt

This setting allows you to select the IRQ (interrupt request) for Serial Port A. The options are IRQ3 and **IRQ4**.

Serial Port B

This setting allows you to assign control of Serial Port B. The options are **Enabled** (user defined), Disabled, Auto (BIOS controlled) and OS Controlled.

Mode

This setting allows you to set the type of device that will be connected to Serial Port B. The options are **Normal** and IR (for an infrared device).

Base I/O Address

This setting allows you to select the base I/O address for Serial Port B. The options are 3F8, **2F8**, 3E8 and 2E8.

Interrupt

This setting allows you to select the IRQ (interrupt request) for Serial Port B. The options are **IRQ3** and IRQ4.

Parallel Port

This setting allows you to assign control of the parallel port. The options are **Enabled** (user defined), Disabled and Auto (BIOS-or OS- controlled).

Base I/O Address

Select the base I/O address for the parallel port. The options are **378**, 278 and 3BC.

Interrupt

This setting allows you to select the IRQ (interrupt request) for the parallel port. The options are IRQ5 and **IRQ7**.

Mode

This feature allows you to specify the parallel port mode. The options are Output only, Bi-Directional, EPP and **ECP**.

DMA Channel

This item allows you to specify the DMA channel for the parallel port. The options are DMA1 and **DMA3**.

Floppy Disk Controller

This setting allows you to assign control of the floppy disk controller. The options are **Enabled** (user defined), Disabled, Auto (BIOS and OS controlled) and OS-Controlled.

▶DMI Event Logging

Access the submenu to make changes to the following settings.

Event Log Validity

This is a display to inform you of the event log validity. It is not a setting.

Event Log Capacity

This is a display to inform you of the event log capacity. It is not a setting.

View DMI Event Log

Highlight this item and press <Enter> to view the contents of the event log.

Event Logging

This setting allows you to **Enable** or Disable event logging.

Mark DMI Events as Read

Highlight this item and press <Enter> to mark the DMI events as read.

Clear All DMI Event Logs

Select Yes and press <Enter> to clear all DMI event logs. The options are Yes and **No**.

▶Console Redirection

Access the submenu to make changes to the following settings.

COM Port Address

This item allows you to specify which COM port to direct the remote console to: Onboard COM A or Onboard COM B. This setting can also be **Disabled**.

BAUD Rate

This item allows you to set the BAUD rate for the console redirection. The options are 300, 1200, 2400, 9600, **19.2K**, 38.4K, 57.6K, and 115.2K.

Console Type

This item allows you to set the console redirection type. The options are VT100, VT100/8bit, PC-ANSI/7bit, **PC ANSI**, VT100+, VT-UTF8 and ASCII.

Flow Control

This item allows you to select the flow control option for the console. The options are: None, XON/XOFF, and **CTS/RTS**.

Console Connection

This item allows you to decide how console redirection is to be connected: either **Direct** or Via Modem.

Continue CR after POST

This feature allows you to decide if you want to continue with console redirection after the POST routine. The options are On and **Off**.

▶Hardware Monitoring

CPU Temperature

CPU Overheat Temperature

This option indicates the CPU temperature overheat threshold that will activate the alarm system when the CPU temperature reaches this pre-set temperature threshold. The options are **75°C,** 80°C, 85°C, and 90°C **(Note)**

Highlight this and hit <Enter> to see monitor data for the following items:

CPU Temperature

System Temperature

CPU Fan/Fan 1 to Fan 6

If the feature of Auto Fan Control is enabled, the BIOS will automatically display the status of each fan as specified.

Fan Speed Control Modes

This feature allows the user to decide how the system controls the speeds of the onboard fans. The CPU temperature and the fan speed are correlative. When the CPU on-die temperature increases, the fan speed will also increase, and vice versa. If the option is set to 3-pin fan, the fan speed is controlled by voltage. If the option is set to 4-pin, the fan speed will be controlled by Pulse Width Modulation (PWM). Select 3-pin if your chassis came with 3-pin fan headers. Select 4-pin if your chassis came with 4-pin fan headers. Select Workstation if your system is used as a Workstation. Select Server if your system is used as a Server. Select Disable to disable the fan speed control function to allow the onboard fans to constantly run at the full speed (12V). The Options are: **1. Disable**, 2. Optimized Server w/3-pin, 3. Optimized Workstation w/3-pin, 4. Optimized Server w/4-pin and 5. Optimized Workstation w/4-pin.

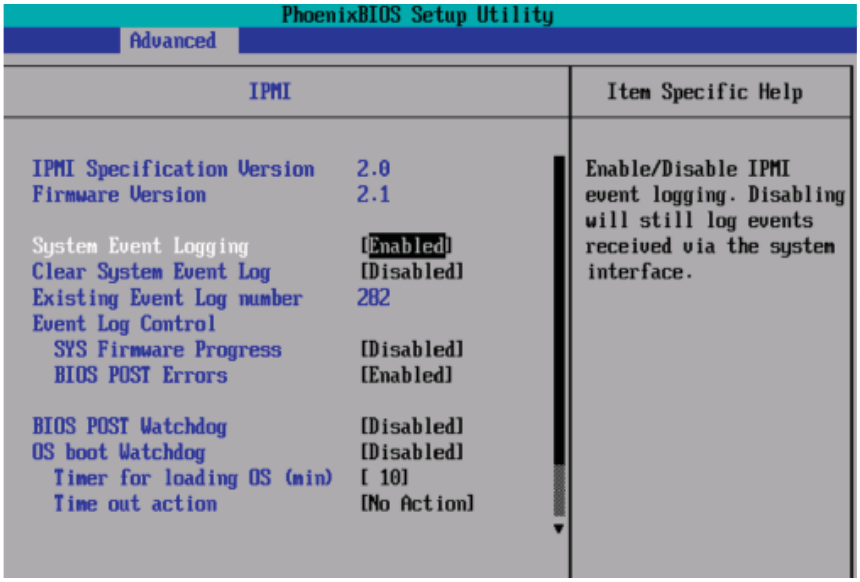
Voltage Monitoring

The following items will be monitored and displayed:

Vcore A, MCH Core, -12V, VDIMM, +3.3V, +12V, +5Vsb, +5VDD, P_VTT, Vbat

Note: In the Windows OS environment, the Supero Doctor III settings take precedence over the BIOS settings. When first installed, Supero Doctor III adopts the temperature threshold settings previously set in the BIOS. Any subsequent changes to these thresholds must be made within Supero Doctor, since the SD III settings override the BIOS settings. For the Windows OS to adopt the BIOS temperature threshold settings, please change the SDIII settings to be the same as those set in the BIOS.

▶IPMI (The option is available only when an IPMI card is installed in the system.)



IPMI Specification Version: This item displays the current IPMI Version.

Firmware Version: This item displays the current Firmware Version.

System Event Logging

Select Enabled to enable IPMI Event Logging. When this function is set to Disabled, the system will continue to log events received via system interface. The options are **Enabled** and Disabled.

Clear System Event Logging

Enabling this function to force the BIOS to clear the system event logs during the next cold boot. The options are Enabled and **Disabled**.

Existing Event Log Number

This item displays the number of the existing event log.

Event Log Control

System Firmware Progress

Enabling this function to log POST progress. The options are Enabled and **Disabled**.

BIOS POST Errors

Enabling this function to log POST errors. The options are Enabled and **Disabled**.

BIOS POST Watch Dog

Set to Enabled to enable POST Watch Dog. The options are Enabled and **Disabled**.

OS Boot Watch Dog

Set to Enabled to enable OS Boot Watch Dog. The options are Enabled and **Disabled**.

Timer for Loading OS (Minutes)

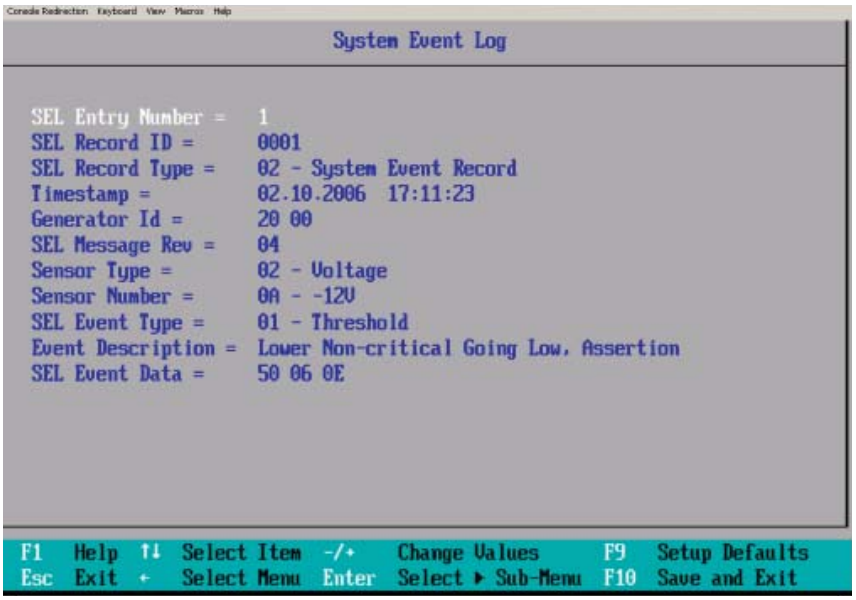
This feature allows the user to set the time value (in minutes) for the previous item: OS Boot Watch Dog by keying-in a desired number in the blank. The default setting is 10 (minutes.) (Please ignore this option when OS Boot Watch Dog is set to "Disabled".)

Time Out Option

This feature allows the user to determine what action to take in an event of a system boot failure. The options are **No Action,** Reset, Power Off and Power Cycles.

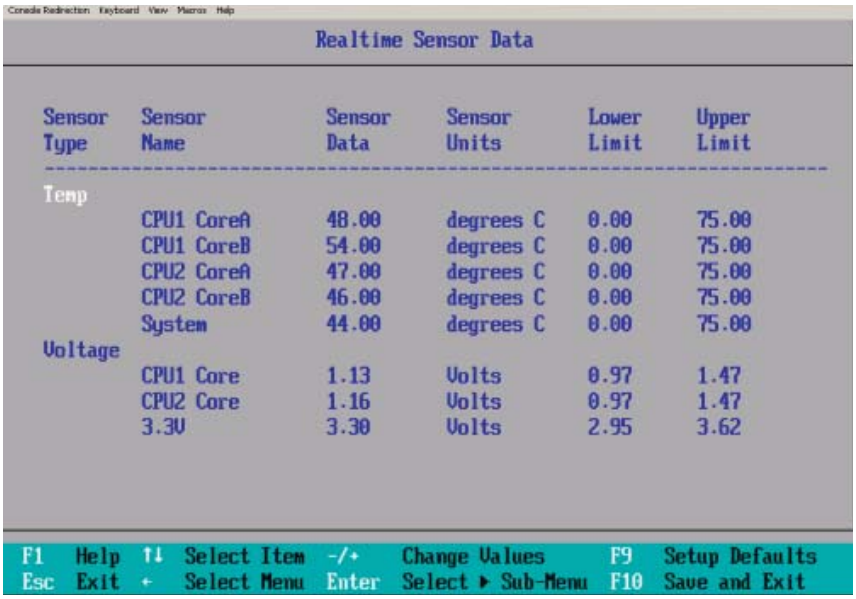
▶System Event Log/System Event Log (List Mode)

These options display the System Event (SEL) Log and System Event (SEL) Log in List Mode. Items include: SEL (System Event Log) Entry Number, SEL Record ID, SEL Record Type, Time Stamp, Generator ID, SEL Message Revision, Sensor Type, Sensor Number, SEL Event Type, Event Description, and SEL Event Data.



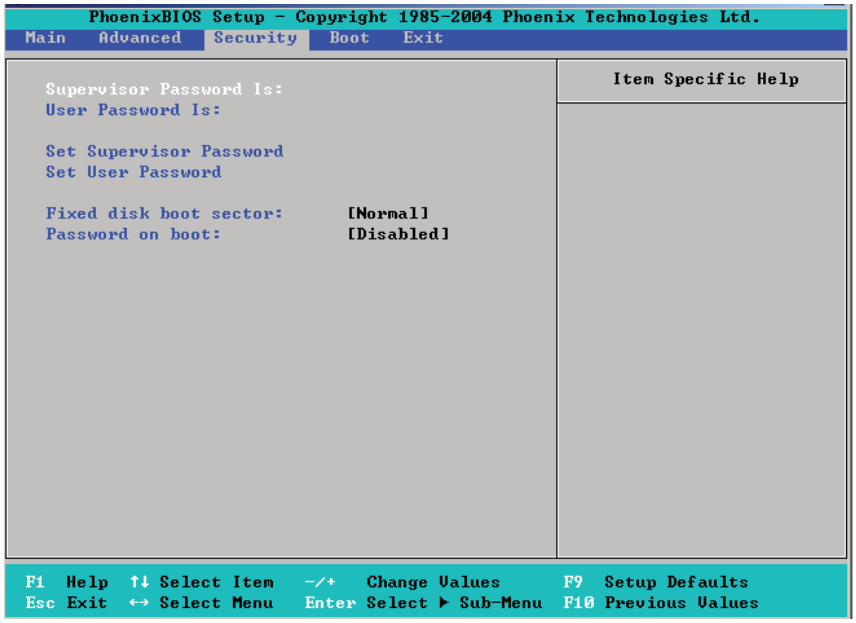
▶Realtime Sensor Data

This feature display information from motherboard sensors, such as temperatures, fan speeds and voltages of various components.



7-5 Security Settings

Choose Security from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. Security setting options are displayed by highlighting the setting using the arrow keys and pressing <Enter>. All Security BIOS settings are described in this section.



Supervisor Password Is:

This item indicates if a supervisor password has been entered for the system. Clear means such a password has not been used and Set means a supervisor password has been entered for the system.

User Password Is:

This item indicates if a user password has been entered for the system. Clear means such a password has not been used and Set means a user password has been entered for the system.

Set Supervisor Password

When the item "Set Supervisor Password" is highlighted, hit the <Enter> key. When prompted, type the Supervisor's password in the dialogue box to set or to change supervisor's password, which allows access to the BIOS.

Set User Password

When the item "Set User Password" is highlighted, hit the <Enter> key. When prompted, type the user's password in the dialogue box to set or to change the user's password, which allows access to the system at boot-up.

Fixed Disk Boot Sector

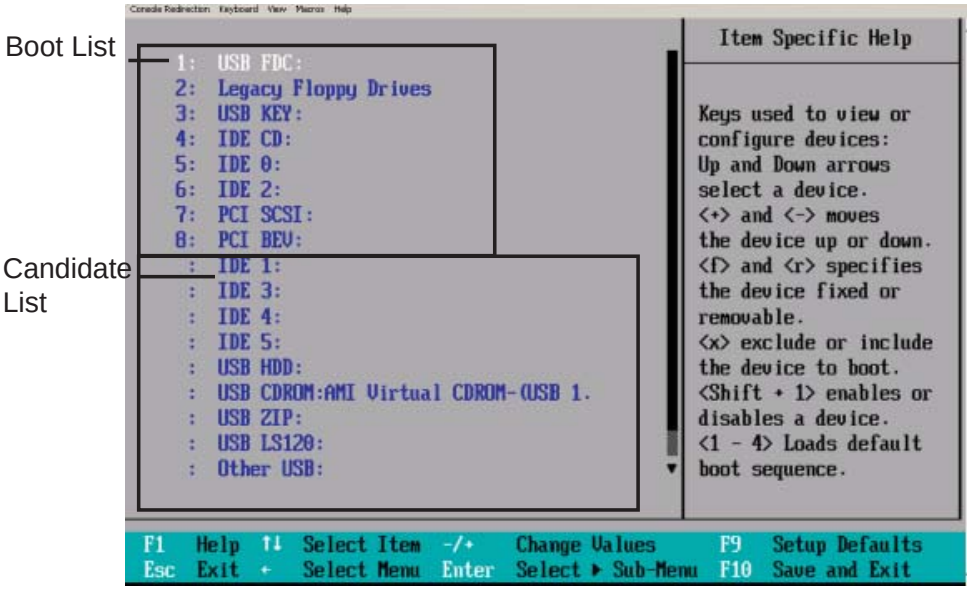
Select **Normal** to enable the feature of Write-Protect to protect the boot sector on the hard drives from virus intrusion.

Password on Boot

When set to Enabled, a user will need to key-in a password to enter the system at system boot. The options are Enabled (password required) and Disabled (password not required).

7-6 Boot Settings

Choose Boot from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. See details on how to change the order and specs of boot devices in the Item Specific Help window. All Boot BIOS settings are described in this section.

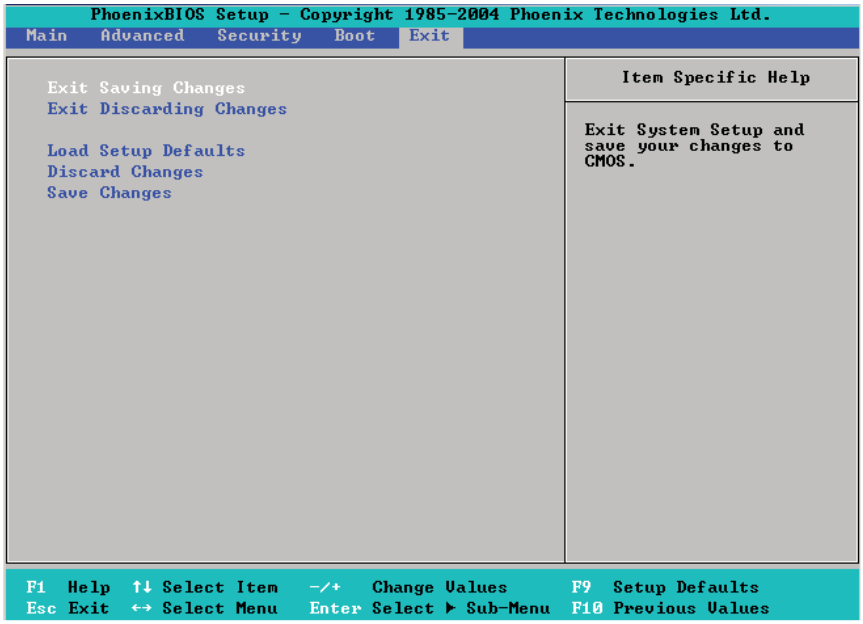


Boot Priority Order/Excluded from Boot Orders

The devices included in the boot list section (above) are bootable devices listed in the sequence of boot order as specified. The boot functions for the devices included in the candidate list (above) are currently disabled. Use a <+> key or a <-> key to move the device up or down. Use the <f> key or the <r> key to specify the type of an USB device, either fixed or removable. You can select one item from the boot list and hit the <x> key to remove it from the list of bootable devices (to make its resource available for other bootable devices). Subsequently, you can select an item from the candidate list and hit the <x> key to remove it from the candidate list and put it in the boot list. This item will then become a bootable device. See details on how to change the priority of boot order of devices in the "Item Specific Help" window.

7-7 Exit

Choose Exit from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. All Exit BIOS settings are described in this section.



Exit Saving Changes

Highlight this item and hit <Enter> to save any changes you made and to exit the BIOS Setup utility.

Exit Discarding Changes

Highlight this item and hit <Enter> to exit the BIOS Setup utility without saving any changes you may have made.

Load Setup Defaults

Highlight this item and hit <Enter> to load the default settings for all items in the BIOS Setup. These are the safest settings to use.

Discard Changes

Highlight this item and hit <Enter> to discard (cancel) any changes you made. You will remain in the Setup utility.

Save Changes

Highlight this item and hit <Enter> to save any changes you made. You will remain in the Setup utility.

Appendix A

BIOS POST Messages

During the Power-On Self-Test (POST), the BIOS will check for problems. If a problem is found, the BIOS will activate an alarm, turn on LED indicators or display a message as listed below.

Failure Fixed Disk
Fixed disk is not working or not configured properly. Check to see if fixed disk is attached properly. Run Setup. Find out if the fixed-disk type is correctly identified.

Stuck key
Stuck key on keyboard.

Keyboard error
Keyboard not working.

Keyboard Controller Failed
Keyboard controller failed test. May require replacing keyboard controller.

Keyboard locked - Unlock key switch
Unlock the system to proceed.

Monitor type does not match CMOS - Run SETUP
Monitor type not correctly identified in Setup

Shadow Ram Failed at offset: nnnn
Shadow RAM failed at offset **nnnn** of the 64k block at which the error was detected.

System RAM Failed at offset: nnnn
System RAM failed at offset **nnnn** of in the 64k block at which the error was detected.

Extended RAM Failed at offset: nnnn
Extended memory not working or not configured properly at offset **nnnn.**

System battery is dead - Replace and run SETUP
The CMOS clock battery indicator shows the battery is dead. Replace the battery and run Setup to reconfigure the system.

System CMOS checksum bad - Default configuration used
System CMOS has been corrupted or modified incorrectly, perhaps by an application program that changes data stored in CMOS. The BIOS installed Default Setup Values. If you do not want these values, enter Setup and enter your own values. If the error persists, check the system battery or contact your dealer.

System timer error
The timer test failed. Requires repair of system board.

Real time clock error
Real-Time Clock fails BIOS hardware test. May require board repair.

Check date and time settings
BIOS found date or time out of range and reset the Real-Time Clock. May require setting legal date (1991-2099).

Previous boot incomplete - Default configuration used
Previous POST did not complete successfully. POST loads default values and offers to run Setup. If the failure was caused by incorrect values and they are not corrected, the next boot will likely fail. On systems with control of **wait states**, improper Setup settings can also terminate POST and cause this error on the next boot. Run Setup and verify that the waitstate configuration is correct. This error is cleared the next time the system is booted.

Memory Size found by POST differed from CMOS
Memory size found by POST differed from CMOS.

Diskette drive A error
Diskette drive B error
Drive A: or B: is present but fails the BIOS POST diskette tests. Check to see that the drive is defined with the proper diskette type in Setup and that the diskette drive is attached correctly.

Incorrect Drive A type - run SETUP
Type of floppy drive A: not correctly identified in Setup.

Incorrect Drive B type - run SETUP
Type of floppy drive B: not correctly identified in Setup.

System cache error - Cache disabled
RAM cache failed and BIOS disabled the cache. On older boards, check the cache jumpers. You may have to replace the cache. See your dealer. A disabled cache slows system performance considerably.

CPU ID:
CPU socket number for Multi-Processor error.

EISA CMOS not writeable
ServerBIOS2 test error: Cannot write to EISA CMOS.

DMA Test Failed
ServerBIOS2 test error: Cannot write to extended **DMA** (Direct Memory Access) registers.

Software NMI Failed
ServerBIOS2 test error: Cannot generate software NMI (Non-Maskable Interrupt).

Fail-Safe Timer NMI Failed
ServerBIOS2 test error: Fail-Safe Timer takes too long.

Device Address Conflict
Address conflict for specified **device.**

Allocation Error for: device
Run ISA or EISA Configuration Utility to resolve resource conflict for the specified **device**.

CD ROM Drive
CD ROM Drive identified.

Entering SETUP ...
Starting Setup program

Failing Bits: nnnn
The hex number **nnnn** is a map of the bits at the RAM address which failed the memory test. Each 1 (one) in the map indicates a failed bit. See errors 230, 231, or 232 above for offset address of the failure in System, Extended, or Shadow memory.

Fixed Disk n
Fixed disk **n** (0-3) identified.

Invalid System Configuration Data
Problem with NVRAM (CMOS) data.

I/O device IRQ conflict
I/O device IRQ conflict error.

PS/2 Mouse Boot Summary Screen:
PS/2 Mouse installed.

nnnn kB Extended RAM Passed
Where **nnnn** is the amount of RAM in kilobytes successfully tested.

nnnn Cache SRAM Passed
Where **nnnn** is the amount of system cache in kilobytes successfully tested.

nnnn kB Shadow RAM Passed
Where **nnnn** is the amount of shadow RAM in kilobytes successfully tested.

nnnn kB System RAM Passed
Where **nnnn** is the amount of system RAM in kilobytes successfully tested.

One or more I2O Block Storage Devices were excluded from the Setup Boot Menu
There was not enough room in the IPL table to display all installed I2O block-storage devices.

Operating system not found
Operating system cannot be located on either drive A: or drive C:. Enter Setup and see if fixed disk and drive A: are properly identified.

Parity Check 1 nnnn
Parity error found in the system bus. BIOS attempts to locate the address and display it on the screen. If it cannot locate the address, it displays **????.** Parity is a method for checking errors in binary data. A parity error indicates that some data has been corrupted.

Parity Check 2 nnnn

Parity error found in the I/O bus. BIOS attempts to locate the address and display it on the screen. If it cannot locate the address, it displays **????.**

Press <F1> to resume, <F2> to Setup, <F3> for previous

Displayed after any recoverable error message. Press <F1> to start the boot process or <F2> to enter Setup and change the settings. Press <F3> to display the previous screen (usually an initialization error of an **Option ROM**, i.e., an add-on card). Write down and follow the information shown on the screen.

Press <F2> to enter Setup

Optional message displayed during POST. Can be turned off in Setup.

PS/2 Mouse:

PS/2 mouse identified.

Run the I2O Configuration Utility

One or more unclaimed block storage devices have the Configuration Request bit set in the LCT. Run an I2O Configuration Utility (e.g. the SAC utility).

System BIOS shadowed

System BIOS copied to shadow RAM.

UMB upper limit segment address: *nnnn*

Displays the address ***nnnn*** of the upper limit of **Upper Memory Blocks**, indicating released segments of the BIOS which can be reclaimed by a virtual memory manager.

Video BIOS shadowed

Video BIOS successfully copied to shadow RAM.

Notes

Appendix B

BIOS POST Codes

This section lists the POST (Power On Self Test) codes for the Phoenix BIOS. POST codes are divided into two categories: recoverable and terminal.

Recoverable POST Errors

When a recoverable type of error occurs during POST, the BIOS will display an POST code that describes the problem. The BIOS may also issue one of the following beep codes:

1 long and two short beeps - video configuration error
1 continuous long beep - overheat (Overheat LED will be on)
1 long beep and 1 short pause - memory not detected

Terminal POST Errors

If a terminal type of error occurs, the BIOS will shut down the system. Before doing so, BIOS will write the error to port 80h, attempt to initialize video and write the error in the top left corner of the screen.

The following is a list of codes that may be written to port 80h.

POST Code	Description
02h	Verify Real Mode
03h	Disable Non-Maskable Interrupt (NMI)
04h	Get CPU type
06h	Initialize system hardware
07h	Disable shadow and execute code from the ROM.
08h	Initialize chipset with initial POST values
09h	Set IN POST flag
0Ah	Initialize CPU registers
0Bh	Enable CPU cache
0Ch	Initialize caches to initial POST values
0Eh	Initialize I/O component
0Fh	Initialize the local bus IDE
10h	Initialize Power Management
11h	Load alternate registers with initial POST values
12h	Restore CPU control word during warm boot
13h	Initialize PCI Bus Mastering devices
14h	Initialize keyboard controller
16h	1-2-2-3 BIOS ROM checksum
17h	Initialize cache before memory Auto size

POST Code	Description
18h	8254 timer initialization
1Ah	8237 DMA controller initialization
1Ch	Reset Programmable Interrupt Controller
20h	1-3-1-1 Test DRAM refresh
22h	1-3-1-3 Test 8742 Keyboard Controller
24h	Set ES segment register to 4 GB
28h	Auto size DRAM
29h	Initialize POST Memory Manager
2Ah	Clear 512 kB base RAM
2Ch	1-3-4-1 RAM failure on address line ***xxxx****
2Eh	1-3-4-3 RAM failure on data bits ***xxxx**** of low byte of memory bus
2Fh	Enable cache before system BIOS shadow
32h	Test CPU bus-clock frequency
33h	Initialize Phoenix Dispatch Manager
36h	Warm start shut down
38h	Shadow system BIOS ROM
3Ah	Auto size cache
3Ch	Advanced configuration of chipset registers
3Dh	Load alternate registers with CMOS values
41h	Initialize extended memory for RomPilot
42h	Initialize interrupt vectors
45h	POST device initialization
46h	2-1-2-3 Check ROM copyright notice
47h	Initialize I20 support
48h	Check video configuration against CMOS
49h	Initialize PCI bus and devices
4Ah	Initialize all video adapters in system
4Bh	QuietBoot start (optional)
4Ch	Shadow video BIOS ROM
4Eh	Display BIOS copyright notice
4Fh	Initialize MultiBoot
50h	Display CPU type and speed
51h	Initialize EISA board
52h	Test keyboard
54h	Set key click if enabled
55h	Enable USB devices
58h	2-2-3-1 Test for unexpected interrupts
59h	Initialize POST display service
5Ah	Display prompt “Press F2 to enter SETUP”
5Bh	Disable CPU cache

POST Code	Description
5Ch	Test RAM between 512 and 640 kB
60h	Test extended memory
62h	Test extended memory address lines
64h	Jump to UserPatch1
66h	Configure advanced cache registers
67h	Initialize Multi Processor APIC
68h	Enable external and CPU caches
69h	Setup System Management Mode (SMM) area
6Ah	Display external L2 cache size
6Bh	Load custom defaults (optional)
6Ch	Display shadow-area message
6Eh	Display possible high address for UMB recovery
70h	Display error messages
72h	Check for configuration errors
76h	Check for keyboard errors
7Ch	Set up hardware interrupt vectors
7Dh	Initialize Intelligent System Monitoring
7Eh	Initialize coprocessor if present
80h	Disable onboard Super I/O ports and IRQs
81h	Late POST device initialization
82h	Detect and install external RS232 ports
83h	Configure non-MCD IDE controllers
84h	Detect and install external parallel ports
85h	Initialize PC-compatible PnP ISA devices
86h	Re-initialize onboard I/O ports.
87h	Configure Motherboard Configurable Devices (optional)
88h	Initialize BIOS Data Area
89h	Enable Non-Maskable Interrupts (NMIs)
8Ah	Initialize Extended BIOS Data Area
8Bh	Test and initialize PS/2 mouse
8Ch	Initialize floppy controller
8Fh	Determine number of ATA drives (optional)
90h	Initialize hard-disk controllers
91h	Initialize local-bus hard-disk controllers
92h	Jump to UserPatch2
93h	Build MPTABLE for multi-processor boards
95h	Install CD ROM for boot
96h	Clear huge ES segment register
97h	Fix up Multi Processor table
98h	1-2 Search for option ROMs. One long, two short beeps on checksum failure

POST Code	Description
99h	Check for SMART Drive (optional)
9Ah	Shadow option ROMs
9Ch	Set up Power Management
9Dh	Initialize security engine (optional)
9Eh	Enable hardware interrupts
9Fh	Determine number of ATA and SCSI drives
A0h	Set time of day
A2h	Check key lock
A4h	Initialize typematic rate
A8h	Erase F2 prompt
AAh	Scan for F2 key stroke
ACh	Enter SETUP
AEh	Clear Boot flag
B0h	Check for errors
B1h	Inform RomPilot about the end of POST.
B2h	POST done - prepare to boot operating system
B4h	1 One short beep before boot
B5h	Terminate QuietBoot (optional)
B6h	Check password (optional)
B7h	Initialize ACPI BIOS
B9h	Prepare Boot
BAh	Initialize SMBIOS
BBh	Initialize PnP Option ROMs
BCh	Clear parity checkers
BDh	Display MultiBoot menu
BEh	Clear screen (optional)
BFh	Check virus and backup reminders
C0h	Try to boot with INT 19
C1h	Initialize POST Error Manager (PEM)
C2h	Initialize error logging
C3h	Initialize error display function
C4h	Initialize system error handler
C5h	PnPnd dual CMOS (optional)
C6h	Initialize Note dock (optional)
C7h	Initialize Note dock late
C8h	Force check (optional)
C9h	Extended checksum (optional)
CAh	Redirect Int 15h to enable remote keyboard
CBh	Redirect Int 13h to Memory Technologies
	Devices such as ROM, RAM, PCMCIA, and serial disk
CCh	Redirect Int 10h to enable remote serial video

POST Code	Description
CDh	Re-map I/O and memory for PCMCIA
CEh	Initialize digitizer and display message
D2h	Unknown interrupt

The following are for boot block in Flash ROM

POST Code	Description
E0h	Initialize the chipset
E1h	Initialize the bridge
E2h	Initialize the CPU
E3h	Initialize system timer
E4h	Initialize system I/O
E5h	Check force recovery boot
E6h	Checksum BIOS ROM
E7h	Go to BIOS
E8h	Set Huge Segment
E9h	Initialize Multi Processor
EAh	Initialize OEM special code
EBh	Initialize PIC and DMA
ECh	Initialize Memory type
EDh	Initialize Memory size
EEh	Shadow Boot Block
EFh	System memory test
F0h	Initialize interrupt vectors
F1h	Initialize Run Time Clock
F2h	Initialize video
F3h	Initialize System Management Manager
F4h	Output one beep
F5h	Clear Huge Segment
F6h	Boot to Mini DOS
F7h	Boot to Full DOS

If the BIOS detects errors on 2C, 2E, or 30 (base 512K RAM error), it displays an additional word-bitmap (***xxxx***) indicating the address line or bits that have failed. For example, “2C 0002” means address line 1 (bit one set) has failed. “2E 1020" means data bits 12 and 5 (bits 12 and 5 set) have failed in the lower 16 bits. The BIOS also sends the bitmap to the port-80 LED display. It first displays the checkpoint code, followed by a delay, the high-order byte, another delay, and then the loworder byte of the error. It repeats this sequence continuously.

Notes

Appendix C

System Specifications

Processors
Single Intel Xeon 3200/3000 Series LGA775 processors at system bus speeds of 1333, 1066 and 800 MHz

Note: Please refer to the motherboard specifications pages on our web site for updates on supported processors.

Chipset
Intel 3210/ICH9R chipset

BIOS
16 Mb Phoenix® Flash ROM

Memory Capacity
Four 240-pin DIMM slots that can support up to 8 GB of unbuffered ECC DDR2-800/667 SDRAM

Note: See the memory section in Chapter 5 for details.

SATA Controller
On-chip (ICH9R) 3 Gb/s Intel SATA controller

Hard Drive Bay
One (1) fixed drive bay for one (1) 3.5" hard drive

PCI Expansion Slots
One (1) PCI-E x8 card or one 64-bit 133 MHz (3.3V) PCI-X slot (with included riser card)

Motherboard
Model: X7SBi

Form Factor: ATX

Dimensions: 12 x 9.6 in (305 x 244 mm)

Chassis

Model: SC513L-260 (1U Rackmount)
Dimensions: (WxHxD) 17.2 x 1.7 x 19.85 in. (437 x 43 x 504 mm)

Weight

Gross Weight: 16.5 lbs. (7.5 kg.)

System Cooling

One (1) 10-cm 5K rpm cooling fan
One (1) air shroud
One (1) fan holder

System Input Requirements

AC Input Voltage: 100-240V AC auto-range
Rated Input Current: 4A max
Rated Input Frequency: 50 to 60 Hz

Power Supply

Rated Output Power: 260W (Model# SP262-1S, Part# PWS-0055)
Rated Output Voltages: +3.3V (15A), +5V (25A), +12V (18A), -12V (1A), +5Vsb (2A)

Operating Environment

Operating Temperature: 10º to 35º C (50º to 95º F)
Non-operating Temperature: -40º to 70º C (-40º to 158º F)
Operating Relative Humidity: 8% to 90% (non-condensing)
Non-operating Relative Humidity: 5 to 95% (non-condensing)

Regulatory Compliance

Electromagnetic Emissions: FCC Class A, EN 55022 Class A, EN 61000-3-2/-3-3, CISPR 22 Class A

Electromagnetic Immunity: EN 55024/CISPR 24, (EN 61000-4-2, EN 61000-4-3, EN 61000-4-4, EN 61000-4-5, EN 61000-4-6, EN 61000-4-8, EN 61000-4-11)

Safety: CSA/EN/IEC/UL 60950-1 Compliant, UL or CSA Listed (USA and Canada), CE Marking (Europe)

California Best Management Practices Regulations for Perchlorate Materials: This Perchlorate warning applies only to products containing CR (Manganese Dioxide) Lithium coin cells. “Perchlorate Material-special handling may apply. See www.dtsc.ca.gov/hazardouswaste/perchlorate”

Notes

(continued from front)

The products sold by Supermicro are not intended for and will not be used in life support systems, medical equipment, nuclear facilities or systems, aircraft, aircraft devices, aircraft/emergency communication devices or other critical systems whose failure to perform be reasonably expected to result in significant injury or loss of life or catastrophic property damage. Accordingly, Supermicro disclaims any and all liability, and should buyer use or sell such products for use in such ultra-hazardous applications, it does so entirely at its own risk. Furthermore, buyer agrees to fully indemnify, defend and hold Supermicro harmless for and against any and all claims, demands, actions, litigation, and proceedings of any kind arising out of or related to such ultra-hazardous use or sale.