

QSFP-100GB-129780-20-E-J-AO

Juniper Networks® Compatible TAA 100GBase-OWDM QSFP28 Transceiver O-Band Channel OW297 400GHz (SMF, 1297.80nm, 20km, LC, DOM, 5 to 80C)

Features

- Hot-pluggable QSFP28 form factor
- Supports 106.25Gb/s (PAM4)
- Compliant with QSFP28 MSA
- High Sensitivity APD Receiver
- OWDM 8 Wavelengths
- Duplex LC receptacles
- Aligned with IEEE 802.3bs and 100G Lambda MSA
- Single +3.3V power supply
- Operating temperature: -5 to +80 Celsius
- I2C management interface
- RoHS Compliant and Lead-Free



Applications

- 100GBase Ethernet
- Access and Enterprise

Product Description

This Juniper Networks® QSFP28 transceiver provides 100GBase-OWDM throughput up to 20km over single-mode fiber (SMF) using a wavelength of 1297.80nm via an LC connector. It is guaranteed to be 100% compatible with the equivalent Juniper Networks® transceiver. This easy to install, hot swappable transceiver has been programmed, uniquely serialized and data-traffic and application tested to ensure that it will initialize and perform identically. Digital optical monitoring (DOM) support is also present to allow access to real-time operating parameters. This transceiver is Trade Agreements Act (TAA) compliant. We stand behind the quality of our products and proudly offer a limited lifetime warranty.

AddOn's transceivers are RoHS compliant and lead-free.

TAA refers to the Trade Agreements Act (19 U.S.C. & 2501-2581), which is intended to foster fair and open international trade. TAA requires that the U.S. Government may acquire only "U.S. — made or designated country end products."



Regulatory Compliance

- ESD to the Electrical PINs: compatible with MIL-STD-883E Method 3015.4
- ESD to the LC Receptacle: compatible with IEC 61000-4-3
- EMI/EMC: compatible with FCC Part 15 Subpart B Rules, EN55022:2010
- Laser Eye Safety: compatible with FDA 21CFR, EN60950-1& EN (IEC) 60825-1,2
- RoHS compliant with EU RoHS 2.0 directive 2015/863/EU

Absolute Maximum Ratings

Parameter	Symbol	Min.	Max.	Unit
Maximum Supply Voltage	Vcc	0	3.6	V
Storage Temperature	TS	-40	85	°C
Operating Case Temperature	Тс	-5	80	°C
Relative Humidity (No Condensation)	RH	0	85	%
Damage Threshold	THd	0		dBm
Link Distance Through Mux	D		20	km
Link Distance Back-to-Back	D		40	km

Electrical Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Notes	
Supply Voltage	Vcc	3.135	3.3	3.465	V		
Supply Current	Icc			1.8	Α		
Power Consumption			4.7 at -5°C 4.4 at 25°C 5.2 at 80°C	5.0 at -5°C 4.8 at 25°C 5.5 at 80°C	w		
Transmitter High-Speed Electrical Char	acteristics						
Signaling Rate	Rate	25.78125 ± 100ppm			Gbps		
Input Differential Impedance	ZIN		100		Ω		
Differential Input Voltage Per Lane				900	mV		
Input Impedance Mismatch				10	%		
Input High Voltage	VIH	2		Vcc+0.3	V		
Input Low Voltage	VIL	-0.3		0.8	V		
Receiver High-Speed Electrical Characteristics							
Signaling Rate	Rate	25.78125 ± 100ppm			Gbps		
Common-Mode Voltage	Vcm	-350		2850	mV		
Common-Mode Noise (RMS)				17.5	mV	20-80%	

Differential Termination Resistance Mismatch (At 1MHz)			10	%	
Differential Return Loss (SDD22)			Per CEI-28G- VSR	dB	
Common-Mode to Differential Conversion and Differential to Common-Mode Conversion (SDC22, SCD22)			Per CEI-28G- VSR	dB	
Common-Mode Return Loss (SCC22): From 250MHz to 30GHz			-2		
Transition Time: 20-80%		9.5		ps	
Vertical Eye Closure	VEC		6.5	dB	
Eye Width at 10-15 Probability	EW15	0.57		UI	
Eye Height at 10-15 Probability	EH15	228		mV	

Optical Characteristics (EOL)

Transmitter Transmitter	Parameter		Symbol	Min.	Тур.	Max.	Unit	Notes
Modulation Format PAMM4 PAMM4 PAMM4 PAMM6 PA	Transmitter							
Mavelength	Data Rate Per Lane				53.125 ± 100ppn	n	Gbps	
Side-Mode Suppression Ratio SMSR 30 3.4 dB 4 4 4 4 4 4 4 4 4	Modulation Format				PAM4			
Average Launch Power Pavg 0 3.4 dBm 1	Wavelength		λ	1297.40	1297.80	1298.20	nm	
Outer Optical Modulation Amplitude (OMAouter) TDP>1.4dB POMA 1.6+TDP 6.4 dBm Transmitter and Dispersion Penalty TDP 1.6+TDP 6.4 dBm TECQ 3.4 dB 1.5 TECQ 3.9 dB 1.5 Extinction Ratio ER 5.0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0	Side-Mode Suppression Rat	tio	SMSR	30			dB	
Modulation Amplitude (OMAouter) TDP>1.4dB 1.6+TDP 6.4 dBM Common to the part of the part	Average Launch Power		Pavg	0		3.4	dBm	1
Tone Tone	-	TDP<1.4dB	POMA	3.0		6.4	dBm	
Transmitter and Dispersion Penalty TDP 3.4 dB TECQ TECQ 3.9 dB ITDP-TECQI (Maximum) ER 5.0 GB Extinction Ratio ER 5.0 Image: Control of the control o		TDP>1.4dB		1.6+TDP		6.4	dBm	
TDP-TECQ (Maximum)		Penalty	TDP			3.4	dB	
Extinction Ratio ER 5.0 dB Composition CB S.0 CB <	TECQ		TECQ			3.9	dB	
Optical Return Loss Tolerance ORLT 15.6 dB Transmitter Reflectance RL -26 dB 2 Average Launch Power Off Transmitter Poff -15 dBm -136 dB/Hz -136	TDP-TECQ (Maximum)					2.5	dB	
Transmitter Reflectance RL -26 dB 2 Average Launch Power Off Transmitter Poff -15 dBm -15 dBm <td< th=""><th>Extinction Ratio</th><th></th><th>ER</th><th>5.0</th><th></th><th></th><th>dB</th><th></th></td<>	Extinction Ratio		ER	5.0			dB	
Average Launch Power Off Transmitter Poff -15 dBm RIN15.6 OMA RIN -136 dB/Hz Receiver Data Rate Per Lane 53.125 ± 100ppm Gbps Modulation Format PAM	Optical Return Loss Toleran	ice	ORLT			15.6	dB	
RIN15.6 OMA RIN -136 dB/Hz Receiver Data Rate Per Lane 53.125 ± 100ppm Gbps Modulation Format PAM Image: Free Lane PAM Image	Transmitter Reflectance		RL			-26	dB	2
Receiver Data Rate Per Lane 53.125 ± 100ppm Gbps Modulation Format PAM Image: PAM	Average Launch Power Off Transmitter		Poff			-15	dBm	
Data Rate Per Lane 53.125 ± 100ppm Gbps Modulation Format PAM FAM Lane Wavelength λ 1295.04~1311.96 nm Damage Threshold THd 0 dBm 3 Average Receive Power -15.7 -3 dBm 4 Receive Power (OMAouter) RL -2.6 dBm	RIN15.6 OMA		RIN			-136	dB/Hz	
Modulation Format PAM Lane Wavelength λ 1295.04~1311.96 nm Damage Threshold THd 0 dBm 3 Average Receive Power -15.7 -3 dBm 4 Receive Power (OMAouter) RL -2.6 dBm - Receiver Reflectance RL -26 dB - Receiver Sensitivity (OMAouter) SRS -11.6 dBm 5, 6 Stressed Receiver Sensitivity (OMAouter) SRS -11.6 dBm - Transmitter Reflectance RL -26 dB - LOS Assert LOSA -30 -19.5 dBm LOS De-Assert LOSD -16.5 dBm LOS Hysteresis LOSH 0.5 dB	Receiver							
Lane Wavelength λ 1295.04~1311.96 nm Amage Threshold THd 0 dBm 3 Average Receive Power -15.7 -3 dBm 4 Receive Power (OMAouter) -2.6 dBm -2.6 Receiver Reflectance RL -26 dB Receiver Sensitivity (OMAouter) SRS -11.6 dBm 5, 6 Stressed Receiver Sensitivity (OMAouter) SRS -11.6 dBm -11.6 <t< th=""><th>Data Rate Per Lane</th><th></th><th></th><th></th><th>53.125 ± 100ppn</th><th>n</th><th>Gbps</th><th></th></t<>	Data Rate Per Lane				53.125 ± 100ppn	n	Gbps	
Damage Threshold THd 0 dBm 3 Average Receive Power -15.7 -3 dBm 4 Receive Power (OMAouter) RL -2.6 dBm -26 dB -26 dBm 5, 6 Receiver Sensitivity (OMAouter) SRS -11.6 dBm -5, 6 -6 -6 -6 dBm -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6 -6	Modulation Format							
Average Receive Power -15.7 -3 dBm 4 Receive Power (OMAouter) -2.6 dBm -26 dB Receiver Reflectance RL -26 dB -5,6 Stressed Receiver Sensitivity (OMAouter) SRS -11.6 dBm -5,6 Stressed Receiver Sensitivity (OMAouter) SRS -11.6 dBm	Lane Wavelength		λ	1295.04~1311.96		6	nm	
Receiver Power (OMAouter) Receiver Reflectance RL Receiver Sensitivity (OMAouter) Stressed Receiver Sensitivity (OMAouter) Per Lane - Maximum Transmitter Reflectance RL LOSA -30 -19.5 dBm LOS De-Assert LOSD -16.5 dBm LOSH 0.5	Damage Threshold		THd	0			dBm	3
Receiver Reflectance RL -26 dB Receiver Sensitivity (OMAouter) SRS -11.6 dBm Per Lane - Maximum RL Transmitter Reflectance RL LOS Assert LOSA -30 -19.5 dBm LOS De-Assert LOSD -16.5 dBm LOS Hysteresis LOSH 0.5 dB	Average Receive Power			-15.7		-3	dBm	4
Receiver Sensitivity (OMAouter) Stressed Receiver Sensitivity (OMAouter) Per Lane - Maximum Transmitter Reflectance RL LOS Assert LOS De-Assert LOSD LOS Hysteresis LOSH LOSH SRS -11.6 dBm -26 dB -26 dBm -19.5 dBm -16.5 dBm	Receive Power (OMAouter)					-2.6	dBm	
Stressed Receiver Sensitivity (OMAouter) SRS -11.6 dBm Per Lane - Maximum RL -26 dB LOS Assert LOSA -30 -19.5 dBm LOS De-Assert LOSD -16.5 dBm LOS Hysteresis LOSH 0.5 dB			RL			-26	dB	
Per Lane - Maximum RL -26 dB LOS Assert LOSA -30 -19.5 dBm LOS De-Assert LOSD -16.5 dBm LOS Hysteresis LOSH 0.5 dB		•					dBm	5, 6
Transmitter Reflectance RL -26 dB LOS Assert LOSA -30 -19.5 dBm LOS De-Assert LOSD -16.5 dBm LOS Hysteresis LOSH 0.5 dB			SRS			-11.6	dBm	
LOS De-Assert LOSD -16.5 dBm LOS Hysteresis LOSH 0.5 dB			RL			-26	dB	
LOS Hysteresis LOSH 0.5 dB	LOS Assert		LOSA	-30		-19.5	dBm	
	LOS De-Assert		LOSD			-16.5	dBm	
	LOS Hysteresis		LOSH	0.5			dB	
Conditions of Stress Receiver Sensitivity Test	Conditions of Stress Receive							
Stressed Eye Closure for PAM4 (SECQ) Lane Under Test 3.4 dB						3.4	dB	
SECQ – 10*log10 (Ceq) Lane Under Test 3.4 dB						3.4	dB	

Notes:

- 1. Average launch power (minimum) is informative and not the principal indicator of signal strength. A transmitter with launch power below this value cannot be compliant; however, a value above this does not ensure compliance.
- 2. Transmitter Reflectance is defined looking into the transmitter.
- 3. The receiver shall be able to tolerate, without damage, continuous exposure to a modulated optical input signal having this power level on one lane.
- 4. Average receive power (minimum) is informative and not the principal indicator of signal strength. A received power below this value cannot be compliant; however, a value above this does not ensure compliance.
- 5. Receiver Sensitivity (OMAouter) (maximum) is informative and is defined for a transmitter with a value of SECQ up to 3.4dB for 100G ER1 O-Band WDM.
- 6. Measured with a conformance test signal at TP3 (see 3.11) for the BER specified in IEEE Std 802.3.

Pin Descriptions

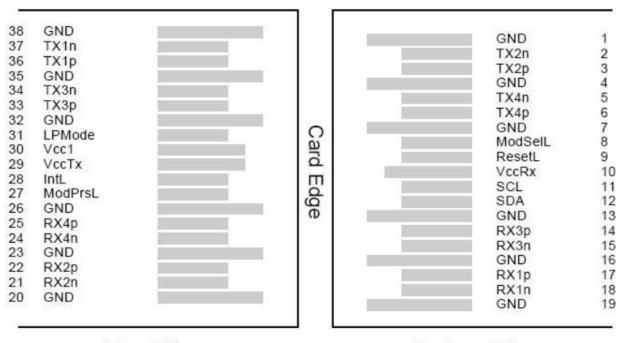
Pin	Symbol	Name/Descriptions	Ref.
1	GND	Transmitter Ground (Common with Receiver Ground).	1
2	Tx2-	Transmitter Inverted Data Input.	
3	Tx2+	Transmitter Non-Inverted Data Output.	
4	GND	Transmitter Ground (Common with Receiver Ground).	1
5	Tx4-	Transmitter Inverted Data Input.	
6	Tx4+	Transmitter Non-Inverted Data Output.	
7	GND	Transmitter Ground (Common with Receiver Ground).	1
8	ModSelL	Module Select.	2
9	ResetL	Module Reset.	2
10	VccRx	+3.3V Power Supply Receiver.	
11	SCL	2-Wire Serial Interface Clock.	2
12	SDA	2-Wire Serial Interface Data.	2
13	GND	Transmitter Ground (Common with Receiver Ground).	1
14	Rx3+	Receiver Non-Inverted Data Output.	
15	Rx3-	Receiver Inverted Data Output.	
16	GND	Transmitter Ground (Common with Receiver Ground).	1
17	Rx1+	Receiver Non-Inverted Data Output.	
18	Rx1-	Receiver Inverted Data Output.	
19	GND	Transmitter Ground (Common with Receiver Ground).	1
20	GND	Transmitter Ground (Common with Receiver Ground).	1
21	Rx2-	Receiver Inverted Data Output.	
22	Rx2+	Receiver Non-Inverted Data Output.	
23	GND	Transmitter Ground (Common with Receiver Ground).	1
24	Rx4-	Receiver Inverted Data Output.	1
25	Rx4+	Receiver Non-Inverted Data Output.	
26	GND	Transmitter Ground (Common with Receiver Ground).	1
27	ModPrsl	Module Present.	
28	IntL	Interrupt.	2
29	VccTx	+3.3V Power Supply Transmitter.	
30	Vcc1	+3.3V Power Supply.	
31	LPMode	Low-Power Mode.	2
32	GND	Transmitter Ground (Common with Receiver Ground).	1
33	Tx3+	Transmitter Non-Inverted Data Input.	
34	Tx3-	Transmitter Inverted Data Output.	

35	GND	Transmitter Ground (Common with Receiver Ground).	1
36	Tx1+	Transmitter Non-Inverted Data Input.	
37	Tx1-	Transmitter Inverted Data Output.	
38	GND	Transmitter Ground (Common with Receiver Ground).	1

Notes:

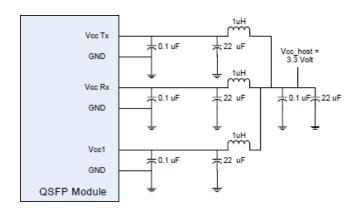
- 1. The module signal grounds are isolated from the module case.
- 2. This is an open collector/drain output that, on the host board, requires a $4.7K\Omega$ to $10K\Omega$ pull-up resistor to Host_Vcc.

Electrical Pin-Out Details

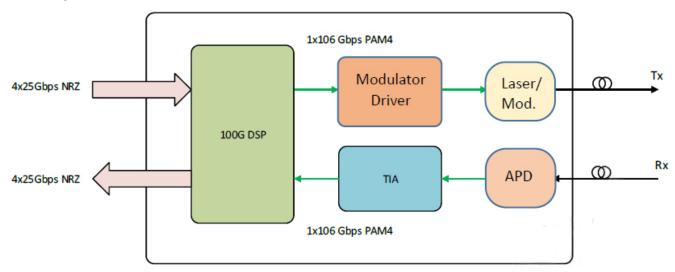


Top Side Bottom Side

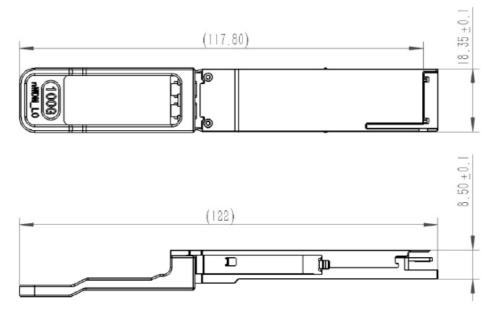
Recommended Host Board Power Supply Filter Network

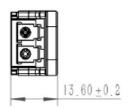


Block Diagram



Mechanical Specifications





About AddOn Networks

In 1999, AddOn Networks entered the market with a single product. Our founders fulfilled a severe shortage for compatible, cost-effective optical transceivers that compete at the same performance levels as leading OEM manufacturers. Adhering to the idea of redefining service and product quality not previously had in the fiber optic networking industry, AddOn invested resources in solution design, production, fulfillment, and global support.

Combining one of the most extensive and stringent testing processes in the industry, an exceptional free tech support center, and a consistent roll-out of innovative technologies, AddOn has continually set industry standards of quality and reliability throughout its history.

Reliability is the cornerstone of any optical fiber network and is in engrained in AddOn's DNA. It has played a key role in nurturing the long-term relationships developed over the years with customers. AddOn remains committed to exceeding industry standards with certifications from ranging from NEBS Level 3 to ISO 9001:2005 with every new development while maintaining the signature reliability of its products.

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